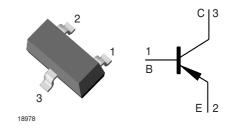


## **Vishay Semiconductors**

# **Small Signal Transistors (PNP)**

#### **Features**

- PNP Silicon Epitaxial Planar Transistors
- Suited for low level, low noise, low frequency applications in hybrid cicuits.
- · Low Current, Low Voltage.
- As complementary types, BCW60 Series NPN transistors are recommended.



### **Mechanical Data**

Case: SOT-23 Plastic case Weight: approx. 8.8 mg

Marking:

BCW61A = BA BCW61B = BB BCW61C = BC BCW61D = BD

#### **Packaging Codes/Options:**

GS18 / 10 k per 13" reel (8 mm tape), 10 k/box GS08 / 3 k per 7" reel (8 mm tape), 15 k/box

### Pinning:

1 = Base, 2 = Emitter, 3 = Collector

## **Absolute Maximum Ratings**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit
Collector-emitter voltage		- V <sub>CEO</sub>	32	V
Collector-base voltage		- V <sub>CBO</sub>	32	V
Emitter-base voltage		- V <sub>EBO</sub>	5.0	V
Collector current (DC)		- I <sub>C</sub>	100	mA
Peak collector current		- I <sub>CM</sub>	200	mA
Base current (DC)		- I <sub>B</sub>	50	mA
Power dissipation	T <sub>A</sub> = 25 °C	P <sub>tot</sub>	250	mW

## **Maximum Thermal Resistance**

Parameter	Test condition	Symbol	Value	Unit
Maximum junction temperature		T <sub>j</sub>	150	°C
Storage temperature range		T <sub>S</sub>	- 65 to + 150	°C
Thermal resistance junction to ambient air		$R_{ hetaJA}$	500 <sup>1)</sup>	°C/W

<sup>1)</sup> Mounted an FR-4 printed-circuit board.

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# **BCW61 Series**

# Vishay Semiconductors



# **Electrical DC Characteristics**

Parameter	Test condition	Part	Symbol	Min	Тур	Max	Unit
DC current gain	$- V_{CE} = 5 V, - I_{C} = 10 \mu A$	BCW61A	h <sub>FE</sub>				
		BCW61B	h <sub>FE</sub>	30			
		BCW61C	h <sub>FE</sub>	40			
		BCW61D	h <sub>FE</sub>	100			
	- V <sub>CE</sub> = 5 V, - I <sub>C</sub> = 2 mA	BCW61A	h <sub>FE</sub>	120		220	
		BCW61B	h <sub>FE</sub>	180		310	
		BCW61C	h <sub>FE</sub>	250		460	
		BCW61D	h <sub>FE</sub>	380		630	
	- V <sub>CE</sub> = 1 V, - I <sub>C</sub> = 50 mA	BCW61B	h <sub>FE</sub>	60			
		BCW61B	h <sub>FE</sub>	80			
		BCW61C	h <sub>FE</sub>	100			
		BCW61D	h <sub>FE</sub>	110			
Collector - emitter saturation voltage	- I <sub>C</sub> = 10 mA, - I <sub>B</sub> = 0.25 mA		V <sub>CEsat</sub>	60		250	mV
	- I <sub>C</sub> = 50 mA, - I <sub>B</sub> = 1.25 mA		V <sub>CEsat</sub>	120		550	mV
Base - emitter saturation voltage	- I <sub>C</sub> = 10 mA, - I <sub>B</sub> = 0.25 mA		V <sub>BEsat</sub>	600		850	mV
	- I <sub>C</sub> = 50 mA, - I <sub>B</sub> = 1.25 mA		V <sub>BEsat</sub>	680		1050	mV
Base - emitter voltage	- V <sub>CE</sub> = 5 V, - I <sub>C</sub> = 2 mA		$V_{BE}$	600	650	750	mV
	$- V_{CE} = 5 V$ , $- I_{C} = 10 \mu A$		$V_{BE}$		520		mV
	- V <sub>CE</sub> = 1 V, - I <sub>C</sub> = 50 mA		$V_{BE}$		780		mV
Collector-emitter cut-off current	- V <sub>CE</sub> = 32 V, V <sub>EB</sub> = 0 V		I <sub>CES</sub>			20	nA
	- V <sub>CE</sub> = 32 V, V <sub>EB</sub> = 0 V, T <sub>A</sub> = 150 °C		I <sub>CES</sub>			20	μΑ
Emitter-base cut-off current	- V <sub>EB</sub> = 4 V, I <sub>C</sub> = 0		I <sub>EBO</sub>			20	nA

# **Electrical AC Characteristics**

Parameter	Test condition	Part	Symbol	Min	Тур	Max	Unit
Gain - bandwidth product	- V <sub>CE</sub> = 5 V, - I <sub>C</sub> = 10 mA, f = 100 MHz		f <sub>T</sub>	100			MHz
Collector - base capacitance	- V <sub>CB</sub> = 10 V, f = 1 MHz, I <sub>E</sub> = 0		C <sub>CBO</sub>		4.5		pF
Emitter - base capacitance	$- V_{EB} = 0.5 \text{ V, f} = 1 \text{ MHz, I}_{C} = 0$		C <sub>EBO</sub>		11		pF
Noise figure	- $V_{CE}$ = 5 V, - $I_{C}$ = 200 μA, $R_{S}$ = 2 kΩ, f = 1 kHz, B = 200 Hz		F		2	6	dB
Small signal current gain	- V <sub>CE</sub> = 5 V, - I <sub>C</sub> = 2 mA, f = 1.0 kHz	BCW61A	h <sub>fe</sub>		200		
		BCW61B	h <sub>fe</sub>		260		
		BCW61C	h <sub>fe</sub>		330		
		BCW61D	h <sub>fe</sub>		520		
Turn - on time	$R_L = 990 \Omega$ (see fig.1) - $V_{CC} = 10 V$ , - $I_C = 10 mA$ , - $I_{B(on)} = I_{B(off)} = 1 mA$		t <sub>on</sub>		85	150	ns
Turn - off time	$R_L = 990 \Omega$ (see fig.1) - $V_{CC} = 10 V$ , - $I_C = 10 mA$ , - $I_{B(on)} = I_{B(off)} = 1 mA$		t <sub>off</sub>		480	800	ns



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# **Vishay Semiconductors**

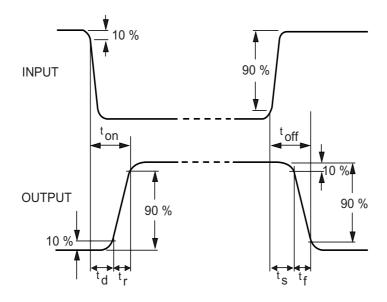
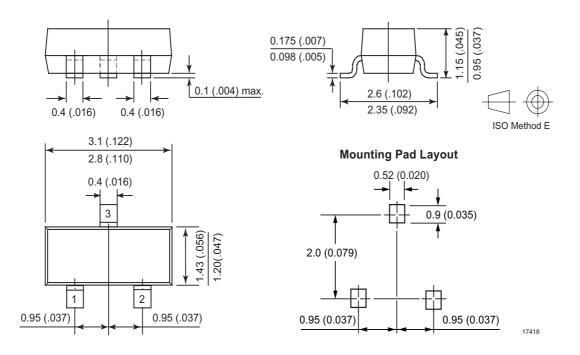


Figure 1. Switching Waveform

# Package Dimensions in mm (Inches)



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# **BCW61 Series**

### **Vishay Semiconductors**



## **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operatingsystems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

### We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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