

Vishay Semiconductors

Ultra Fast Sinterglass Diode

Features

- High temperature metallurgically bonded construction
- Glass passivated cavity-free junction
- Superfast recovery time for high efficiency
- Low forward voltage, high current capability
- Hermetically sealed package
- High surge capability

Mechanical Data

Case: Sintered glass case, DO-204AP

Terminals: Plated axial leads, solderable per MIL-STD-750, Method 2026 **Polarity:** Color band denotes cathode end



Mounting Position: Any Weight: 560 mg

Parts Table

Part	Type differentiation	Package
GI1001	V _{RRM} = 50 V	DO-204AP (G1)
GI1002	V _{RRM} = 100 V	DO-204AP (G1)
GI1003	V _{RRM} = 150 V	DO-204AP (G1)
GI1004	V _{RRM} = 200 V	DO-204AP (G1)

Absolute Maximum Ratings

 T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Part	Symbol	Value	Unit
Reverse voltage = Repetitive peak reverse	see electrical characteristics	GI1001	V _R =	50	V
voltage			V _{RRM}		
	see electrical characteristics	GI1002	V _R =	100	V
			V _{RRM}		
	see electrical characteristics	GI1003	V _R =	150	V
			V _{RRM}		
	see electrical characteristics	GI1004	V _R =	200	V
			V _{RRM}		
Maximum average forward rectified current	0.375 " (9.5 mm) lead length at T _L = 75 °C		I _{F(AV)}	1.0	А
Peak forward surge current	8.3 ms single half sine-wave superimposed on rated load (JEDEC Method), at $T_L = 75 \ ^\circ C$		I _{FSM}	30	A
Operating junction and storage temperature			T _J ,	- 55 to +	°C
range			T _{STG}	175	

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Maximum Thermal Resistance

T_{amb} = 25 °C, unless otherwise specified

Parameter		Symbol	Value	Unit
Typical thermal resistance ^{1), 2)} - junction to ambient		$R_{ hetaJA}$	65	K/W
Typical thermal resistance ^{1), 2)} - junction to lead		$R_{ ext{ heta}JL}$	20	K/W

Electrical Characteristics

T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Тур.	Max	Unit
Maximum instantaneous forward voltage ¹⁾	I _F = 1.0 A	V _F		0.975	V
Maximum reverse current	V _{R/> = VRRM} , Tamb = 25 °C	I _R		2.0	μA
	V _{R/> = VRRM} , Tamb = 100 °C	I _R		50	μA
Maximum reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$	t _{rr}		25	ns
Typical junction capacitance	V _R = 4 V, f = 1 MHz	CJ	45		pF

¹⁾ Pulse test: 300 μ s pulse width, 1 % duty cycle

Typical Characteristics ($T_{amb} = 25 \text{ °C}$ unless otherwise specified)

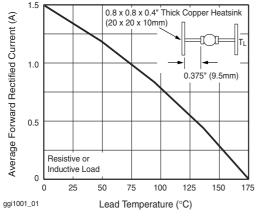


Figure 1. Forward Current Derating Curve

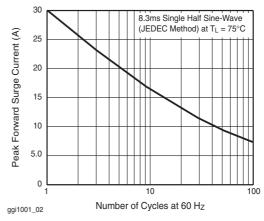


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current



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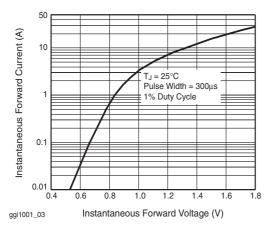


Figure 3. Typical Instantaneous Forward Characteristics

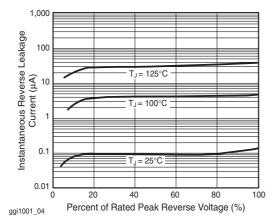
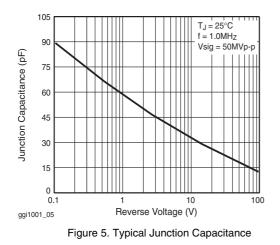


Figure 4. Typical Reverse Leakage Characteristics

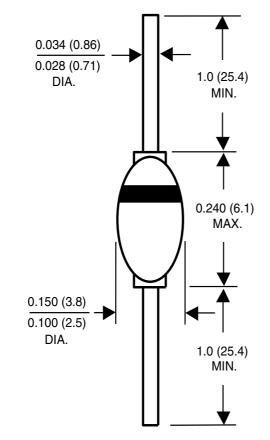


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17030

Package Dimensions in Inches (mm)





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Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operatingsystems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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