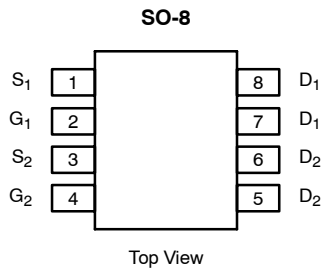




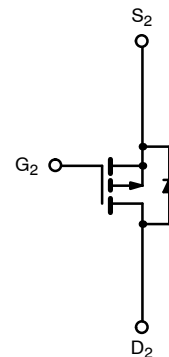
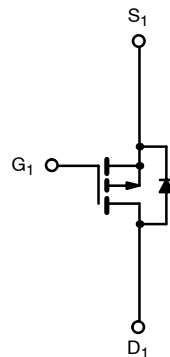
## Dual P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY		
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
-30	0.080 @ $V_{GS} = -10$ V	-3.9
	0.135 @ $V_{GS} = -4.5$ V	-3.0

**TrenchFET<sup>®</sup>**  
Power MOSFETS



Ordering Information: Si4947ADY  
Si4947ADY-T1 (with Tape and Reel)



ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)					
Parameter	Symbol	10 secs	Steady State	Unit	
Drain-Source Voltage	$V_{DS}$	-30		V	
Gate-Source Voltage	$V_{GS}$	$\pm 20$			
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) <sup>a</sup>	$I_D$	$T_A = 25^\circ\text{C}$	-3.9	-3.0	A
		$T_A = 70^\circ\text{C}$	-3.1	-2.4	
Pulsed Drain Current	$I_{DM}$	-20			
continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	-1.7	-1.0		
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_A = 25^\circ\text{C}$	2.0	1.2	W
		$T_A = 70^\circ\text{C}$	1.3	0.76	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150		$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	$R_{thJA}$	$t \leq 10$ sec	54	62.5	$^\circ\text{C/W}$
		Steady State	87	105	
Maximum Junction-to-Foot	$R_{thJF}$	34	45		

Notes

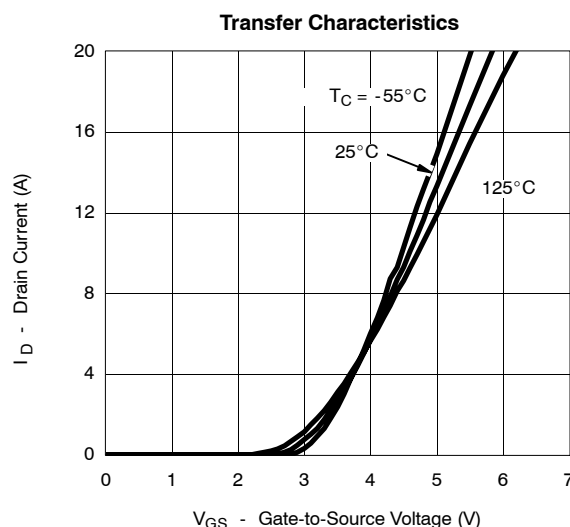
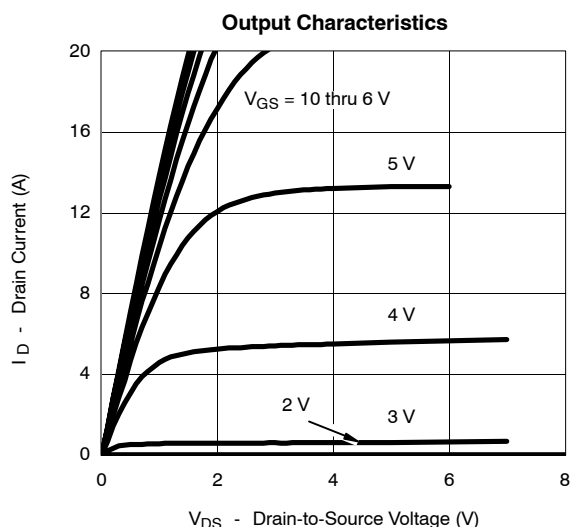
a. Surface Mounted on 1" x 1" FR4 Board.

SPECIFICATIONS (T <sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 μA	-1.0			V
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V			-1	μA
		V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C			-10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = -5 V, V <sub>GS</sub> = -10 V	-15			A
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = -10 V, I <sub>D</sub> = -3.9 A		0.062	0.080	Ω
		V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -3.0 A		0.105	0.135	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = -15 V, I <sub>D</sub> = -2.5 A		5.0		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = -1.7 A, V <sub>GS</sub> = 0 V		-0.82	-1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = -10 V, V <sub>GS</sub> = -5 V, I <sub>D</sub> = -3.9 A		5.8	8	nC
Gate-Source Charge	Q <sub>gs</sub>			2		
Gate-Drain Charge	Q <sub>gd</sub>			1.9		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = -10 V, R <sub>L</sub> = 10 Ω I <sub>D</sub> ≅ -1 A, V <sub>GEN</sub> = -10 V, R <sub>G</sub> = 6 Ω		8	15	ns
Rise Time	t <sub>r</sub>			9	18	
Turn-Off Delay Time	t <sub>d(off)</sub>			21	40	
Fall Time	t <sub>f</sub>			10	20	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>		I <sub>F</sub> = -1.7 A, di/dt = 100 A/μs		27	

**Notes**

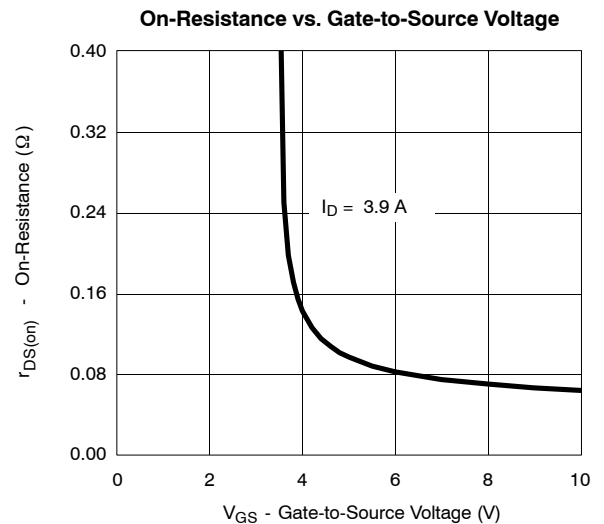
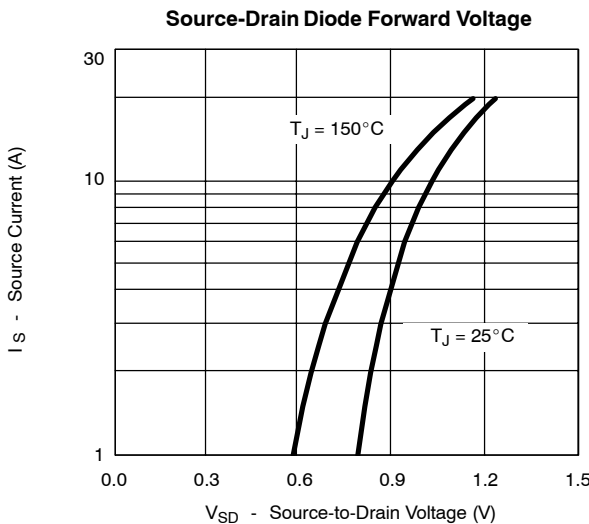
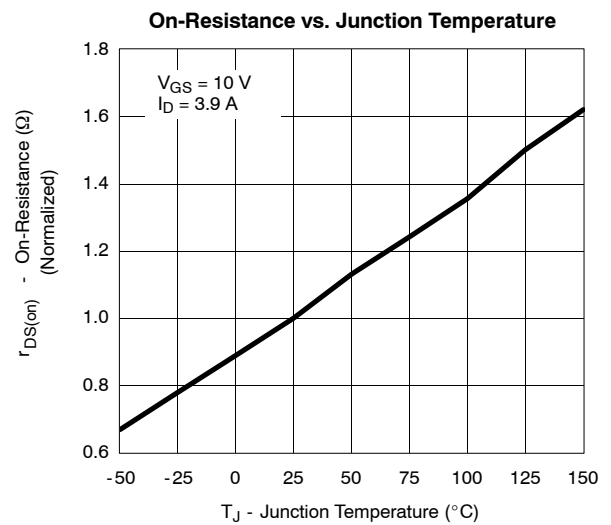
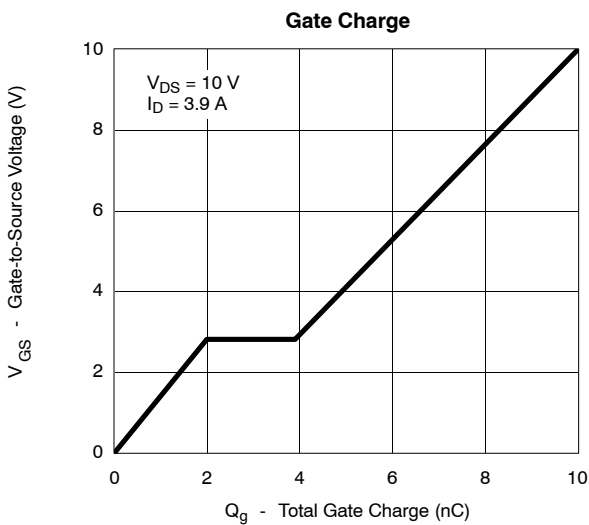
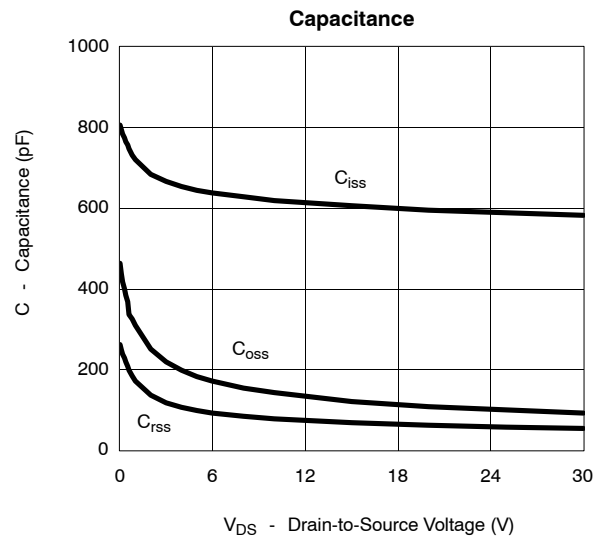
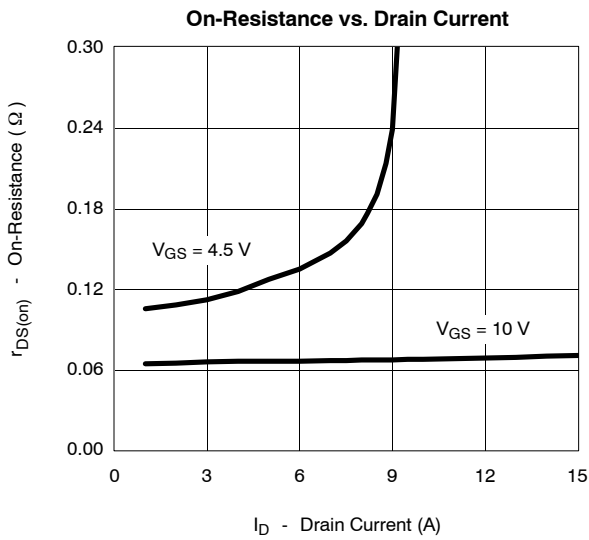
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

### TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)





**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**



**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**

