

## Monolithisch Integrierte Schaltung Monolithic Integrated Circuit

Anwendung: NF-Leistungsverstärker  
Application: Audio power amplifier

### Besondere Merkmale:

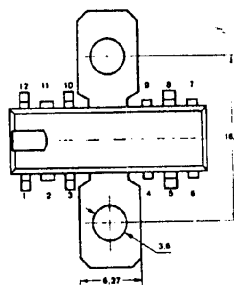
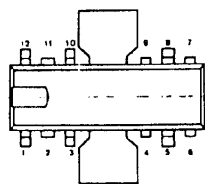
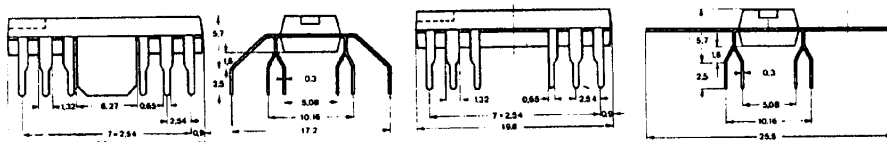
- Thermische Abschaltung
- Hoher Ausgangsstrom bis 2,5 A
- Großer Versorgungsspannungsbereich, 4 bis 20 V
- Hohe Ausgangsleistung, 7 W
- Kleine Übernahmeverzerrungen
- Kleiner Klirrfaktor
- Sehr guter Wirkungsgrad, 70%

### Features:

- Thermal shut-down
- High output current, up to 2.5 A
- Wide range of supply voltage, 4 to 20 V
- High output power 7 W
- Low harmonic distortion
- Low cross over distortion
- Very high efficiency 70%

### Vorläufige technische Daten · Preliminary specifications

Abmessungen in mm  
Dimensions in mm



Spezialgehäuse  
Kunststoff  
Special case  
Plastic  
Gewicht · Weight  
max. 1,5 g

TBA 810 S

TBA 810 AS

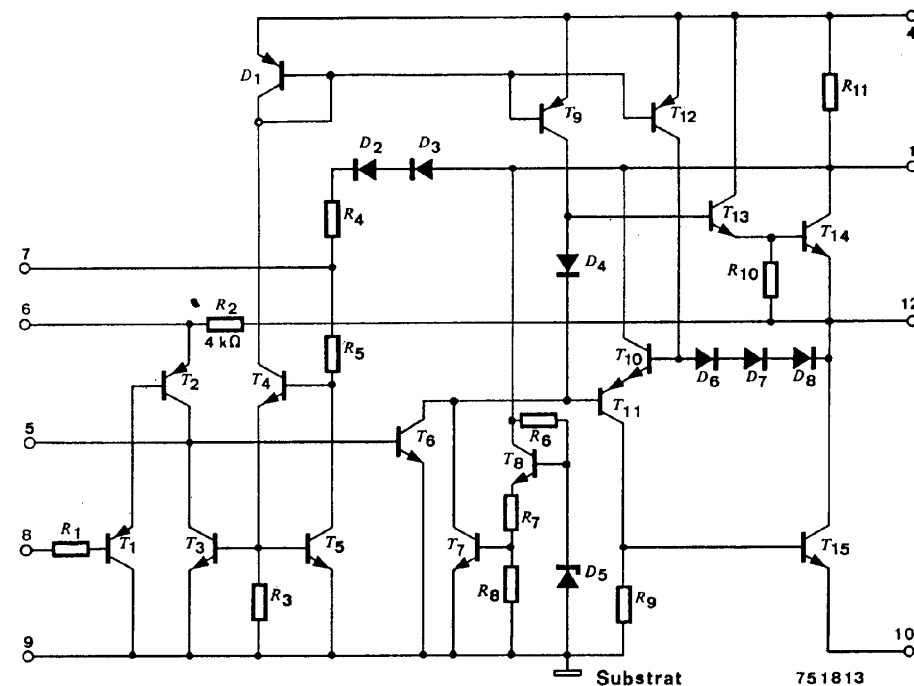


Fig. 1 Schaltung und Anschlußbelegung  
Diagram and pin connections

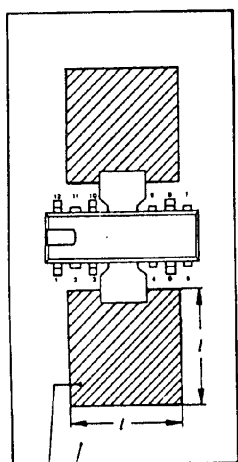
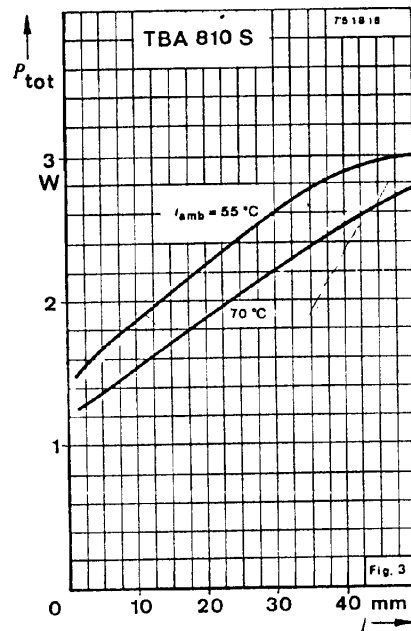
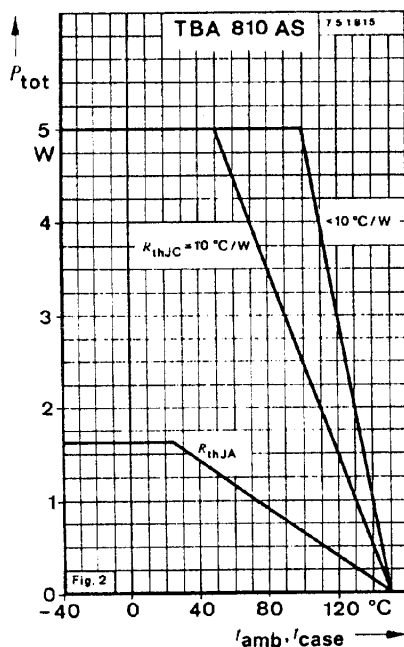
### Absolute Grenzwerte

#### Absolute maximum ratings

Bezugspunkt Pin 9, Pin 10  
Reference point

|                                                             |                    |           |              |                  |
|-------------------------------------------------------------|--------------------|-----------|--------------|------------------|
| Versorgungsspannung<br>Supply voltage                       | Pin 1              | $U_S$     | 20           | V                |
| Ausgangsstoßstrom<br>Surge output current                   | Pin 12             | $I_{QS}$  | 3,5          | A                |
| Ausgangsspitzenstrom<br>Peak output current<br>(repetitive) | Pin 12             | $I_{QM}$  | 2,5          | A                |
| Verlustleistung<br>Power dissipation                        | Fig. 2, 3, 4, 5, 6 |           |              |                  |
| $t_{amb} = 80^\circ\text{C}$                                | TBA 810 S          | $P_{tot}$ | 1            | W                |
| $t_{case} = 100^\circ\text{C}$                              | TBA 810 AS         | $P_{tot}$ | 5            | W                |
| Sperrschichttemperatur<br>Junction temperature              |                    | $t_j$     | +150         | $^\circ\text{C}$ |
| Lagerungstemperaturbereich<br>Storage temperature range     |                    | $t_{stg}$ | -40 ... +150 | $^\circ\text{C}$ |

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Kupferfläche 35 µm dick  
Copper area 35 µm thickness

Fig. 4

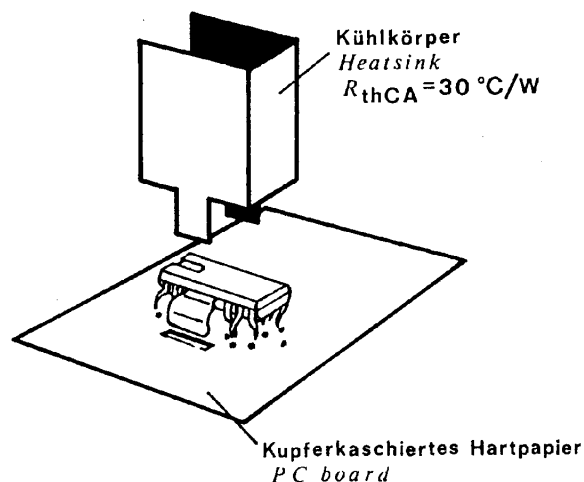


Fig. 5

# TBA 810 S · TBA 810 AS

## Wärmewiderstände Thermal resistances

|                                                    |                         | Min.                               | Typ. | Max.               |
|----------------------------------------------------|-------------------------|------------------------------------|------|--------------------|
| Sperrschicht-Umgebung<br>Junction ambient          | TBA 810 S<br>TBA 810 AS | $R_{thJA}$<br>$R_{thJA}$           |      | 70 °C/W<br>80 °C/W |
| Sperrschicht-Gehäuse Fig. 3, 4, 5<br>Junction case | TBA 810 S<br>TBA 810 AS | $R_{thJC}^{1)}$<br>$R_{thJC}^{1)}$ |      | 12 °C/W<br>10 °C/W |

## Elektrische Kenngrößen Electrical characteristics

$t_{amb} = 25 °C$ ,  $R_f = 56 \Omega$ , Bezugspunkt: Pin 9, Pin 10 falls nicht anders angegeben  
Reference point: unless otherwise specified

|                                                                                                                                  |                      |                   |                        |       |     |     |    |
|----------------------------------------------------------------------------------------------------------------------------------|----------------------|-------------------|------------------------|-------|-----|-----|----|
| Versorgungsspannung<br>Supply voltage                                                                                            |                      | Pin 1             | $U_S$                  | 4     | 20  | V   |    |
| Mittenspannung<br>Quiescent output voltage<br>$U_S = 14,4 \text{ V}$                                                             | Fig. 13              | Pin 12            | $U_{QB}$               | 6,4   | 7,2 | 8   | V  |
| Ruhestrom der Gesamtschaltung<br>Quiescent drain current<br>$U_S = 14,4 \text{ V}$                                               | Fig. 12              | Pin 1             | $I_{SB}$               |       | 12  | 20  | mA |
| Gesamtstromaufnahme<br>Total supply current<br>$P_q = 6 \text{ W}$ , $U_S = 14,4 \text{ V}$ ,<br>$R_L = 4 \Omega$                |                      | Pin 1             | $I_{S \text{ tot}}$    |       | 600 |     | mA |
| Thermische Abschalttemperatur<br>Thermal shut-down temperature<br>$P_{\text{tot}} = 2,8 \text{ W}$                               | Fig. 11              |                   | $t_{\text{case}}$      |       | 120 |     | °C |
| Brummunterdrückung<br>Supply voltage rejection ratio<br>$U_S = 14,4 \text{ V}$ , $R_L = 4 \Omega$ ,<br>$f_{Br} = 100 \text{ Hz}$ | Fig. 14, 15          |                   | $k_{SVR}$              |       | 48  |     | dB |
| Eingangsstrom<br>Input current<br>$U_S = 14,4 \text{ V}$                                                                         |                      | Pin 8             | $I_{IB}$               |       | 0,4 | 4   | µA |
| Ausgangsleistung<br>Output power<br>$R_L = 4 \Omega$ , $f = 1 \text{ kHz}$ , $k = 10\%$                                          | Fig. 6, 8, 9, 10, 11 |                   |                        |       |     |     |    |
|                                                                                                                                  |                      |                   | $U_S = 16,0 \text{ V}$ | $P_q$ | 7   |     | W  |
|                                                                                                                                  |                      |                   | $U_S = 14,4 \text{ V}$ | $P_q$ | 4,6 | 6   | W  |
|                                                                                                                                  |                      |                   | $U_S = 9,0 \text{ V}$  | $P_q$ |     | 2,5 | W  |
|                                                                                                                                  |                      |                   | $U_S = 6,0 \text{ V}$  | $P_q$ |     | 1   | W  |
| Eingangsspannung<br>Input voltage                                                                                                |                      | Pin 8             | $U_i$                  |       |     | 220 | mV |
| Eingangsspannung<br>Input voltage<br>$U_S = 14,4 \text{ V}$ , $P_q = 6 \text{ W}$ ,<br>$f = 1 \text{ kHz}$ , $R_L = 4 \Omega$    | Fig. 18              | Pin 8             |                        |       |     |     |    |
|                                                                                                                                  |                      | $R_f = 56 \Omega$ | $U_i$                  |       | 80  |     | mV |
|                                                                                                                                  |                      | $R_f = 22 \Omega$ | $U_i$                  |       | 35  |     | mV |

<sup>1)</sup> mit Kühlfläche  $R_{thCA} = 10 °C/W$   
with cooling plate

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|                                                                                                       | Pin 8          | $R_i$    | Min.   | Typ.  | Max. |               |
|-------------------------------------------------------------------------------------------------------|----------------|----------|--------|-------|------|---------------|
| Eingangswiderstand<br>Input resistance                                                                |                |          |        | 5     |      | M $\Omega$    |
| Bandbreite (-3 dB)<br>Band width                                                                      | Fig. 16        |          |        |       |      |               |
| $U_S = 14,4 \text{ V}$ , $R_L = 4 \Omega$ , $C_3 = 820 \text{ pF}$                                    |                | $B$      | 40 ... | 20000 |      | Hz            |
| $C_3 = 1500 \text{ pF}$                                                                               |                | $B$      | 40 ... | 10000 |      | Hz            |
| Klirrfaktor<br>Distortion                                                                             | Fig. 6, 19, 20 |          |        |       |      |               |
| $U_S = 14,4 \text{ V}$ , $R_L = 4 \Omega$ ,<br>$f = 1 \text{ kHz}$ , $P_q = 50 \text{ mW}$ bis/to 3 W |                | $k$      |        | 0,3   |      | %             |
| Spannungsverstärkungen<br>Voltage amplifications                                                      |                |          |        |       |      |               |
| $U_S = 14,4 \text{ V}$ , $R_L = 4 \Omega$ , $f = 1 \text{ kHz}$                                       |                |          |        |       |      |               |
| Leerlauf<br>Open loop                                                                                 |                | $A_{ug}$ |        | 80    |      | dB            |
| mit Gegenkopplung<br>closed loop                                                                      | Fig. 17        | $A_{uf}$ | 34     | 37    | 40   | dB            |
| Eingangsrauschspannung<br>Input noise voltage                                                         |                |          |        |       |      |               |
| $U_S = 14,4 \text{ V}$ , $B = 20 \dots 20000 \text{ Hz}$                                              | Pin 8          | $U_{ni}$ |        | 2     |      | $\mu\text{V}$ |
| Eingangsrauschstrom<br>Input noise current                                                            |                |          |        |       |      |               |
| $U_S = 14,4 \text{ V}$ , $B = 20 \dots 20000 \text{ Hz}$                                              | Pin 8          | $I_{ni}$ |        | 0,1   |      | nA            |
| Wirkungsgrad<br>Efficiency                                                                            | Fig. 6, 9      |          |        |       |      |               |
| $P_q = 5 \text{ W}$ , $U_S = 14,4 \text{ V}$ ,<br>$R_L = 4 \Omega$ , $f = 1 \text{ kHz}$              |                | $\eta$   |        | 70    |      | %             |

75 1814

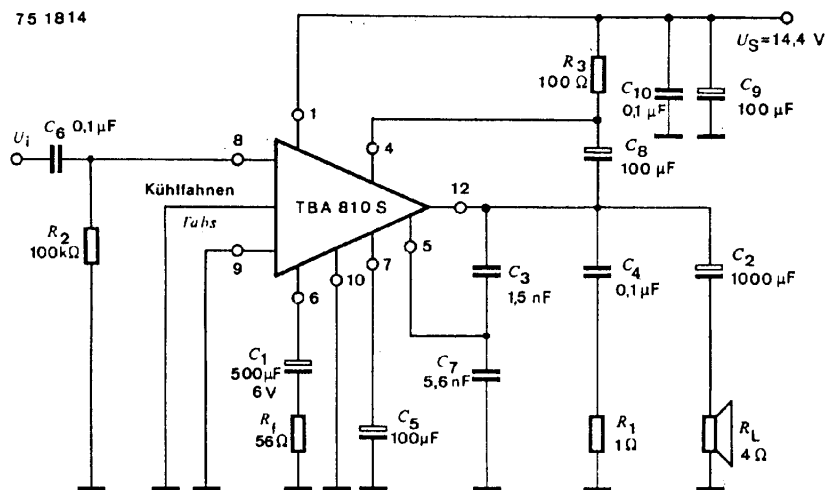
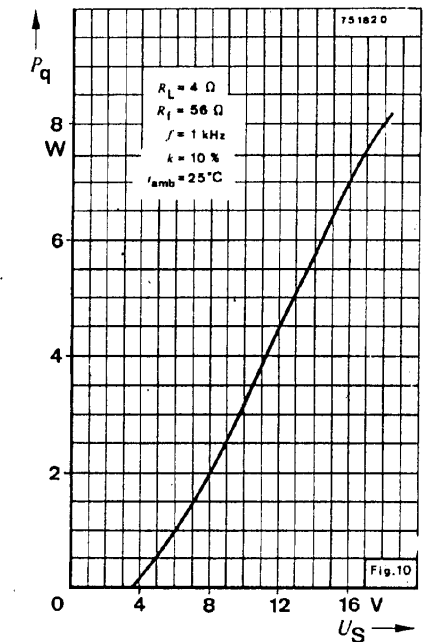
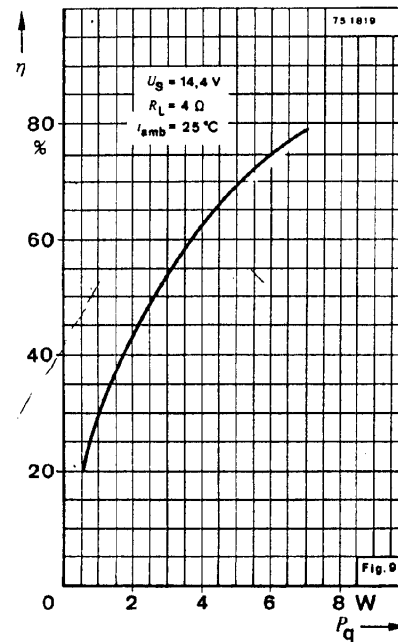
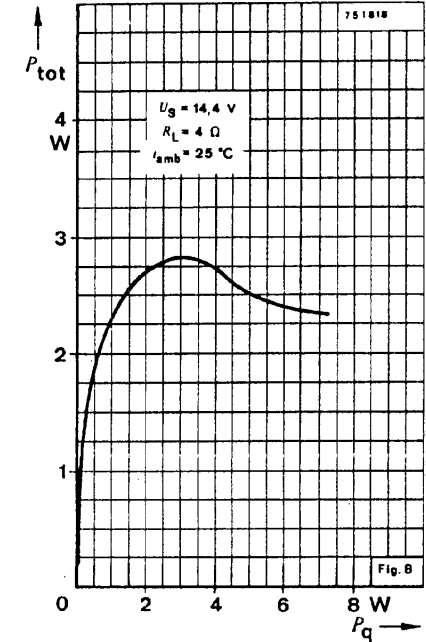
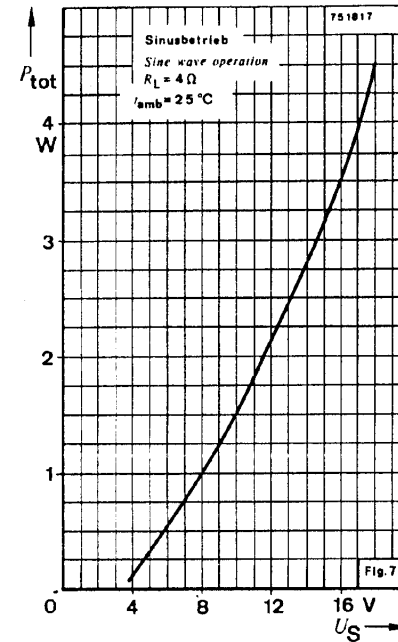
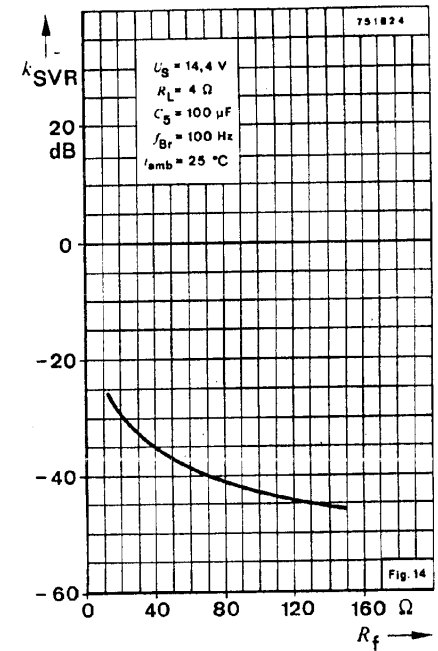
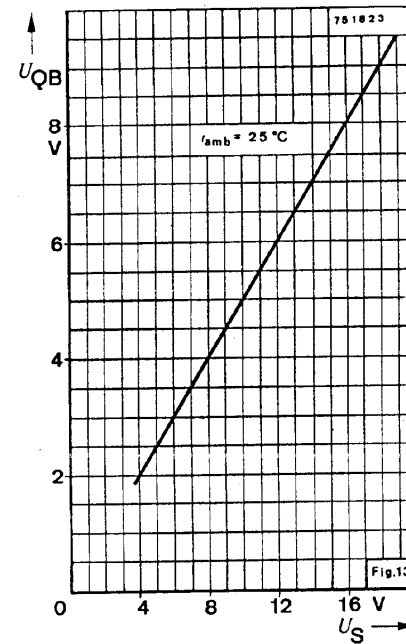
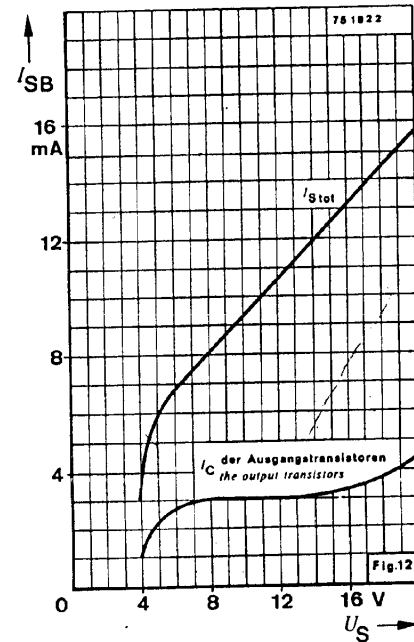
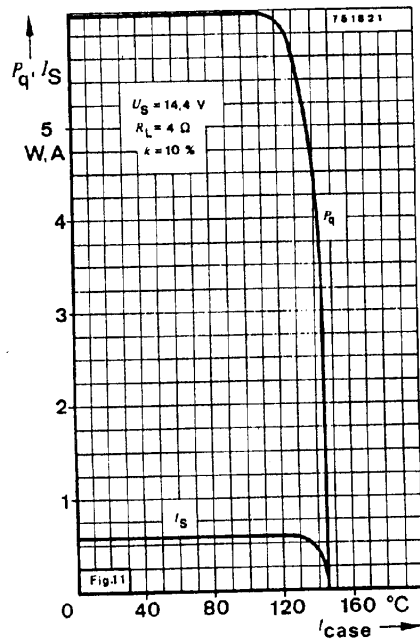


Fig. 6 Meßschaltung für:  $P_q$ ,  $P_{tot}$ ,  $k$ ,  $\eta$  und Anwendungsbeispiel  
Test circuit for: and application note

# TBA 810 S · TBA 810 AS





## Thermisches Abschalten:

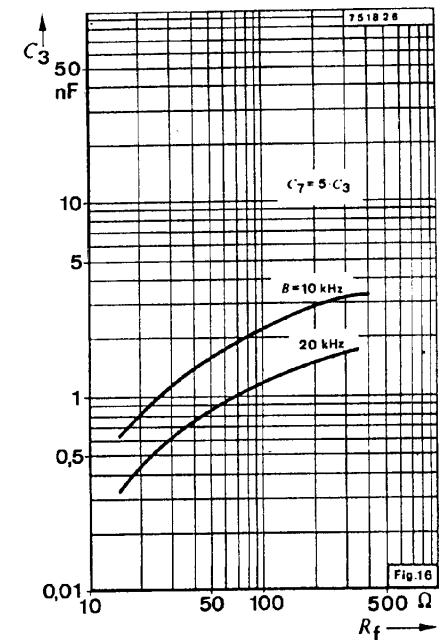
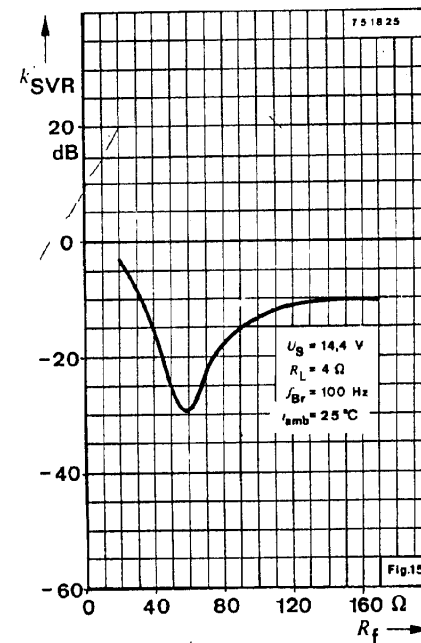
Die eingebaute thermische Begrenzerschaltung ergibt folgende Vorteile:

1. Eine dauernde Überlastung des Ausganges oder eine Erhöhung der zulässigen oberen Umgebungstemperatur kann einfach verhindert werden.
2. Die Kühlung kann gegenüber konventionellen Schaltungen mit einem kleineren Sicherheitsfaktor ausgelegt werden. Bei einer thermischen Überlastung wird der Schaltkreis nicht infolge einer zu hohen Sperrschichttemperatur zerstört, denn es wird ausschließlich  $P_Q$  (und somit  $P_{tot}$ ) und  $I_S$  reduziert (siehe Fig. 11).

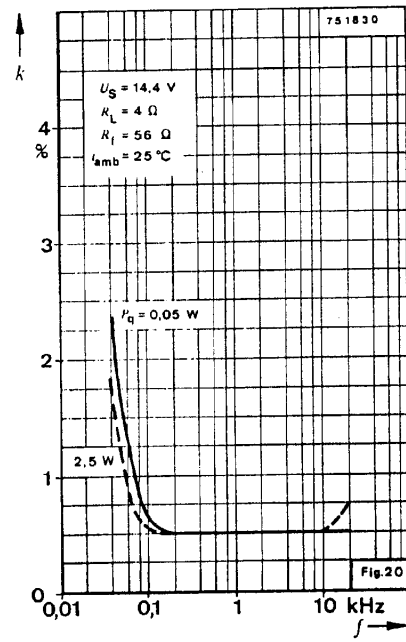
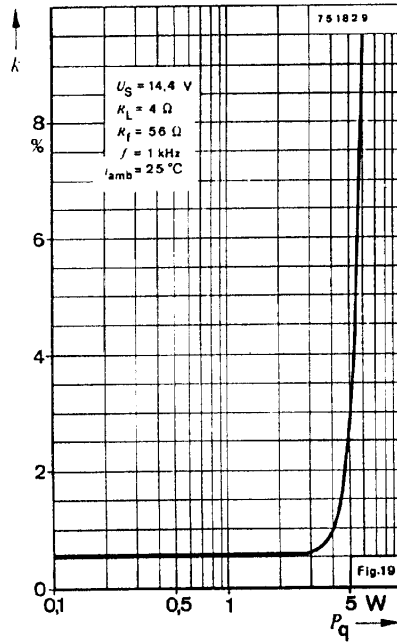
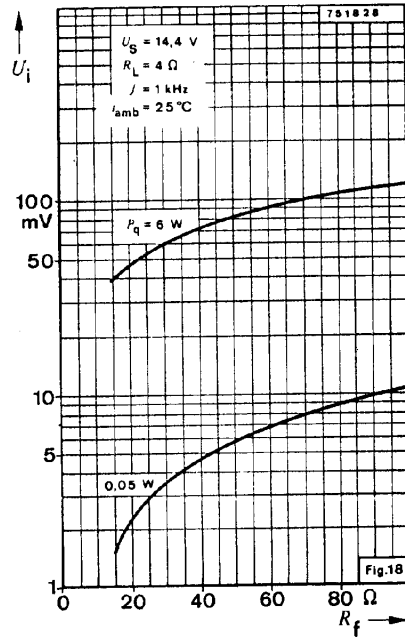
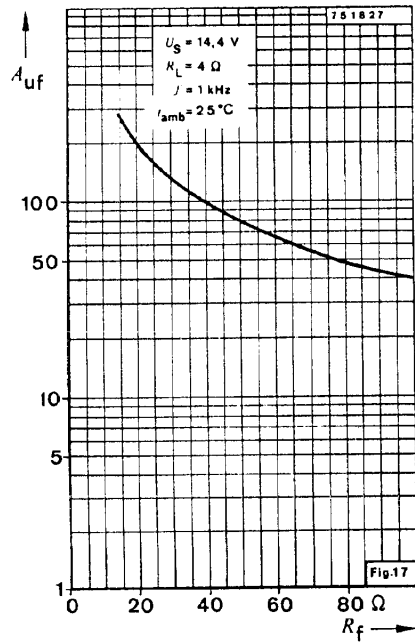
## Thermal shut-down

The presence of a thermal limiting circuit offers the following advantages:

1. An overload on the output (even if it is permanent), or an above-limit ambient temperature can be easily supported.
2. The heat sink can have a smaller factor of safety compared with that of a conventional circuit. There is no device damage in the case of too high a junction temperature: all that happens is that  $P_Q$  (and therefor  $P_{tot}$ ) and  $I_S$  are reduced (fig. 11).



# TBA 810 S · TBA 810 AS



# TBA 810 S · TBA 810 AS

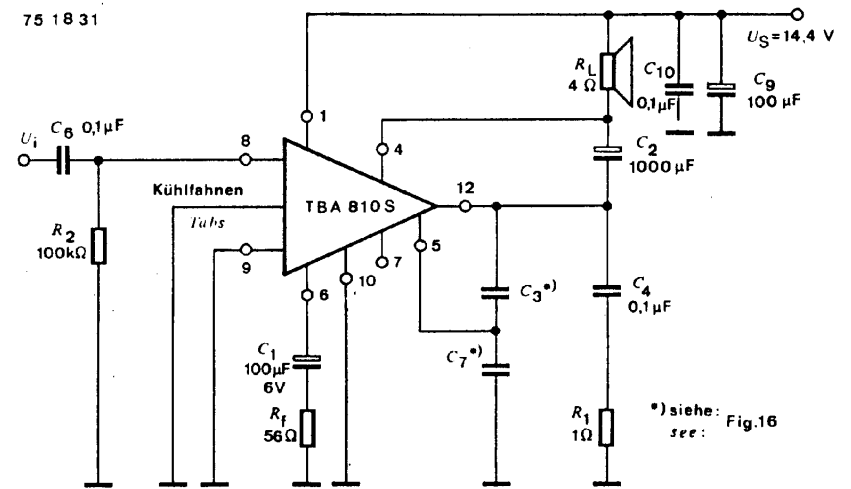


Fig. 21 Schaltungsbeispiel mit hochliegendem  $R_L$   
Circuit example with load connected to supply voltage