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# **2SJ531**

## Silicon P Channel MOS FET High Speed Power Switching

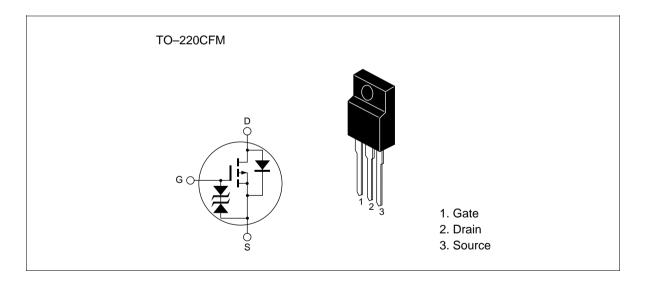


ADE-208-646A (Z) 2nd. Edition Jul. 1998

## **Features**

- Low on-resistance  $R_{DS(on)} = 0.050\Omega \text{ typ.}$
- Low drive current.
- 4V gate drive devices.
- High speed switching.

## Outline



## 2SJ531

## **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V <sub>DSS</sub>	-60	V	
Gate to source voltage	$V_{\sf GSS}$	±20	V	
Drain current	I <sub>D</sub>	-18	Α	
Drain peak current	Note1 D(pulse)	<b>-72</b>	Α	
Body-drain diode reverse drain current	I <sub>DR</sub>	<b>–18</b>	Α	
Avalanche current	I <sub>AP</sub> Note3	-18	Α	
Avalanche energy	E <sub>AR</sub> Note3	27	mJ	
Channel dissipation	Pch Note2	30	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

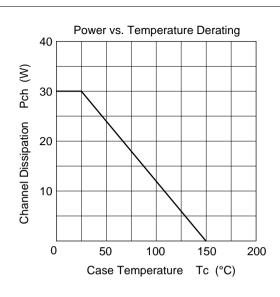
- Note: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1 %
  - 2. Value at Tc = 25°C
  - 3. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$

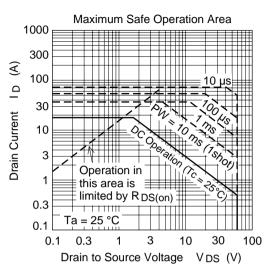
## **Electrical Characteristics** ( $Ta = 25^{\circ}C$ )

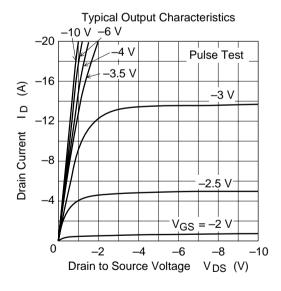
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-60	_	_	V	$I_{D} = -10 \text{mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_{G} = \pm 100 \mu A, \ V_{DS} = 0$
Zero gate voltege drain current	I <sub>DSS</sub>	_	_	-10	μΑ	$V_{DS} = -60 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.0	_	-2.0	V	$I_{D} = -1 \text{mA}, V_{DS} = -10 \text{V}$
Static drain to source on state	R <sub>DS(on)</sub>	_	0.050	0.065	Ω	$I_{\rm D} = -9A, \ V_{\rm GS} = -10V^{\rm Note4}$
resistance	R <sub>DS(on)</sub>	_	0.070	0.110	Ω	$I_{\rm D} = -9A, \ V_{\rm GS} = -4V^{\rm Note4}$
Forward transfer admittance	y <sub>fs</sub>	10	16	_	S	$I_{D} = -9A, V_{DS} = -10V^{Note4}$
Input capacitance	Ciss	_	1300	_	pF	V <sub>DS</sub> = -10V
Output capacitance	Coss	_	650	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	180	_	pF	f = 1MHz
Turn-on delay time	t <sub>d(on)</sub>	_	14	_	ns	$V_{GS} = -10V, I_{D} = -9A$
Rise time	t <sub>r</sub>	_	95	_	ns	$R_L = 3.33\Omega$
Turn-off delay time	t <sub>d(off)</sub>	_	190	_	ns	_
Fall time	t <sub>f</sub>	_	135	_	ns	_
Body-drain diode forward voltage	$V_{DF}$	_	-1.0	_	V	$I_F = -18A, V_{GS} = 0$
Body–drain diode reverse recovery time	t <sub>rr</sub>	_	70	_	ns	$I_F = -18A, V_{GS} = 0$ diF/ dt =50A/ $\mu$ s

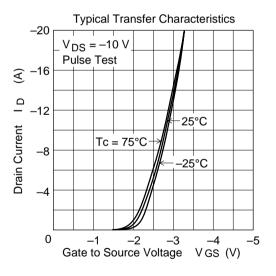
Note: 4. Pulse test

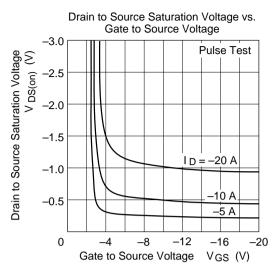
## **Main Characteristics**

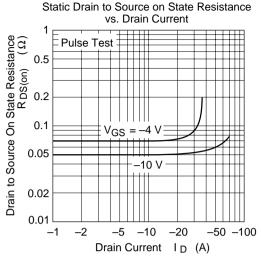


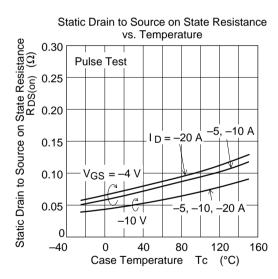


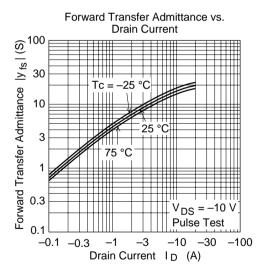


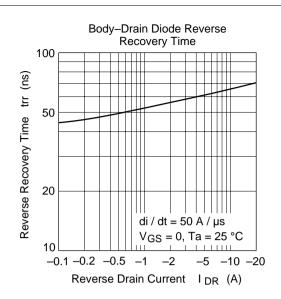


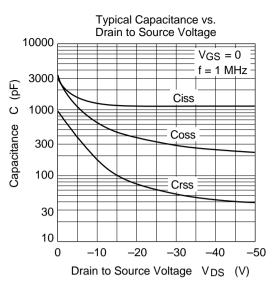


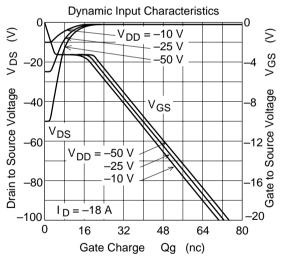


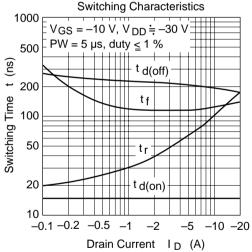


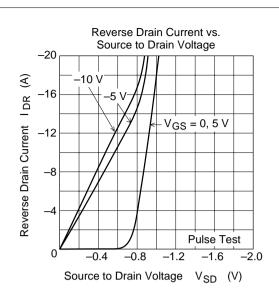


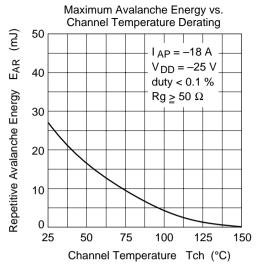


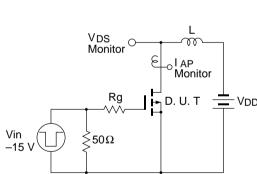




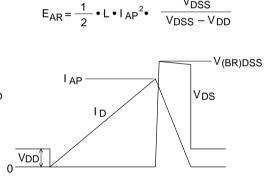




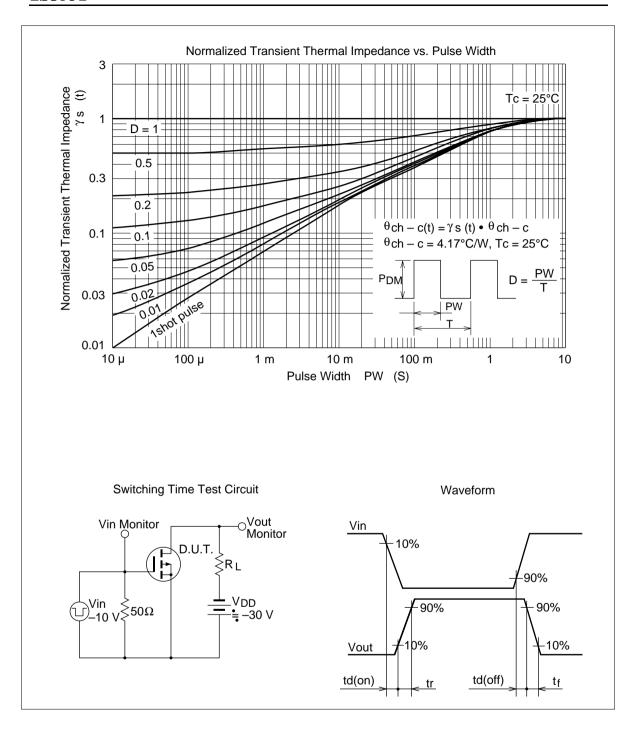




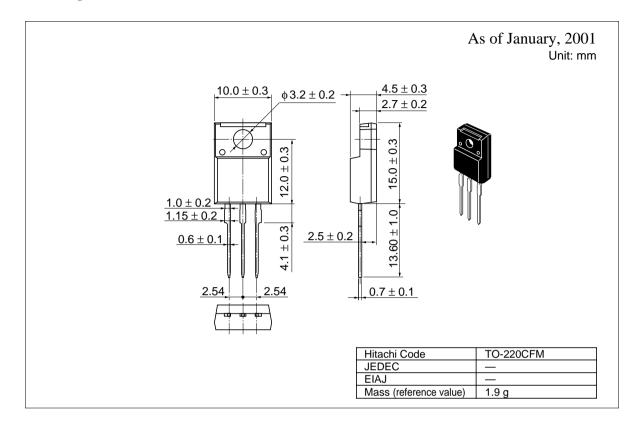
Avalanche Test Circuit



Avalanche Waveform



## **Package Dimensions**



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