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2SJ532

Silicon P Channel MOS FET High Speed Power Switching

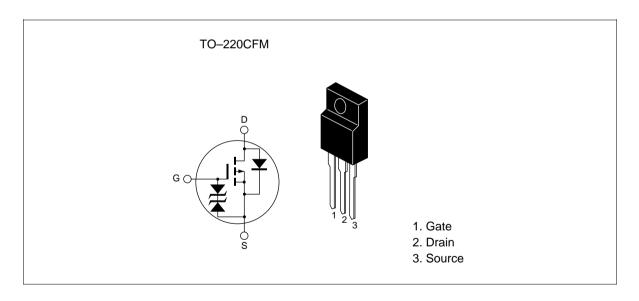


ADE-208-653B (Z) 3rd. Edition Jul. 1998

Features

- Low on-resistance $R_{DS(on)} = 0.042\Omega$ typ.
- Low drive current.
- 4V gate drive devices.
- High speed switching.

Outline



2SJ532

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	-60	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I _D	-20	А
Drain peak current	Note1 D(pulse)	-80	А
Body-drain diode reverse drain current	I _{DR}	-20	Α
Avalanche current	I _{AP} Note3	-20	А
Avalanche energy	E _{AR} Note3	34	mJ
Channel dissipation	Pch Note2	30	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

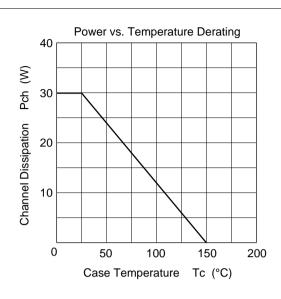
- Note: 1. PW \leq 10 μ s, duty cycle \leq 1 %
 - 2. Value at Tc = 25°C
 - 3. Value at Tch = 25°C, Rg \geq 50 Ω

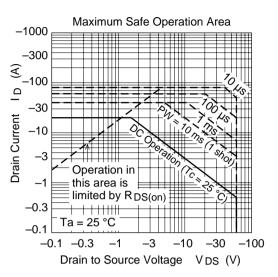
Electrical Characteristics (Ta = 25°C)

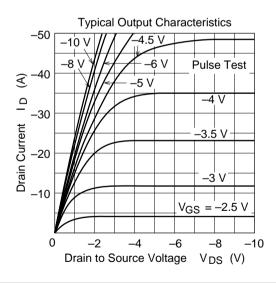
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	-60	_	_	V	$I_{D} = -10 \text{mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_{G} = \pm 100 \mu A, V_{DS} = 0$
Zero gate voltege drain current	I _{DSS}	_	_	-10	μΑ	$V_{DS} = -60 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.0	_	-2.0	V	$I_{D} = -1 \text{mA}, V_{DS} = -10 \text{V}$
Static drain to source on state	R _{DS(on)}	_	0.042	0.055	Ω	$I_{\rm D} = -10 {\rm A}, \ V_{\rm GS} = -10 {\rm V}^{\rm Note4}$
resistance	R _{DS(on)}	_	0.065	0.095	Ω	$I_D = -10A, V_{GS} = -4V^{Note4}$
Forward transfer admittance	y _{fs}	10	16	_	S	$I_D = -10A, V_{DS} = -10V^{Note4}$
Input capacitance	Ciss	_	1750	_	pF	$V_{DS} = -10V$
Output capacitance	Coss	_	800	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	180	_	pF	f = 1MHz
Turn-on delay time	t _{d(on)}	_	16	_	ns	$V_{GS} = -10V, I_{D} = -10A$
Rise time	t _r	_	100	_	ns	$R_L = 3\Omega$
Turn-off delay time	t _{d(off)}	_	230	_	ns	
Fall time	t _f	_	140	_	ns	_
Body-drain diode forward voltage	V_{DF}	_	-1.0	_	V	$I_F = -20A, V_{GS} = 0$
Body-drain diode reverse recovery time	t _{rr}	_	100	_	ns	$I_F = -20A, V_{GS} = 0$ diF/ dt =50A/ μ s

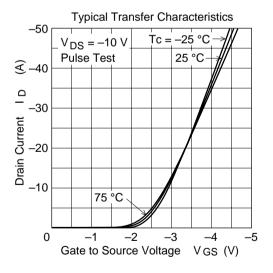
Note: 4. Pulse test

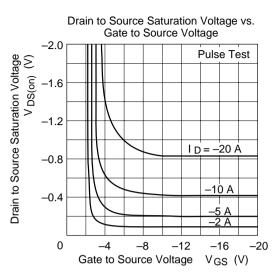
Main Characteristics

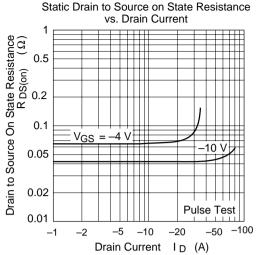


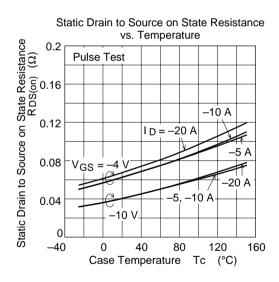


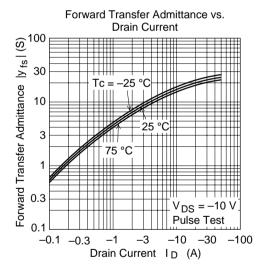


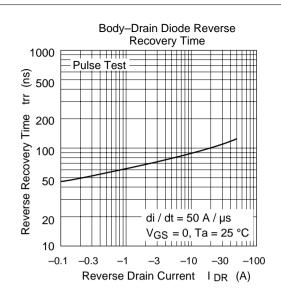


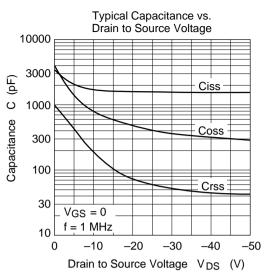


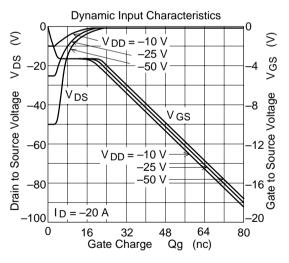


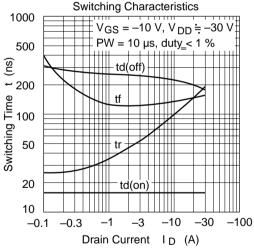


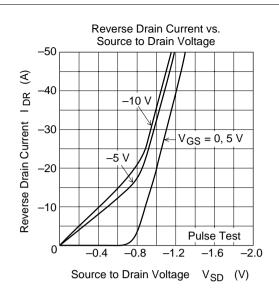


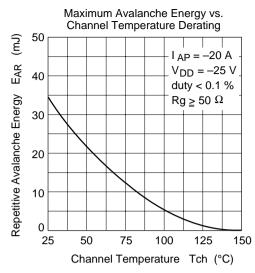






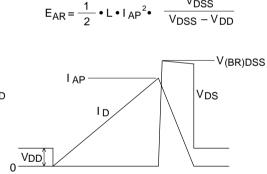




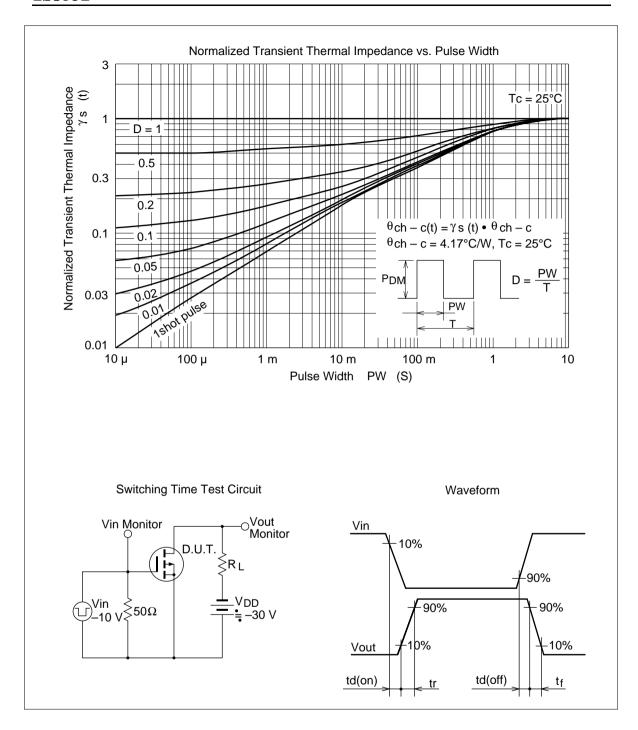


VDS Monitor S I AP Monitor D. U. T VDD

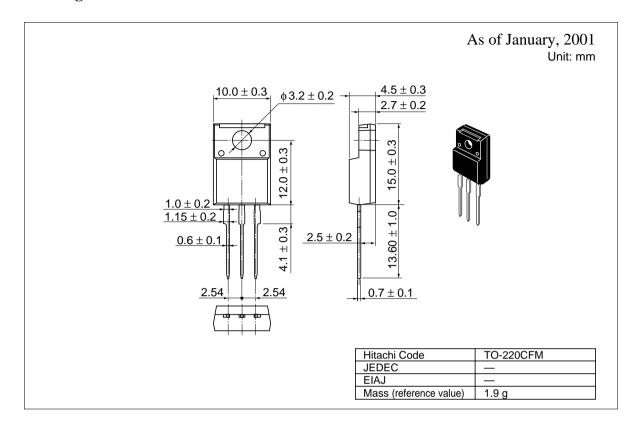
Avalanche Test Circuit



Avalanche Waveform



Package Dimensions



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