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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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Silicon P Channel MOS FET High Speed Power Switching

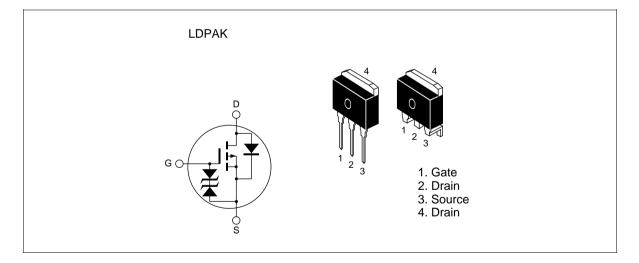


ADE-208-633A (Z) 2nd. Edition Jul. 1998

#### Features

- Low on-resistance  $R_{DS(on)} = 0.075\Omega$  typ.
- Low drive current.
- 4V gate drive devices.
- High speed switching.

#### Outline



#### **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	-60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	–15	A
Drain peak current	Note1 D(pulse)	-60	A
Body-drain diode reverse drain current	I <sub>DR</sub>	–15	A
Avalanche current	I Note3	–15	A
Avalanche energy	E <sub>AR</sub> <sup>Note3</sup>	19	mJ
Channel dissipation	Pch Note2	50	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Note: 1.  $PW \le 10\mu s$ , duty cycle  $\le 1 \%$ 

2. Value at Tc =  $25^{\circ}$ C

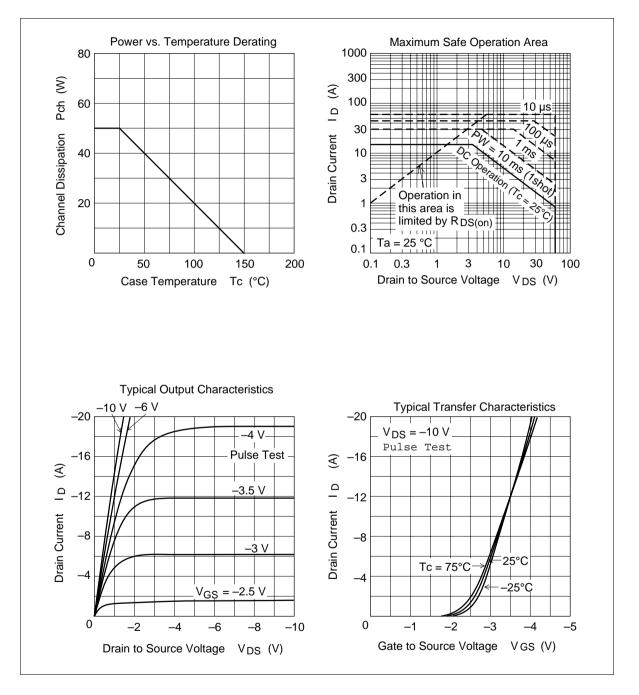
3. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$ 

#### **Electrical Characteristics** (Ta = 25°C)

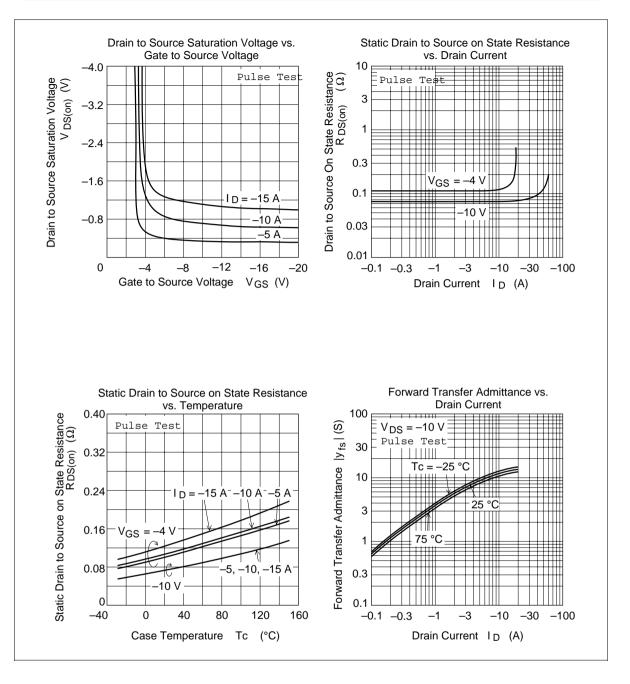
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-60	_	_	V	$I_{\rm D} = -10$ mA, $V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_{g} = \pm 100 \mu A, V_{DS} = 0$
Zero gate voltege drain current	I <sub>DSS</sub>		_	-10	μΑ	$V_{\rm DS} = -60 \text{ V}, \text{ V}_{\rm GS} = 0$
Gate to source leak current	I <sub>GSS</sub>		_	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.0	_	-2.0	V	$I_{\rm D} = -1$ mA, $V_{\rm DS} = -10$ V
Static drain to source on state	$R_{\text{DS(on)}}$		0.075	0.095	Ω	$I_{\rm D} = -8A, V_{\rm GS} = -10V^{\rm Note4}$
resistance	$R_{\text{DS(on)}}$	_	0.105	0.155	Ω	$I_{\rm D} = -8A, V_{\rm GS} = -4V^{\rm Note4}$
Forward transfer admittance	y <sub>fs</sub>	6.5	11	_	S	$I_{\rm D} = -8A, V_{\rm DS} = -10V^{\rm Note4}$
Input capacitance	Ciss		850	_	pF	$V_{DS} = -10V$
Output capacitance	Coss	_	420	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	110	_	pF	f = 1MHz
Turn-on delay time	t <sub>d(on)</sub>		12	_	ns	$V_{GS} = -10V, I_{D} = -8A$
Rise time	t,	_	75	_	ns	$R_{L} = 3.75\Omega$
Turn-off delay time	t <sub>d(off)</sub>		125	_	ns	_
Fall time	t <sub>f</sub>	_	75	_	ns	_
Body-drain diode forward voltage	$V_{\text{DF}}$	—	-1.1	—	V	$I_{\rm F} = -15 {\rm A},  V_{\rm GS} = 0$
Body–drain diode reverse recovery time	t <sub>rr</sub>		70	_	ns	$I_F = -15A, V_{GS} = 0$ diF/ dt =50A/µs

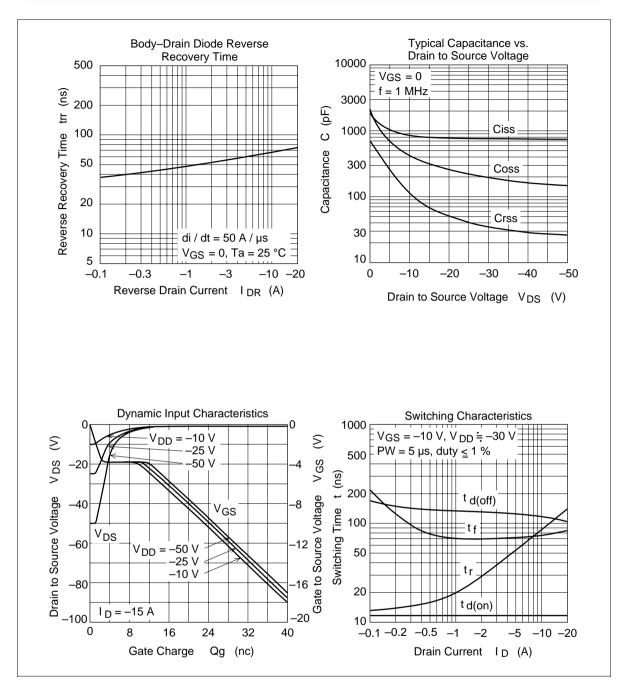
Note: 4. Pulse test

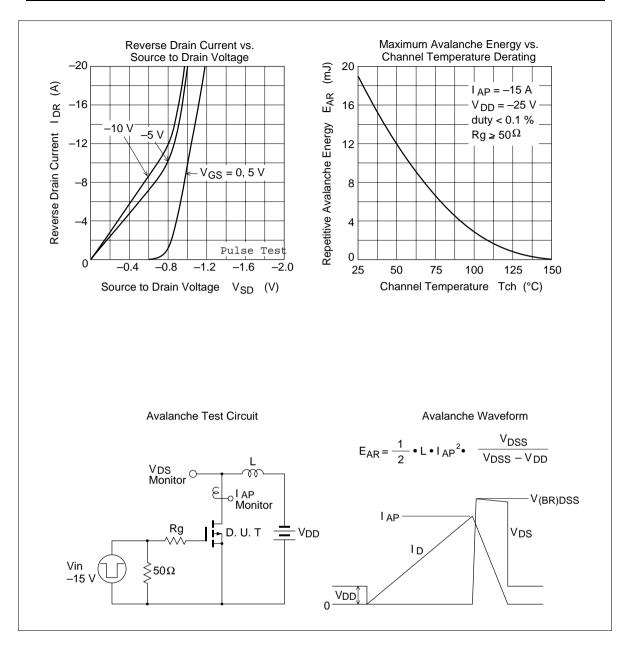
#### **Main Characteristics**

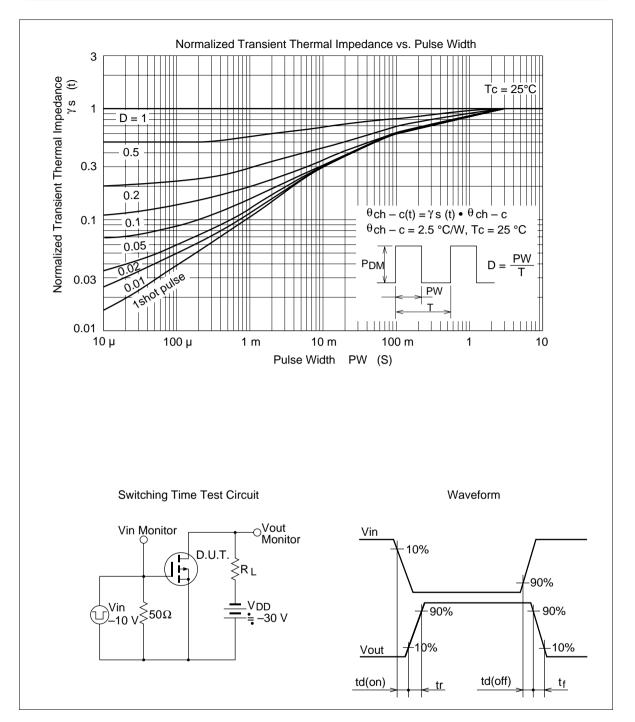


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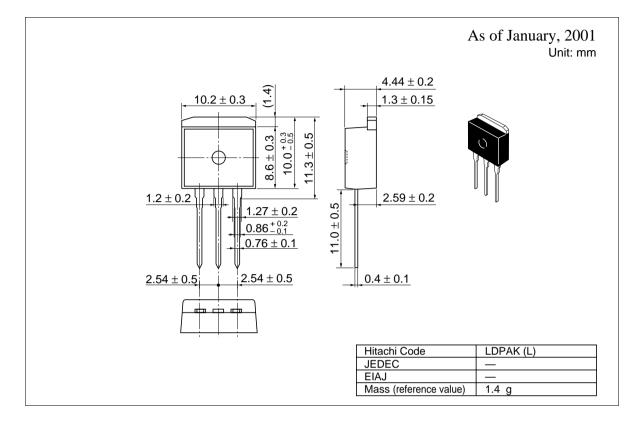


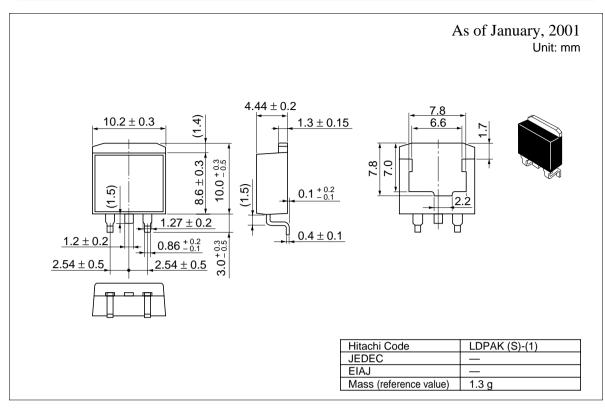




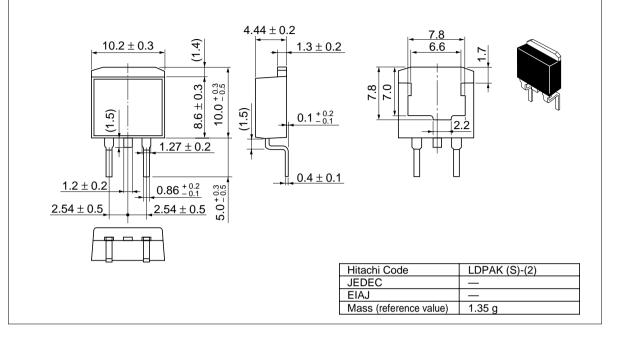
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#### **Package Dimensions**





As of January, 2001 Unit: mm



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