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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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Silicon N-Channel MOS FET



ADE-208-1275 (Z) 1st. Edition Mar. 2001

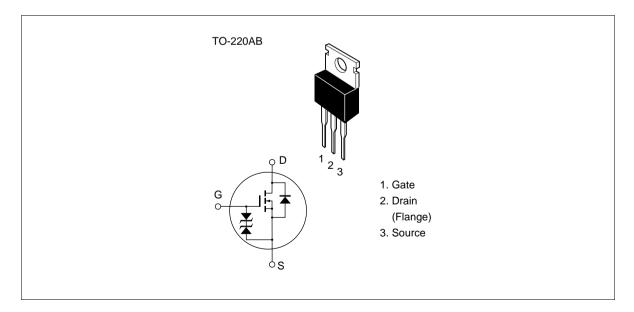
#### Application

High speed power switching

#### Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

#### Outline



#### **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	900	V
Gate to source voltage	V <sub>GSS</sub>	±30	V
Drain current	I <sub>D</sub>	2	А
Drain peak current	I D(pulse) * 1	6	A
Body to drain diode reverse drain current	I <sub>DR</sub>	2	A
Channel dissipation	Pch*2	50	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1.  $PW \le 10 \ \mu s$ , duty cycle  $\le 1\%$ 

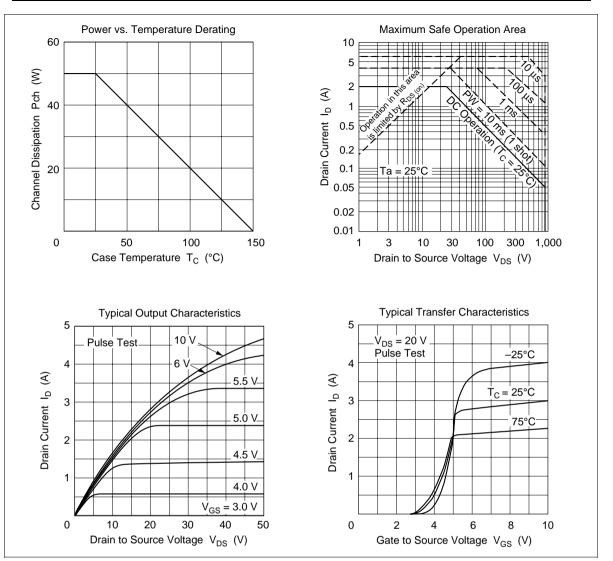
2. Value at  $T_c = 25^{\circ}C$ 

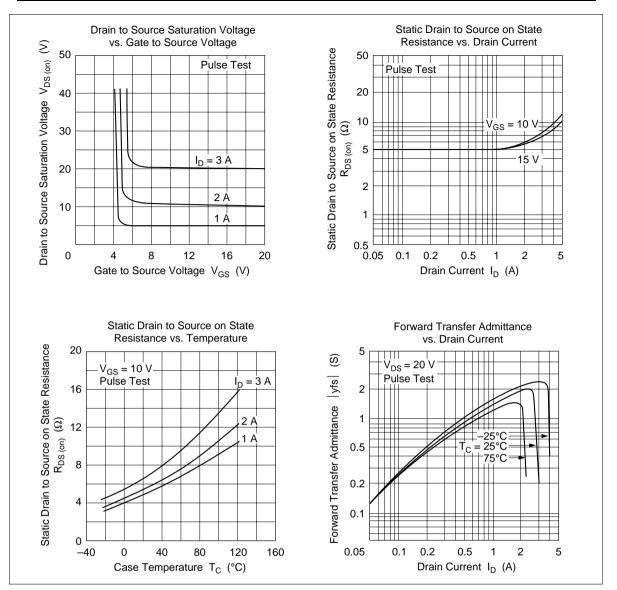
#### **Electrical Characteristics** (Ta = 25°C)

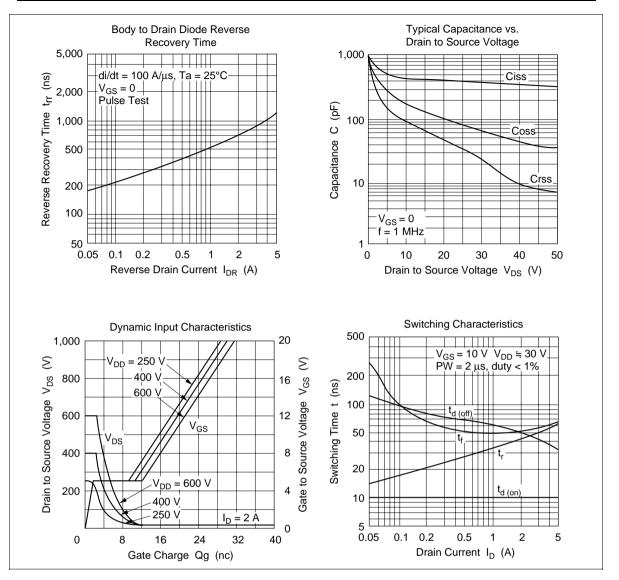
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	900	_		V	$I_{\rm D} = 10 \text{ mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30	—	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	—		±10	μA	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—		250	μA	$V_{\rm DS} = 720 \ V, \ V_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	_	3.0	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{\text{DS(on)}}$	_	5.0	7.0	Ω	$I_{D} = 1 \text{ A}, \text{ V}_{GS} = 10 \text{ V}^{*1}$
Forward transfer admittance	yfs	0.9	1.5		S	$I_{D} = 1 \text{ A}, V_{DS} = 20 \text{ V}^{*1}$
Input capacitance	Ciss	_	425	_	pF	$V_{\rm DS} = 10 \ V, \ V_{\rm GS} = 0,$
Output capacitance	Coss	_	175	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	—	85	_	pF	
Turn-on delay time	t <sub>d(on)</sub>	—	10	_	ns	$I_{D} = 1 \text{ A}, V_{GS} = 10 \text{ V},$
Rise time	t,	—	35	_	ns	$R_{L} = 30 \Omega$
Turn-off delay time	t <sub>d(off)</sub>	—	60	—	ns	
Fall time	t <sub>f</sub>	_	50	_	ns	
Body to drain diode forward voltage	$V_{\text{DF}}$	—	0.9	_	V	$I_{F} = 2 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>	—	700	—	ns	$I_F = 2 A, V_{GS} = 0,$ $di_F/dt = 100 A/\mu s$
Note: 1 Pulse test						

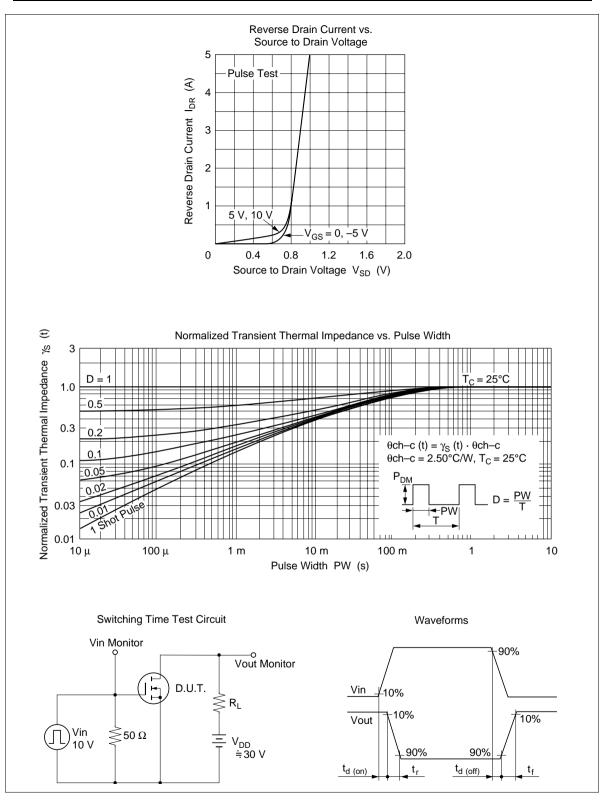
Note: 1. Pulse test





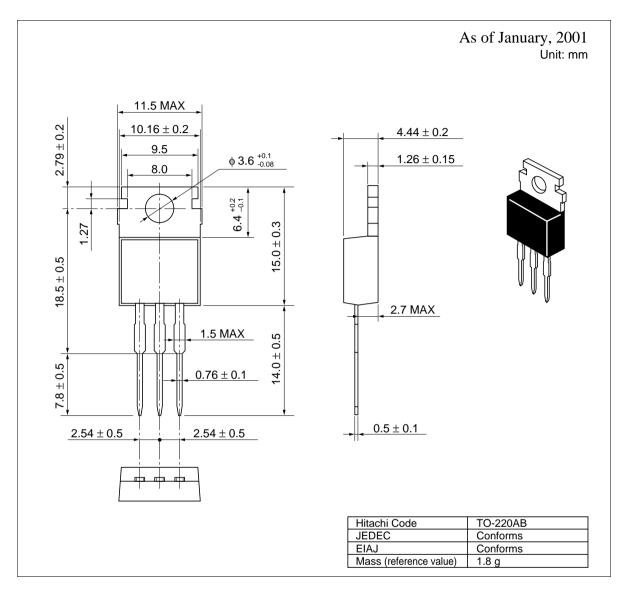








#### **Package Dimensions**



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