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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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Silicon N-Channel MOS FET



ADE-208-1276 (Z) 1st. Edition Mar. 2001

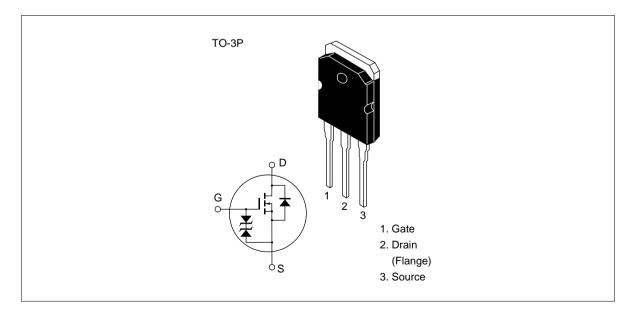
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

Outline



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	900	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	3	A
Drain peak current	I D(pulse) *1	7	А
Body to drain diode reverse drain current	I _{DR}	3	А
Channel dissipation	Pch*2	80	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

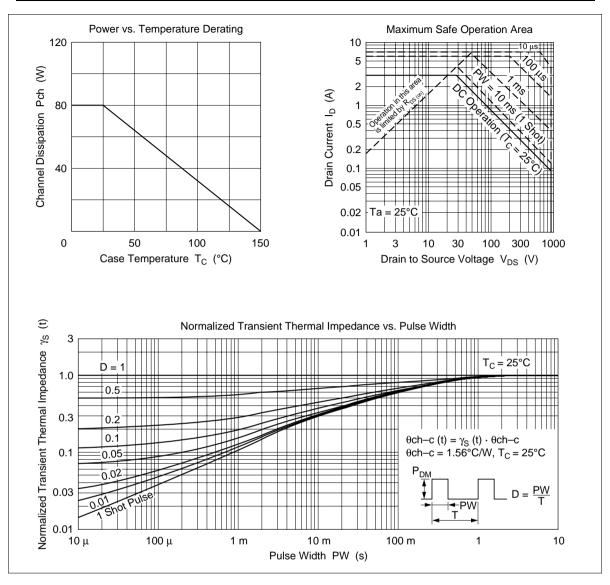
Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at T_c = 25°C

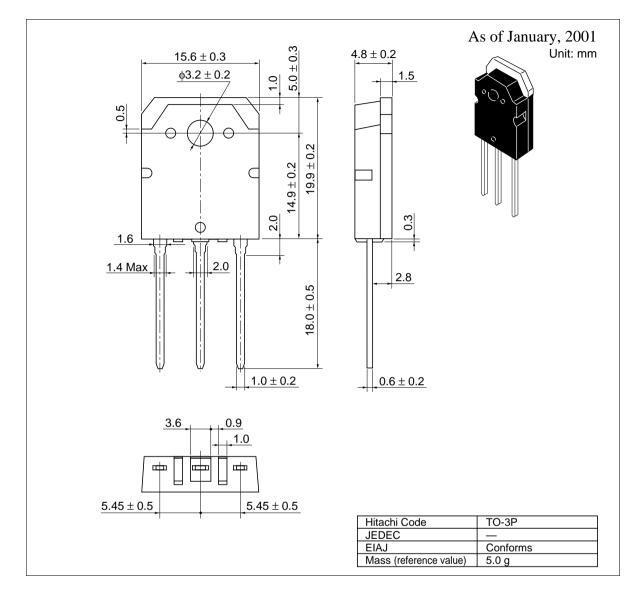
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	900	_	_	V	$I_{\rm D} = 10$ mA, $V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30	—	—	V	$I_{g} = \pm 100 \ \mu A$, $V_{DS} = 0$
Gate to source leak current	I _{GSS}	—	—	±10	μΑ	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	—	—	250	μA	$V_{\rm DS} = 720 \ V, \ V_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{\text{GS(off)}}$	2.0	—	3.0	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{\text{DS(on)}}$	_	5.0	7.0	Ω	$I_{\rm D}$ = 1.5 A, $V_{\rm GS}$ = 10 V * ¹
Forward transfer admittance	yfs	1.2	1.9	_	S	$I_{\rm D}$ = 1.5 A, $V_{\rm DS}$ = 20 V * ¹
Input capacitance	Ciss	_	425	_	pF	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0,$
Output capacitance	Coss		175	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	85	_	pF	
Turn-on delay time	t _{d(on)}	_	10	_	ns	$I_{\rm D} = 2 \text{ A}, \text{ V}_{\rm GS} = 10 \text{ V},$
Rise time	t,	_	40	_	ns	$R_L = 15 \Omega$
Turn-off delay time	$t_{d(off)}$	_	50	_	ns	
Fall time	t _f	_	55	_	ns	
Body to drain diode forward voltage	V_{DF}	_	0.9	_	V	$I_{F} = 3 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}	_	850	—	ns	$I_F = 3 \text{ A}, V_{GS} = 0,$ $di_F/dt = 100 \text{ A}/\mu\text{s}$
Note: 1. Pulse test						

Electrical Characteristics (Ta = 25° C)

See characteristic curves of 2SK1338.



Package Dimensions



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