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2SK1668

Silicon N-Channel MOS FET



ADE-208-1309 (Z) 1st. Edition Mar. 2001

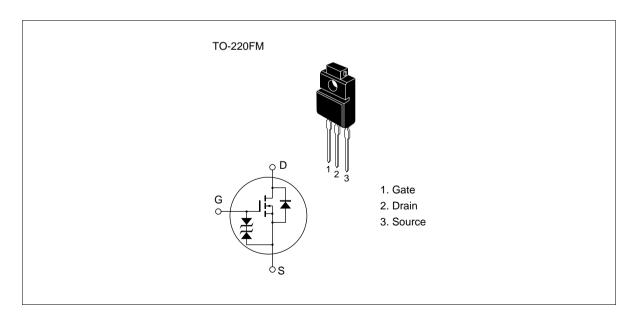
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- · No secondary breakdown
- Suitable for switching regulator and DC DC converter

Outline



2SK1668

Absolute Maximum Ratings $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{\scriptscriptstyle DSS}$	250	V
Gate to source voltage	V _{GSS} ±30		V
Drain current	I _D	7	А
Drain peak current	l _{D(pulse)} *1	28	А
Body to drain diode reverse drain current	I _{DR}	7	Α
Channel dissipation	Pch*2	30	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes 1. PW 10 µs, duty cycle 1%

2. Value at $T_c = 25$ °C

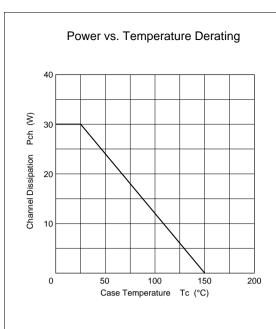
Electrical Characteristics ($Ta = 25^{\circ}C$)

Item	Symbol	Min	Тур	Max	Unit	Test conditions	
Drain to source breakdown voltage	$V_{(BR)DSS}$	250	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$	
Gate to source leak current	I _{GSS}	_	_	±10	μA	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$	
Zero gate voltage drain current	I _{DSS}	_	_	250	μΑ	$V_{DS} = 200 \text{ V}, V_{GS} = 0$	
Gate to source cutoff voltage	$V_{\text{GS(off)}}$	2.0	_	3.0	V	$I_{D} = 1 \text{ mA}, V_{DS} = 10 \text{ V}$	
Static drain to source on state resistance	R _{DS(on)}	_	0.4	0.55		$I_D = 4 \text{ A}, V_{GS} = 10 \text{ V}^{*1}$	
Forward transfer admittance	y _{fs}	3.0	5.0	_	S	$I_D = 4 \text{ A}, V_{DS} = 10 \text{ V}^{*1}$	
Input capacitance	Ciss	_	690	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$	
Output capacitance	Coss	_	265	_	pF	f = 1 MHz	
Reverse transfer capacitance	Crss	_	45	_	pF		
Turn-on delay time	$t_{d(on)}$	_	13	_	ns	$I_D = 4 A, V_{GS} = 10 V,$	
Rise time	t _r	_	55	_	ns	R _L = 7.5	
Turn-off delay time	t _{d(off)}	_	65	_	ns		
Fall time	t _f	_	37	_	ns		
Body to drain diode forward voltage	V_{DF}	_	1.0	_	V	$I_F = 7 \text{ A}, V_{GS} = 0$	
Body to drain diode reverse recovery time	t _{rr}		180		ns	$I_F = 7 \text{ A}, V_{GS} = 0,$ $di_F/dt = 100 \text{ A/}\mu\text{s}$	

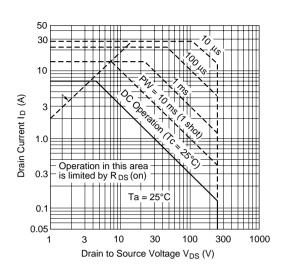
Note 1. Pulse test

See characteristic curves of 2SK1667.

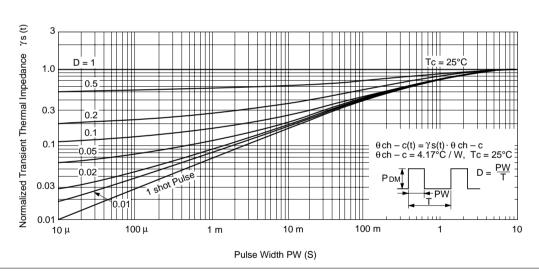
2SK1668



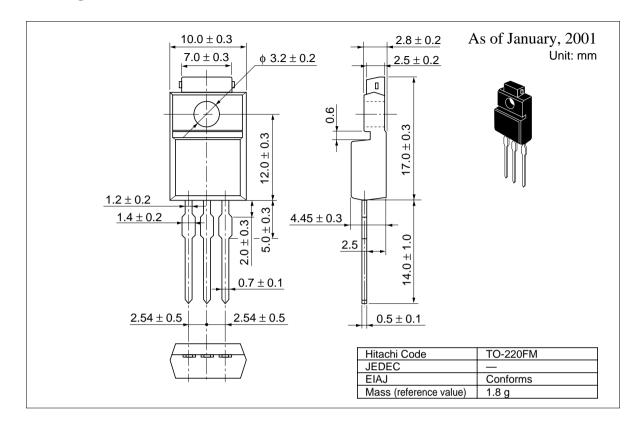
Maximum Safe Operation Area



Normalized Transient Thermal Impedance vs. Pulse Width



Package Dimensions



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