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2SK1807

Silicon N-Channel MOS FET



ADE-208-1321 (Z) 1st. Edition Mar. 2001

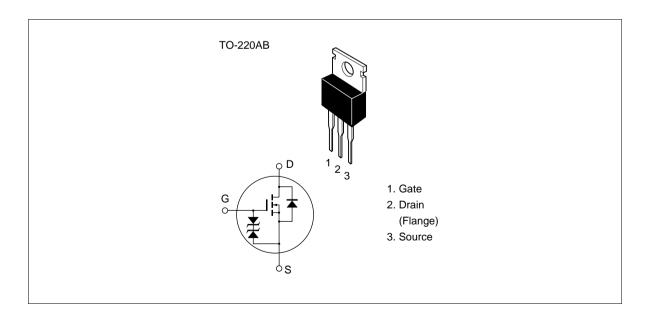
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switchingregulator, DC-DC converter

Outline



2SK1807

Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Ratings	Unit	
Drain to source voltage	$V_{\scriptscriptstyle DSS}$	900	V	
Gate to source voltage	$V_{\sf GSS}$	±30	V	
Drain current	I _D	4	А	
Drain peak current	I _{D(pulse)} *1	10	А	
Body to drain diode reverse drain current	I _{DR}	4	Α	
Channel dissipation	Pch*2	60	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Notes 1. PW 10 µs, duty cycle 1 %

2. Value at Tc = 25 °C

Electrical Characteristics ($Ta = 25^{\circ}C$)

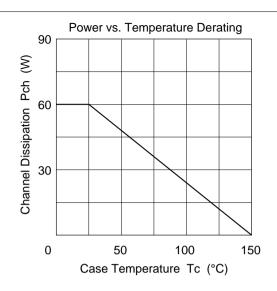
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	900	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μA	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	250	μΑ	$V_{DS} = 720 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0	_	3.0	V	$I_{D} = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	R _{DS(on)}	_	3.0	4.0		$I_D = 2 A$ $V_{GS} = 10 V^{*1}$
Forward transfer admittance	y _{fs}	1.7	2.7	_	S	$I_D = 2 A$ $V_{DS} = 10 V^{*1}$
Input capacitance	Ciss	_	740	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	305	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	150	_	pF	f = 1 MHz
Turn-on delay time	t _{d(on)}	_	15	_	ns	I _D = 2 A
Rise time	t _r	_	60	_	ns	V _{GS} = 10 V
Turn-off delay time	t _{d(off)}	_	100	_	ns	R _L = 15
Fall time	t _f	_	80	_	ns	
Body to drain diode forward voltage	V_{DF}	_	0.9	_	V	$I_F = 4 A, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}	_	800	_	ns	$I_F = 4 \text{ A}, V_{GS} = 0,$ $di_F / dt = 100 \text{ A} / \mu \text{s}$
Note 1 Pulse Test						

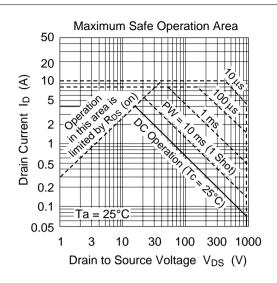
Note 1. Pulse Test

See characteristic curves of 2SK1340

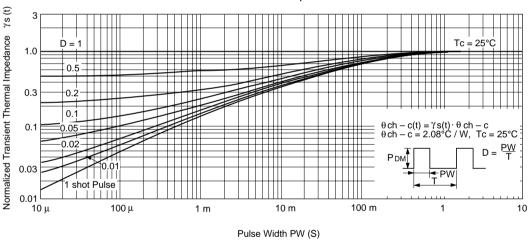
2

2SK1807

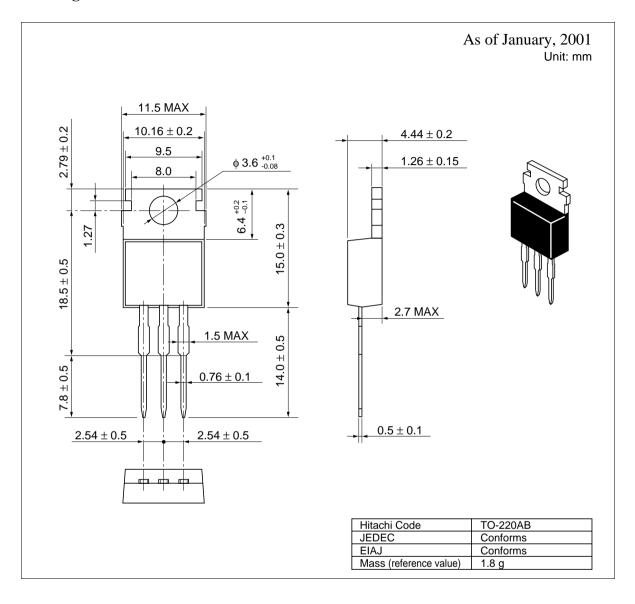




Normalized Transient Thermal Impedance vs. Pulse Width



Package Dimensions



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