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2SK2726

Silicon N Channel MOS FET High Speed Power Switching

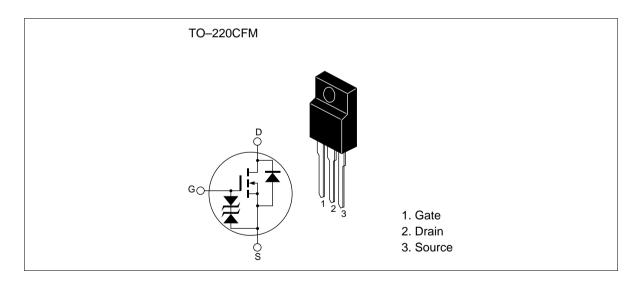


ADE-208-453B (Z) 3rd. Edition Sep. 1997

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- · Avalanche ratings

Outline



2SK2726

Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	500	V	
Gate to source voltage	$V_{\sf GSS}$	±30	V	
Drain current	I _D	7	А	
Drain peak current	I _{D(pulse)} *1	28	А	
Body to drain diode reverse drain current	I _{DR}	7	Α	
Avalanche current	I _{AP} *3	7	А	
Avalanche energy	E _{AR} *3	2.7	mJ	
Channel dissipation	Pch*2	30	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1 %

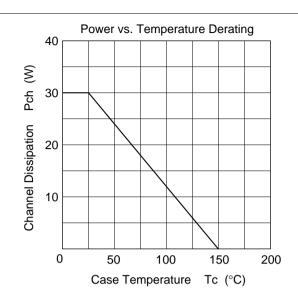
- 2. Value at Tc = 25°C
- 3. Value at Tch = 25°C, Rg \geq 50 Ω

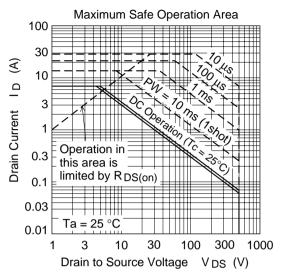
Electrical Characteristics ($Ta = 25^{\circ}C$)

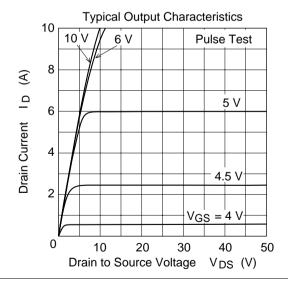
Symbol	Min	Тур	Max	Unit	Test Conditions
$V_{(BR)DSS}$	500	_	_	V	$I_D = 10 \text{mA}, V_{GS} = 0$
$V_{(BR)GSS}$	±30	_	_	V	$I_G = \pm 100 \mu A, \ V_{DS} = 0$
I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 25V, V_{DS} = 0$
I _{DSS}	_	_	10	μА	$V_{DS} = 500 \text{ V}, V_{GS} = 0$
$V_{GS(off)}$	2.5	_	3.5	V	$I_D = 1 \text{mA}, \ V_{DS} = 10 \text{V}^{*1}$
R _{DS(on)}	_	0.75	0.95	Ω	$I_D = 4A, V_{GS} = 10V^{*1}$
y _{fs}	3.5	6.0	_	S	$I_D = 4A, V_{DS} = 10V^{*1}$
Ciss	_	1100	_	pF	V _{DS} = 10V
Coss	_	330	_	pF	$V_{GS} = 0$
Crss	_	65	_	pF	f = 1MHz
Qg	_	21	_	nc	$V_{DD} = 400V$
Qgs	_	5	_	nc	V _{GS} = 10V
Qgd	_	8	_	nc	$I_D = 7A$
$t_{\text{\tiny d(on)}}$	_	20	_	ns	$V_{GS} = 10V, I_{D} = 4A$
t _r	_	65	_	ns	$R_L = 7.5\Omega$
$t_{d(off)}$	_	60	_	ns	
t_{\scriptscriptstylef}	_	40	_	ns	
V_{DF}	_	0.95	_	V	$I_{D} = 7A, V_{GS} = 0$
t _{rr}	_	260	_	ns	$I_F = 7A, V_{GS} = 0$ diF/ dt = 100A/µs
	$\begin{array}{c} V_{(BR)DSS} \\ \\ V_{(BR)GSS} \\ \\ \\ I_{DSS} \\ \\ \\ V_{GS(off)} \\ \\ R_{DS(on)} \\ \\ \\ IJ_{fs} \\ \\ Ciss \\ Coss \\ Crss \\ Qg \\ Qgs \\ Qgd \\ \\ t_{d(on)} \\ \\ t_r \\ \\ t_{d(off)} \\ \\ t_f \\ \\ V_{DF} \\ \\ \end{array}$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

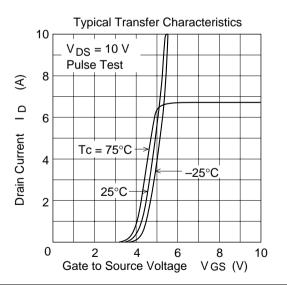
Note: 1. Pulse test

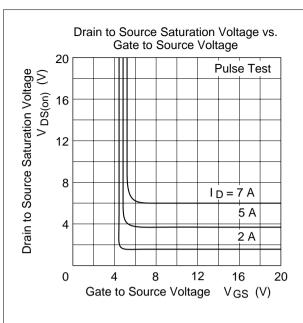
Main Characteristics

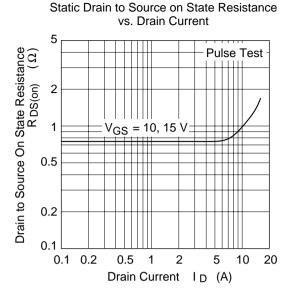


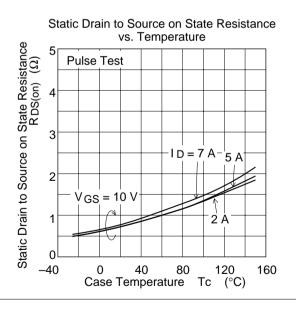


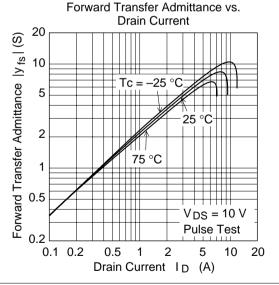


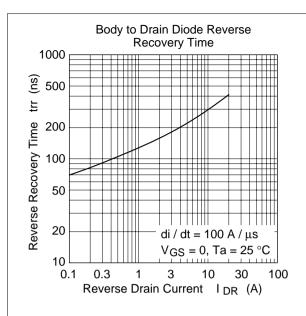


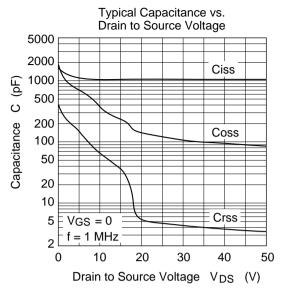


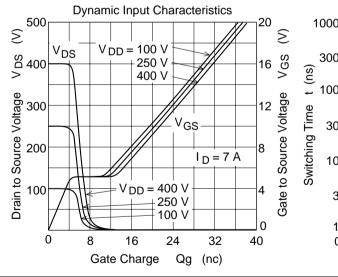


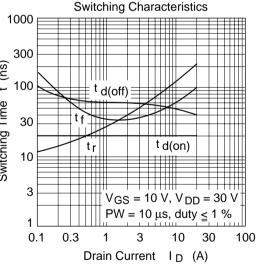


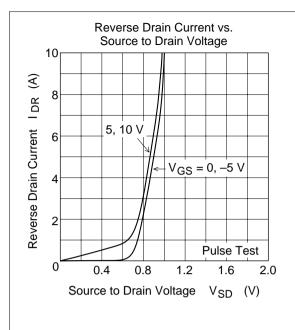


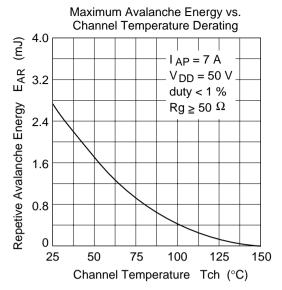


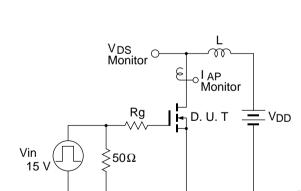








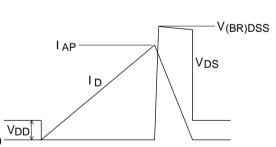


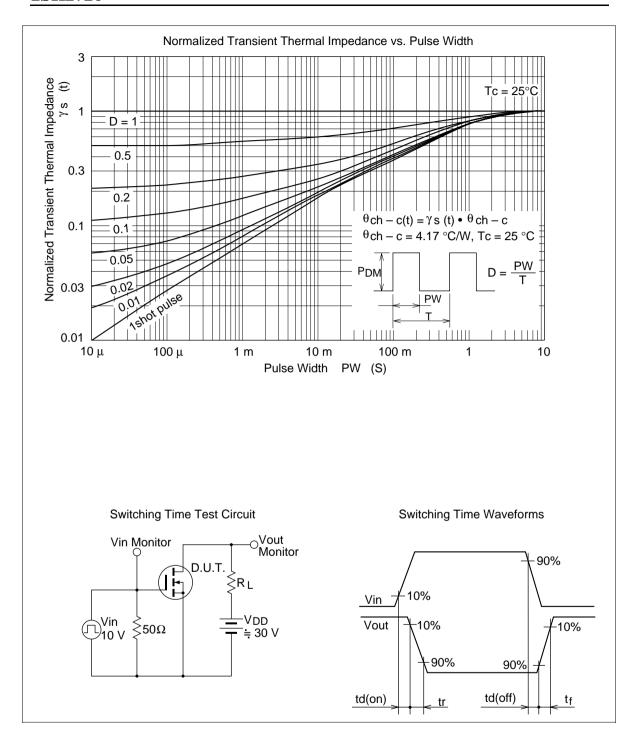


Avalanche Test Circuit

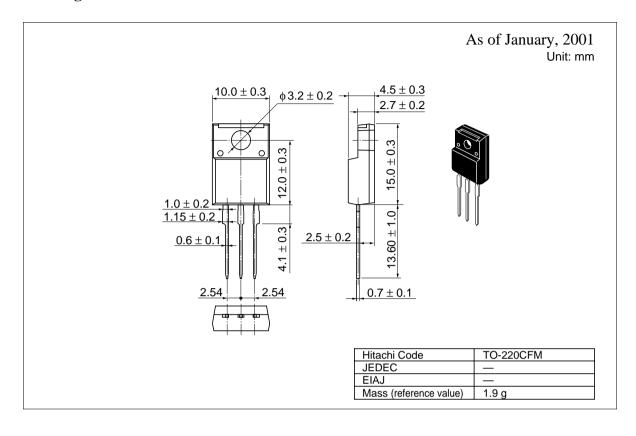
Avalanche Waveform

 $E_{AR} = \frac{1}{2} \cdot L \cdot I_{AP}^2 \cdot$





Package Dimensions



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