

To all our customers

Regarding the change of names mentioned in the document, such as Mitsubishi Electric and Mitsubishi XX, to Renesas Technology Corp.

The semiconductor operations of Hitachi and Mitsubishi Electric were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Mitsubishi Electric, Mitsubishi Electric Corporation, Mitsubishi Semiconductors, and other Mitsubishi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

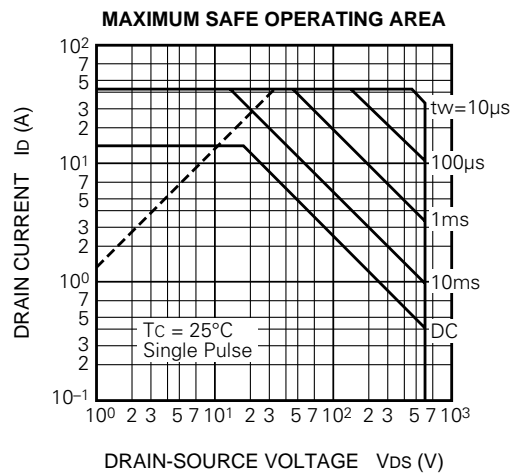
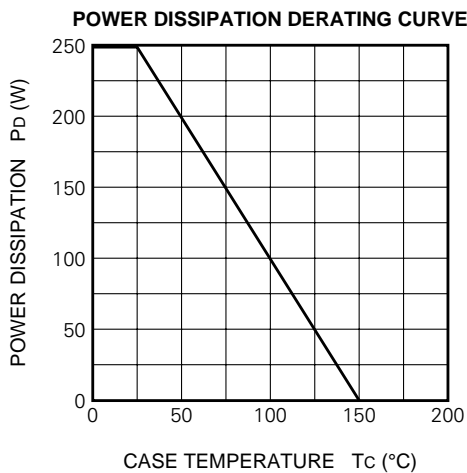
Note : Mitsubishi Electric will continue the business operations of high frequency & optical devices and power devices.

Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

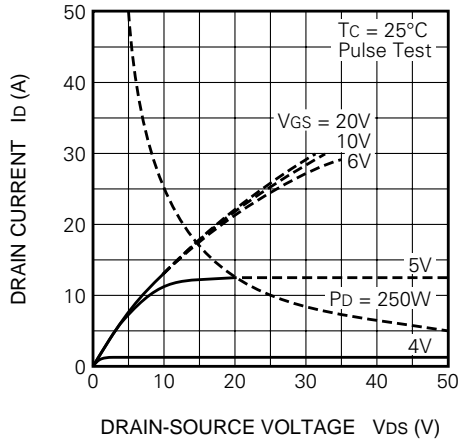
ELECTRICAL CHARACTERISTICS (T_{ch} = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V (BR) DSS	Drain-source breakdown voltage	I _D = 1mA, V _{GS} = 0V	600	—	—	V
V (BR) GSS	Gate-source breakdown voltage	I _G = ±100μA, V _{DS} = 0V	±30	—	—	V
I _{GSS}	Gate-source leakage current	V _{GS} = ±25V, V _{DS} = 0V	—	—	±10	μA
I _{DSS}	Drain-source leakage current	V _{DS} = 600V, V _{GS} = 0V	—	—	1	mA
V _{GS} (th)	Gate-source threshold voltage	I _D = 1mA, V _{DS} = 10V	2	3	4	V
r _{DS} (ON)	Drain-source on-state resistance	I _D = 7A, V _{GS} = 10V	—	0.58	0.75	Ω
V _{DS} (ON)	Drain-source on-state voltage	I _D = 7A, V _{GS} = 10V	—	4.06	5.25	V
y _{fs}	Forward transfer admittance	I _D = 7A, V _{DS} = 10V	6.0	9.0	—	S
C _{iss}	Input capacitance	V _{DS} = 25V, V _{GS} = 0V, f = 1MHz	—	2100	—	pF
C _{oss}	Output capacitance		—	250	—	pF
C _{rss}	Reverse transfer capacitance		—	40	—	pF
t _d (on)	Turn-on delay time		V _{DD} = 200V, I _D = 7A, V _{GS} = 10V, R _{GEN} = R _{GS} = 50Ω	—	40	—
t _r	Rise time	—		60	—	ns
t _d (off)	Turn-off delay time	—		250	—	ns
t _f	Fall time	—		85	—	ns
V _{SD}	Source-drain voltage	I _S = 7A, V _{GS} = 0V	—	1.5	2.0	V
R _{th} (ch-c)	Thermal resistance	Channel to case	—	—	0.50	°C/W
t _{rr}	Reverse recovery time	I _S = 14A, di _s /dt = -100A/μs	—	—	150	ns

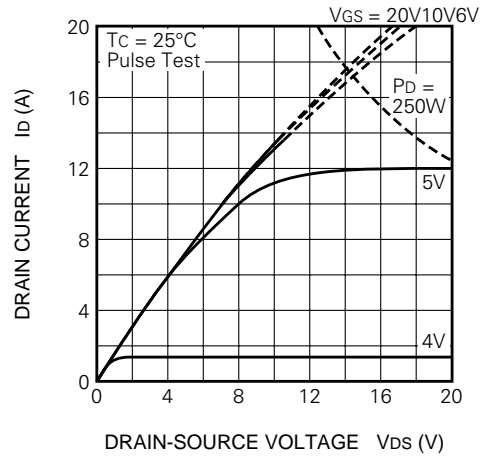
PERFORMANCE CURVES



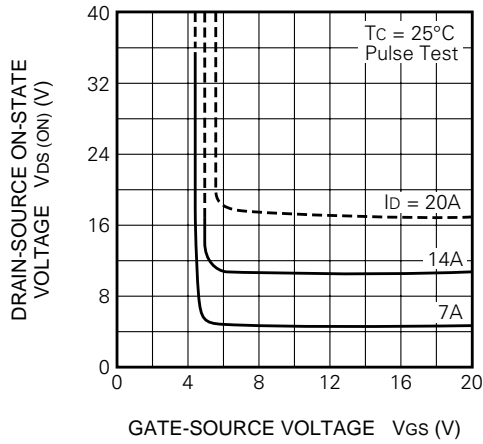
OUTPUT CHARACTERISTICS (TYPICAL)



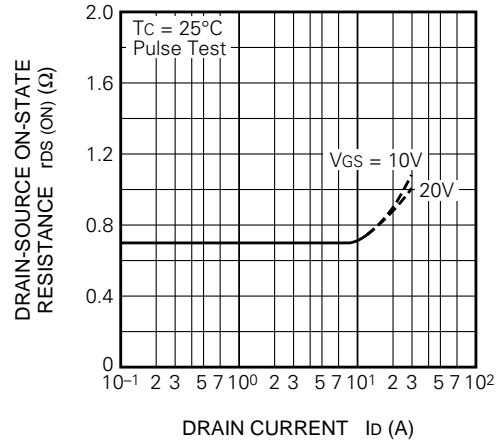
OUTPUT CHARACTERISTICS (TYPICAL)



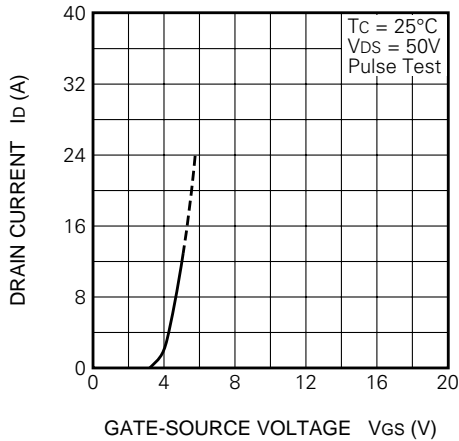
ON-STATE VOLTAGE VS. GATE-SOURCE VOLTAGE (TYPICAL)



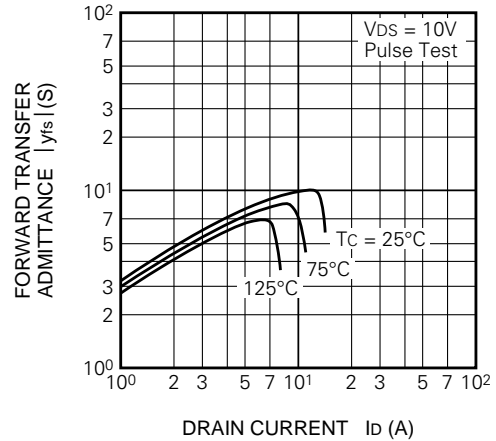
ON-STATE RESISTANCE VS. DRAIN CURRENT (TYPICAL)



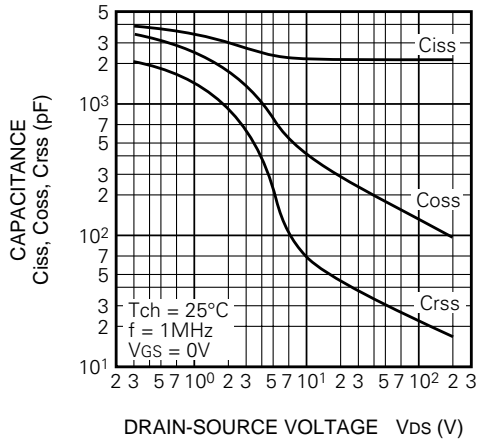
TRANSFER CHARACTERISTICS (TYPICAL)



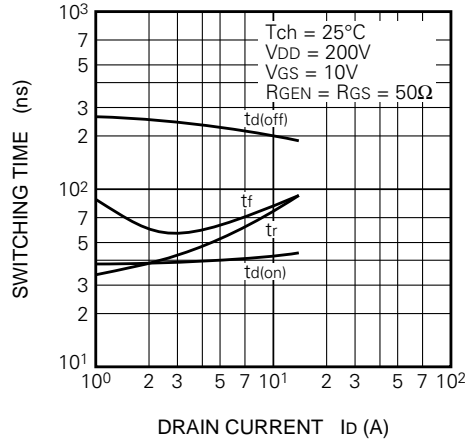
FORWARD TRANSFER ADMITTANCE VS. DRAIN CURRENT (TYPICAL)



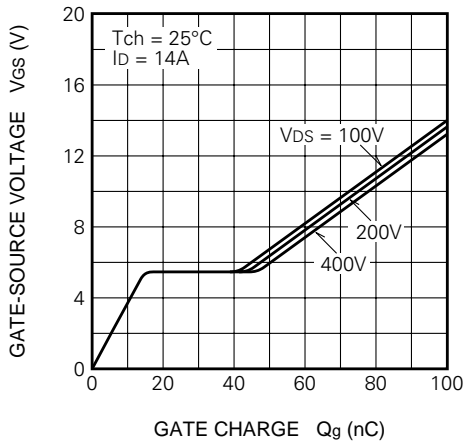
CAPACITANCE VS. DRAIN-SOURCE VOLTAGE (TYPICAL)



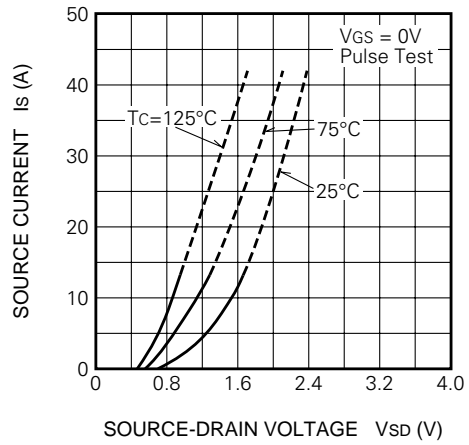
SWITCHING CHARACTERISTICS (TYPICAL)



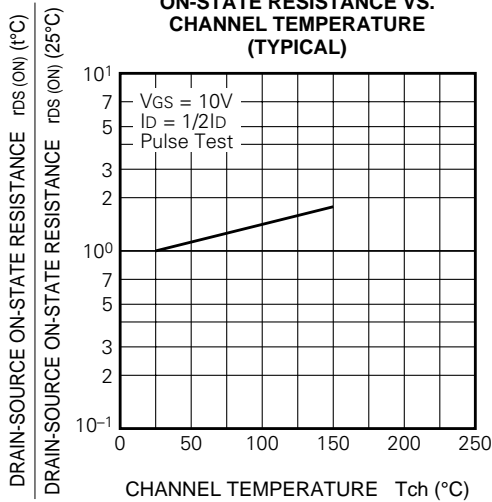
GATE-SOURCE VOLTAGE VS. GATE CHARGE (TYPICAL)



SOURCE-DRAIN DIODE FORWARD CHARACTERISTICS (TYPICAL)



ON-STATE RESISTANCE VS. CHANNEL TEMPERATURE (TYPICAL)



THRESHOLD VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)

