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Dual Operational Amplifier



ADE-204-042 (Z) Rev. 0 Dec. 2000

Description

HA17558 is dual operational amplifiers which provides internal frequency compensation and high performance. It can be applied widely to measuring control equipment and to general Use. The two amplifiers share a common bias network and power supply leads.

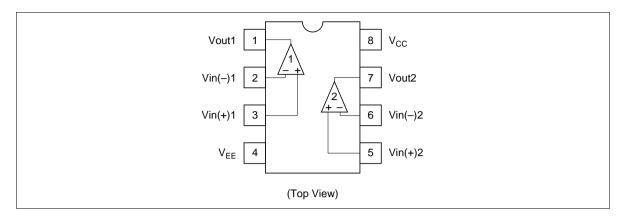
Features

- High voltage Gain: 104dB (Typ)
- High speed: 1V/µs
- Continuous short-circuit protection
- Low-noise operational amplifiers
- Internal frequency compensation

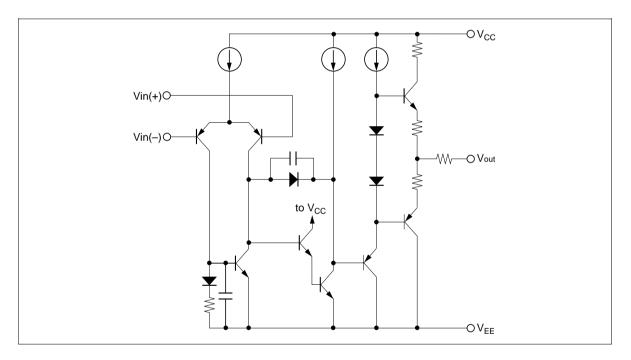
Ordering Information

Type No.	Application	Package
HA17558FP	Industrial use	FP-8D
HA17558F	Commercial use	FP-8D
HA17558	Commercial use	DP-8
HA17558PS	Industrial use	DP-8

Pin Arrangement



Circuit Schematic (1/2)



		Ratings				
Item	Symbol	HA17558	HA17558 PS	HA17558 F	HA17558 FP	Unit
Supply voltage	V _{cc}	+18	+18	+18	+18	V
	V _{EE}	-18	-18	-18	-18	V
Differential input voltage	V _{IN (diff)}	±30	±30	±30	±30	V
Common-mode input voltage	V _{CM} * ³	±15	±15	±15	±15	V
Power dissipation	Ρ _τ	670* ¹	670* ¹	385* ²	385* ²	mW
Operating temperature	Topr	–20 to +75	–20 to +75	–20 to +75	–20 to +75	–20 to +75
Storage temperature	Tstg	–55 to +125	–55 to +125	–55 to +125	–55 to +125	°C

Notes: 1. These are the allowable values up to Ta = 45 °C. Derate by 8.3 mW/°C above that temperature.

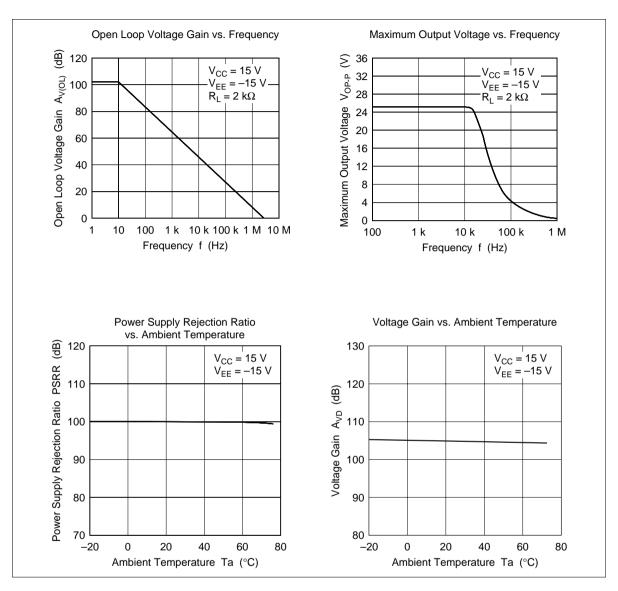
2. These are the allowable values up to Ta = 31 °C mounting on 30% wiring density glass epoxy board. Derate by 7.14mW/°C above that temperature.

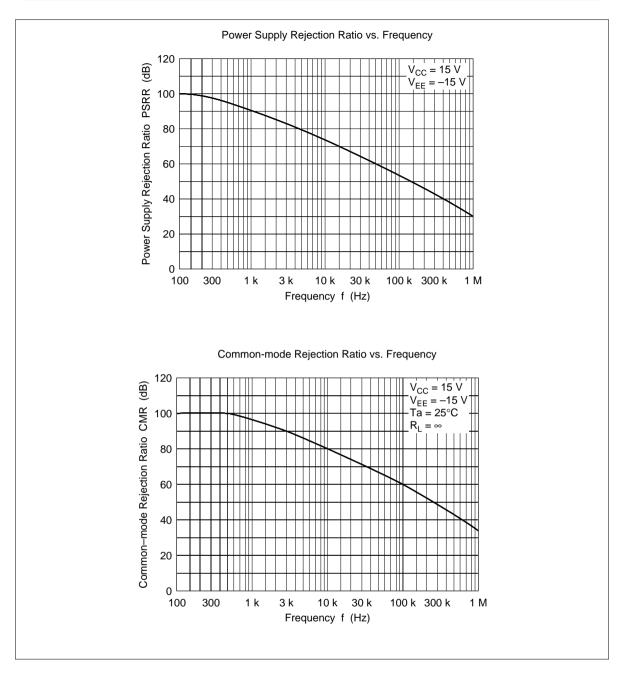
3. If the supply voltage is less than $\pm 15V$, input voltage should be less than supply voltage.

Electrical Characteristics (Ta = 25° C, V_{CC} = +15V, V_{EE} = -15V)

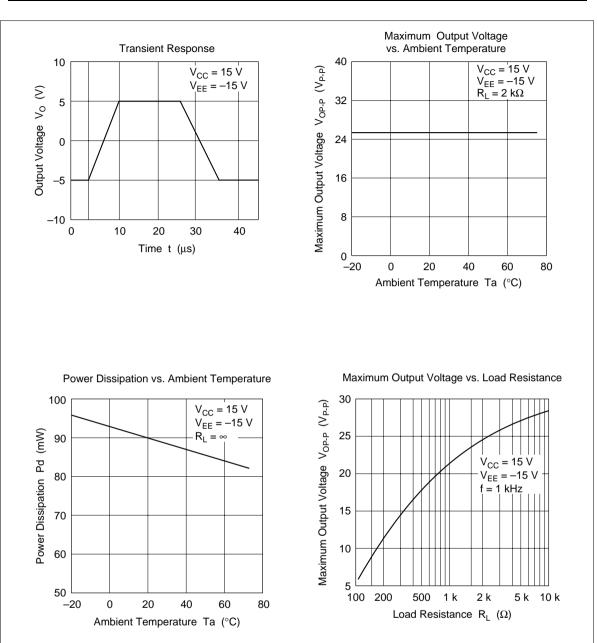
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Input offset voltage	V _{IO}	—	0.5	6	mV	$R_s \le 10 k\Omega$
Input offset current	I _{IO}	—	5	200	nA	
Input bias current	I _{IB}	—	50	500	nA	
Voltage gain	A _{VD}	86	104	—	dB	$R_L \ge 2k\Omega, V_O = \pm 10V$
Maximum output voltage	Vop-p	±12	±14	_	V	$R_L \ge 10 k\Omega$
Maximum output voltage	Vop-p	±10	±12.4	_	V	$R_L \ge 2k\Omega$
Common mode input voltage range	V _{CM}	±12	±14	—	V	
Common mode rejection ratio	CMR	70	100	_	dB	$R_s \le 10 k\Omega$
Supply voltage rejection ratio	PSRR	_	10	150	μV/V	$R_s \le 10 k\Omega$
Power dissipation	Pd	_	90	170	mW	2-channel, No load
Slew rate	SR	_	1.0	_	V/µs	A _{VD} = 1
Equivalent input noise voltage	V _{NI}	_	6	_	μVp-p	$R_s = 1k\Omega$, f = 1H _z to 1kH _z
Channel separation	CS	_	105	_	dB	f = 1kHz

Characteristic Curves

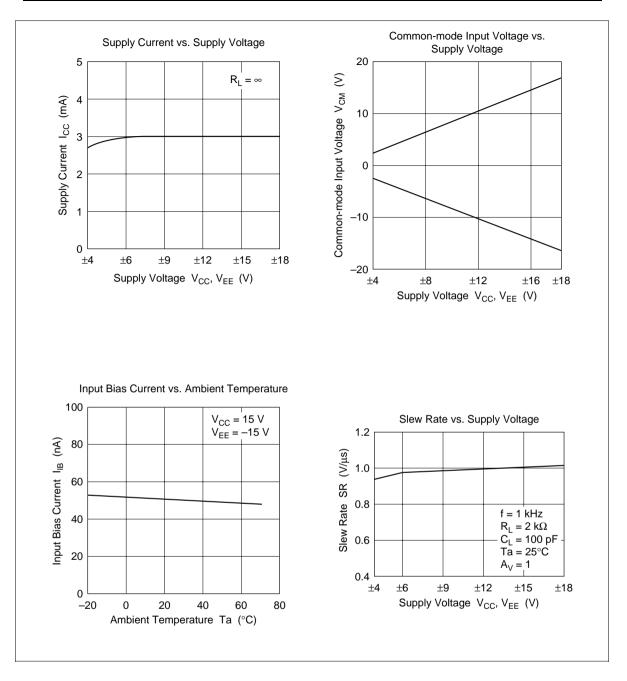


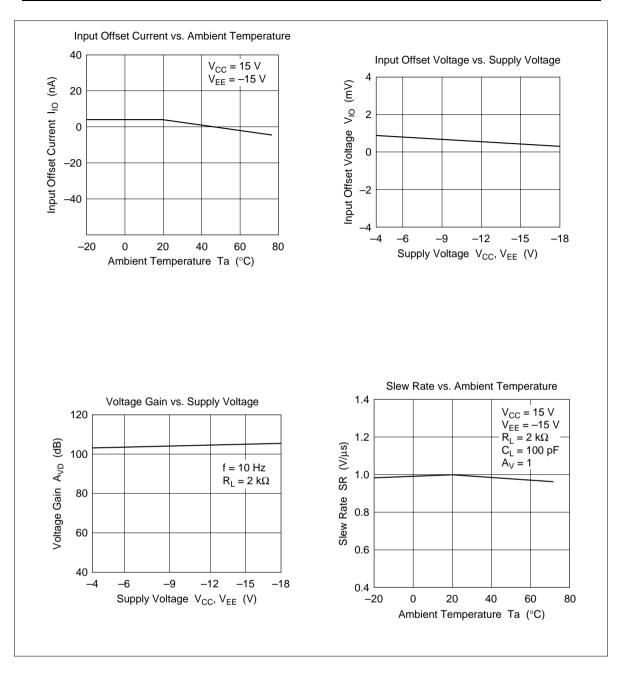




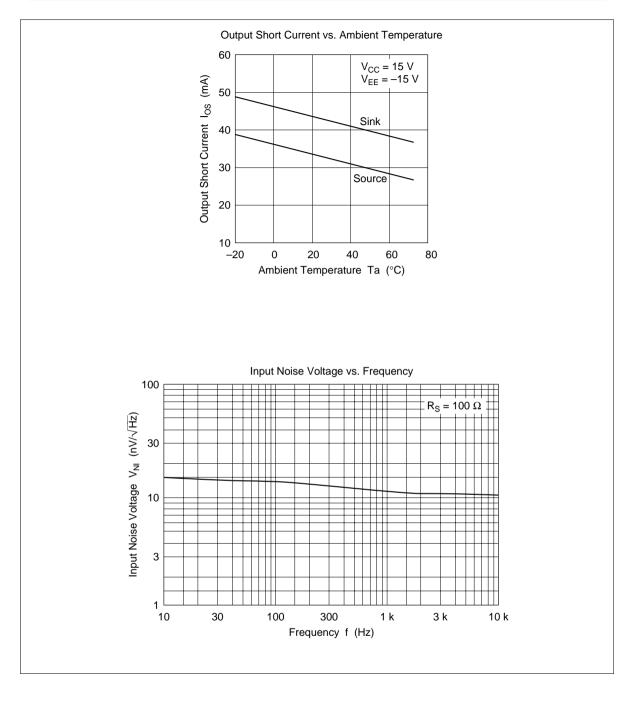


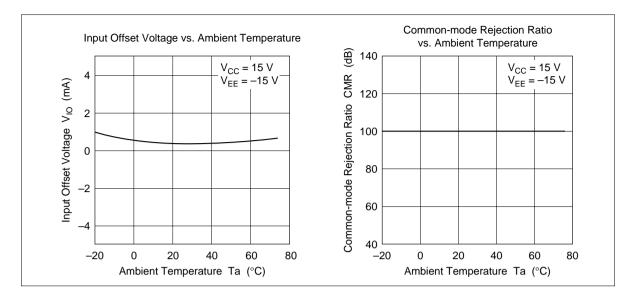




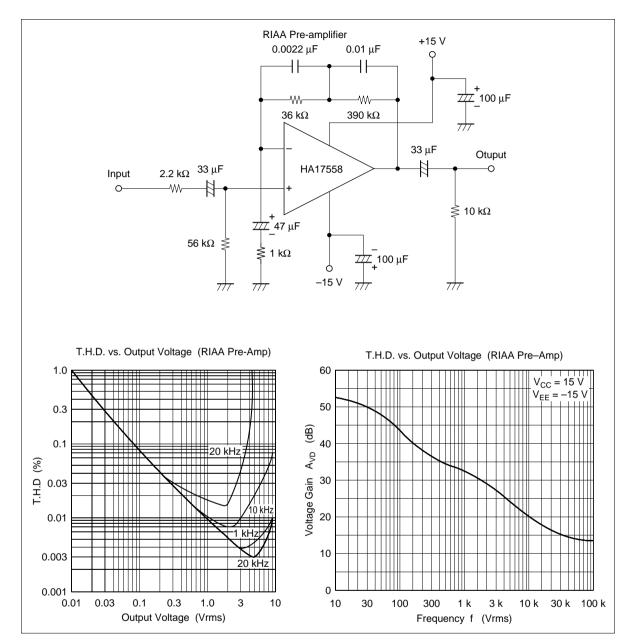






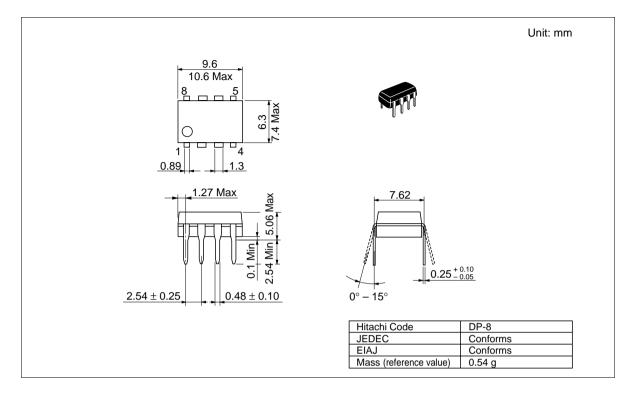


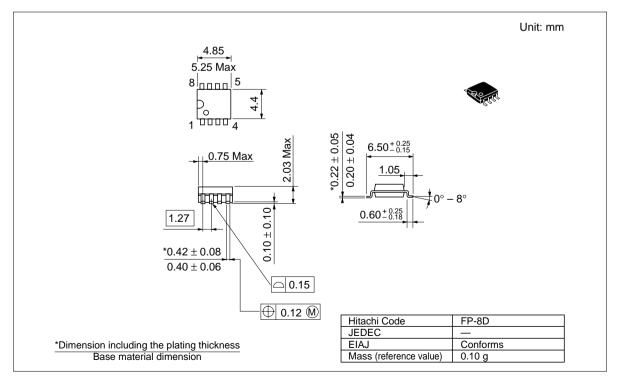
Circuit Example





Package Dimensions







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