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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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Silicon N Channel Power MOS FET High Speed Power Switching

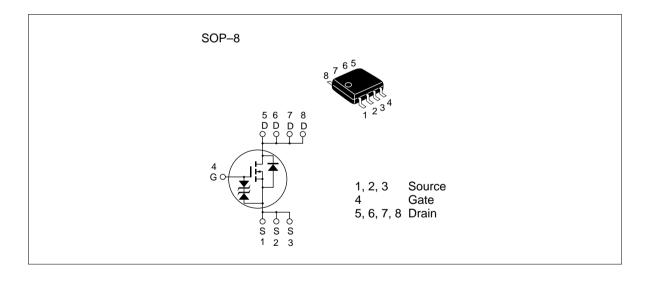


ADE-208-518C (Z) 4th. Edition Feb. 1999

Features

- High speed switching
- Low on-resistance
- Capable of 4 V gate drive
- Low drive current
- High density mounting

Outline



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V_{GSS}	± 20	V
Drain current	I _D	8	Α
Drain peak current	I Note1	64	A
Body-drain diode reverse drain current	I _{DR}	8	A
Channel dissipation	Pch Note2	2.5	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	- 55 to + 150	°C

Note: 1. PW \leq 10 μ s, duty cycle \leq 1 %

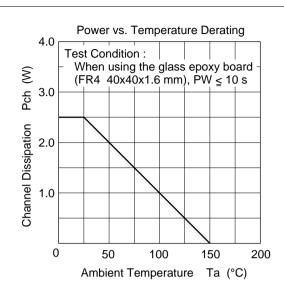
2. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10s

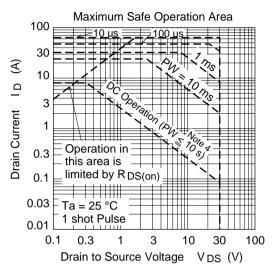
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	± 20	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	± 10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltege drain current	I _{DSS}	_	_	10	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{\text{GS(off)}}$	1.3	_	2.4	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state	$R_{DS(on)}$	_	0.019	0.026	Ω	$I_D = 4 A$, $V_{GS} = 10 V^{Note3}$
resistance	R _{DS(on)}	_	0.030	0.050	Ω	$I_D = 4 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	$ y_{fs} $	7	11	_	S	$I_D = 4 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	660	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	510	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	130	_	pF	f = 1MHz
Turn-on delay time	$t_{d(on)}$	_	30	_	ns	$V_{GS} = 4 \text{ V}, I_D = 4 \text{ A}$
Rise time	t _r	_	265	_	ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	$t_{\text{d(off)}}$	_	35	_	ns	
Fall time	t _f	_	58	_	ns	
Body-drain diode forward voltage	V_{DF}	_	0.8	1.3	V	$IF = 8 A, V_{GS} = 0^{Note3}$
Body–drain diode reverse recovery time	t _{rr}	_	55	_	ns	$IF = 8 \text{ A}, \text{ V}_{GS} = 0$ $diF/dt = 20 \text{ A}/\mu\text{s}$

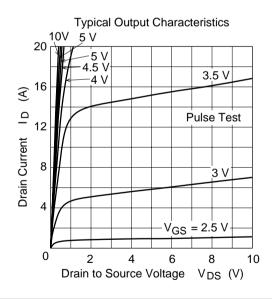
Note: 3. Pulse test

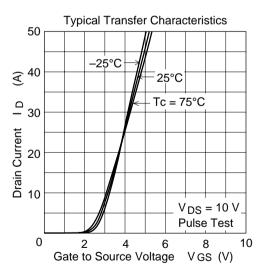
Main Characteristics



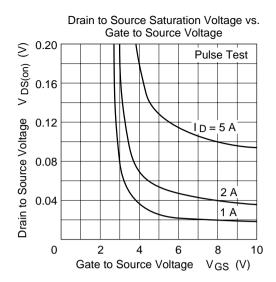


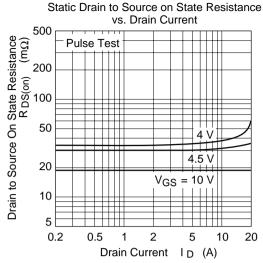
Note 4:
When using the glass epoxy board (FR4 40x40x1.6 mm)

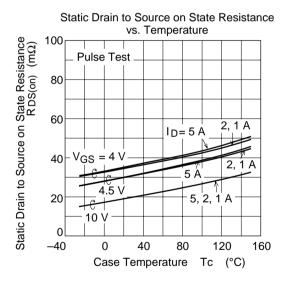


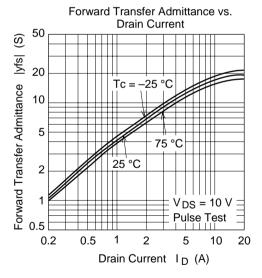


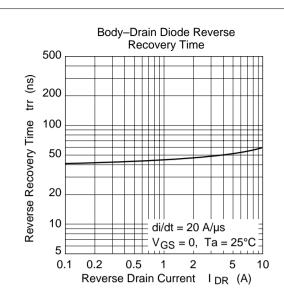
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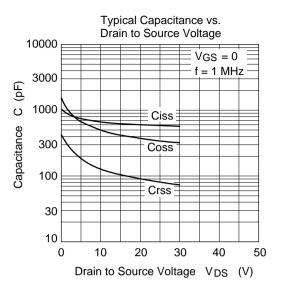


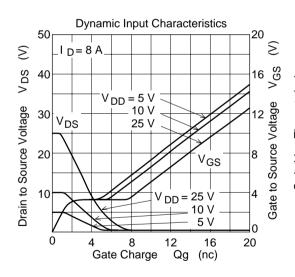


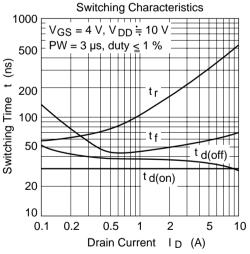


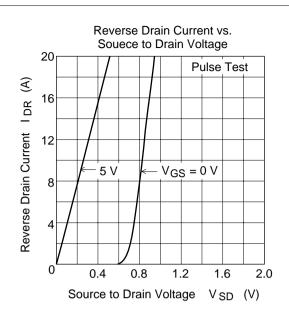


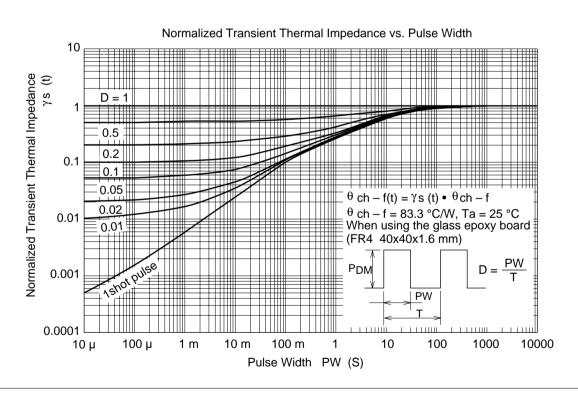


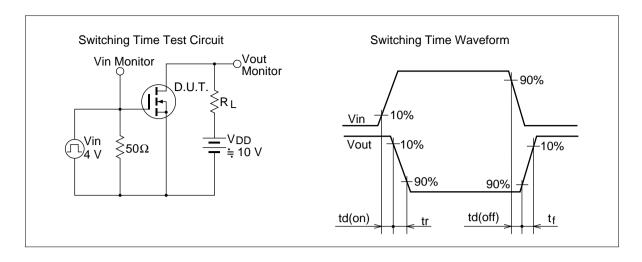






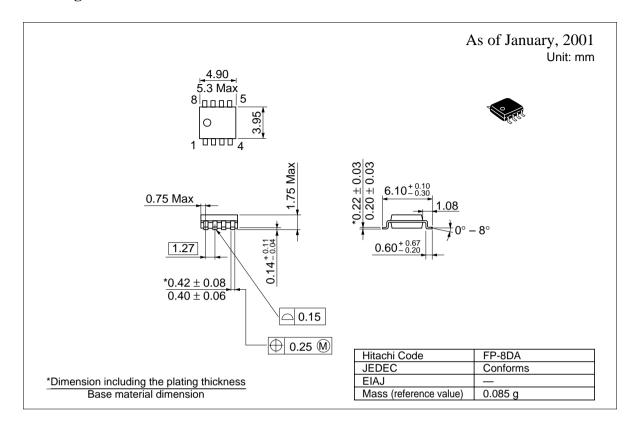






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Package Dimensions



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