

SIPMOS® Small-Signal-Transistor

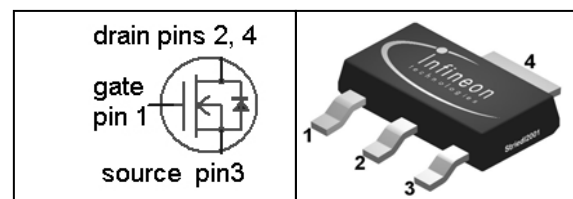
Features

- N-channel
- Depletion mode
- dv/dt rated

Product Summary

V_{DS}	200	V
$R_{DS(on),max}$	3.5	Ω
$I_{DSS,min}$	0.14	A

SOT-223



Type	Package	Ordering Code	Tape and Reel Information	Marking
BSP149	SOT-223	Q67000-S071	E6327: 1000 pcs/reel	BSP149

Maximum ratings, at $T_J=25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	I_D	$T_A=25\text{ °C}$	0.66	A
		$T_A=70\text{ °C}$	0.53	
Pulsed drain current	$I_{D,pulse}$	$T_A=25\text{ °C}$	2.6	
Reverse diode dv/dt	dv/dt	$I_D=0.66\text{ A}$, $V_{DS}=160\text{ V}$, $di/dt=200\text{ A}/\mu\text{s}$, $T_{j,max}=150\text{ °C}$	6	kV/ μs
Gate source voltage	V_{GS}		± 20	V
ESD sensitivity (HBM) as per MIL-STD 883			Class 1	
Power dissipation	P_{tot}	$T_A=25\text{ °C}$	1.8	W
Operating and storage temperature	T_J, T_{stg}		-55 ... 150	$^{\circ}\text{C}$
IEC climatic category; DIN IEC 68-1			55/150/56	

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

Thermal characteristics

Thermal resistance, junction - soldering point (pin 4)	R_{thJS}		-	-	25	K/W
SMD version, device on PCB	R_{thJA}	minimal footprint	-	-	115	
		6 cm ² cooling area ¹⁾	-	-	70	

Electrical characteristics, at $T_j=25\text{ °C}$, unless otherwise specified
Static characteristics

Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS}=-3\text{ V}, I_D=250\text{ }\mu\text{A}$	200	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=3\text{ V}, I_D=400\text{ }\mu\text{A}$	-2.1	-1.4	-1	
Drain-source leakage current	$I_{D(off)}$	$V_{DS}=200\text{ V}, V_{GS}=-3\text{ V}, T_j=25\text{ °C}$	-	-	0.1	μA
		$V_{DS}=200\text{ V}, V_{GS}=-3\text{ V}, T_j=125\text{ °C}$	-	-	5	
Gate-source leakage current	I_{GSS}	$V_{GS}=20\text{ V}, V_{DS}=0\text{ V}$	-	-	10	nA
Saturated drain current	I_{DSS}	$V_{GS}=0\text{ V}, V_{DS}=10\text{ V}$	140	-	-	mA
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=0\text{ V}, I_D=70\text{ mA}$	-	1.7	3.5	Ω
		$V_{GS}=10\text{ V}, I_D=660\text{ mA}$	-	1.0	1.8	
Transconductance	g_{fs}	$ V_{DS} >2 I_D R_{DS(on)max}, I_D=0.48\text{ A}$	0.4	0.8	-	S

¹⁾ Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm² (single layer, 70 μm thick) copper area for drain connection. PCB is vertical in still air.

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

Dynamic characteristics

Input capacitance	C_{iss}	$V_{GS} = -3\text{ V}$, $V_{DS} = 25\text{ V}$, $f = 1\text{ MHz}$	-	326	430	pF
Output capacitance	C_{oss}		-	41	55	
Reverse transfer capacitance	C_{rss}		-	17	25	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 100\text{ V}$, $V_{GS} = -2 \dots -7\text{ V}$, $I_D = 0.50\text{ A}$, $R_G = 6\text{ }\Omega$	-	5.1	7.7	ns
Rise time	t_r		-	3.4	5.1	
Turn-off delay time	$t_{d(off)}$		-	45	68	
Fall time	t_f		-	21	31	

Gate Charge Characteristics

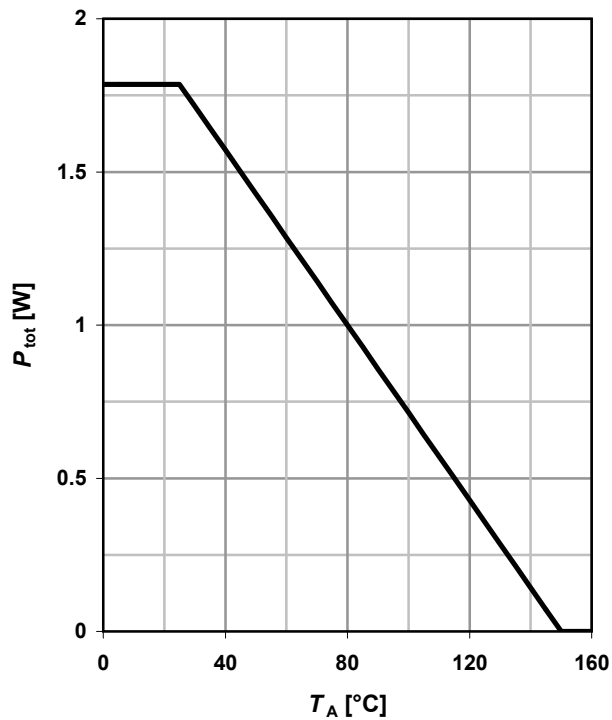
Gate to source charge	Q_{gs}	$V_{DD} = 160\text{ V}$, $I_D = 0.05\text{ A}$, $V_{GS} = -3\text{ to }5\text{ V}$	-	0.74	1.0	nC
Gate to drain charge	Q_{gd}		-	5.6	8.4	
Gate charge total	Q_g		-	11	14	
Gate plateau voltage	$V_{plateau}$		-	0.16	-	V

Reverse Diode

Diode continuous forward current	I_S	$T_A = 25\text{ }^\circ\text{C}$	-	-	0.66	A
Diode pulse current	$I_{S,pulse}$		-	-	2.6	
Diode forward voltage	V_{SD}	$V_{GS} = -3\text{ V}$, $I_F = 0.66\text{ A}$, $T_J = 25\text{ }^\circ\text{C}$	-	0.9	1.2	V
Reverse recovery time	t_{rr}	$V_R = 100\text{ V}$, $I_F = 0.5\text{ A}$, $di_F/dt = 100\text{ A}/\mu\text{s}$	-	42	65	ns
Reverse recovery charge	Q_{rr}		-	60	90	nC

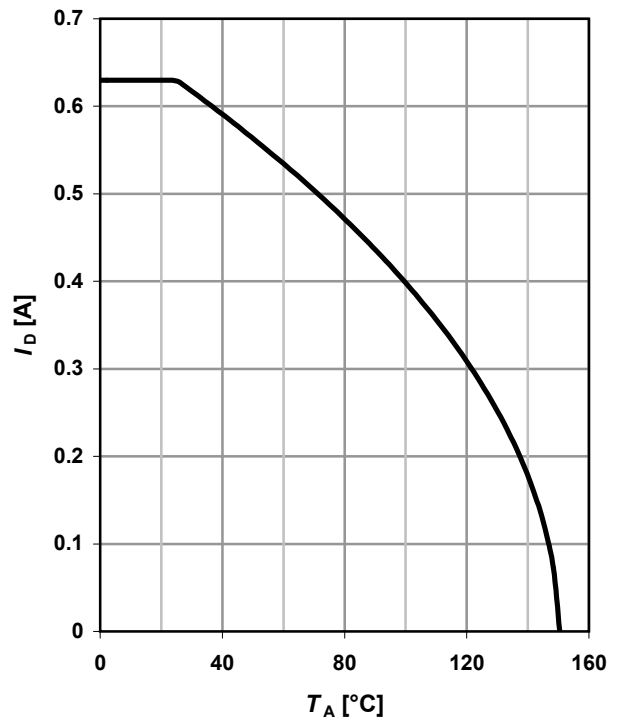
1 Power dissipation

$$P_{\text{tot}} = f(T_A)$$



2 Drain current

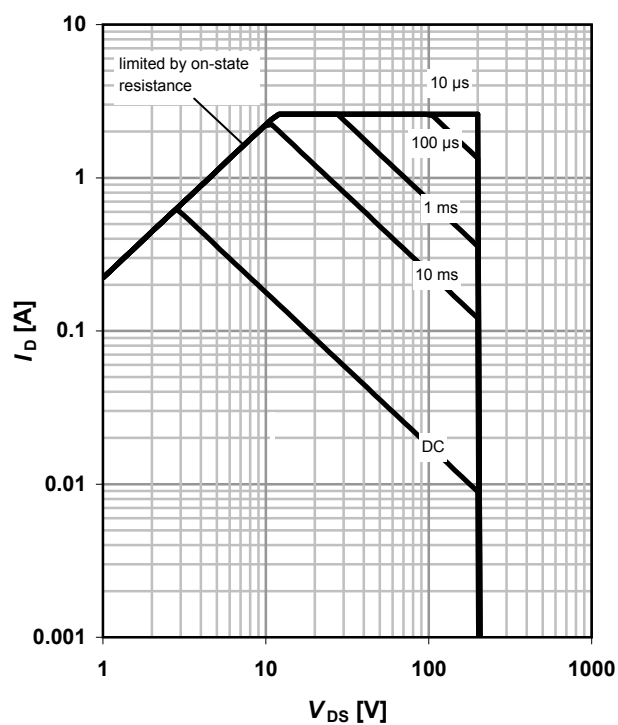
$$I_D = f(T_A); V_{\text{GS}} \geq 10 \text{ V}$$



3 Safe operation area

$$I_D = f(V_{\text{DS}}); T_A = 25^\circ\text{C}; D = 0$$

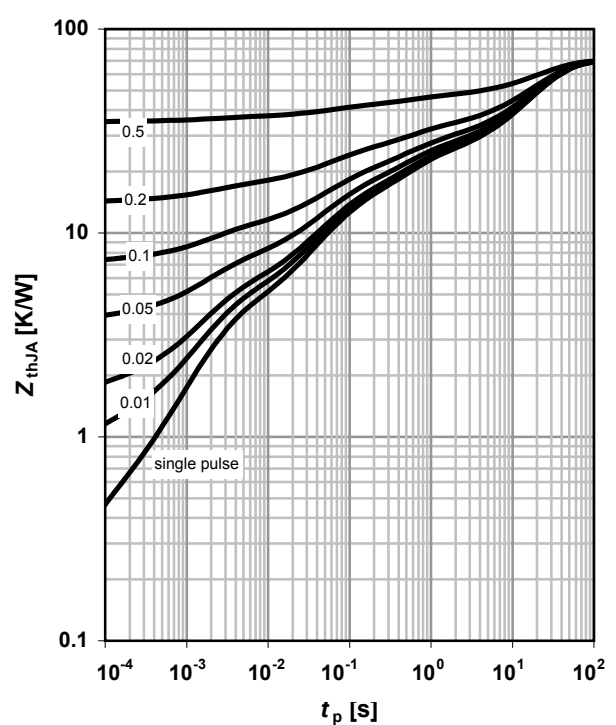
parameter: t_p



4 Max. transient thermal impedance

$$Z_{\text{thJA}} = f(t_p)$$

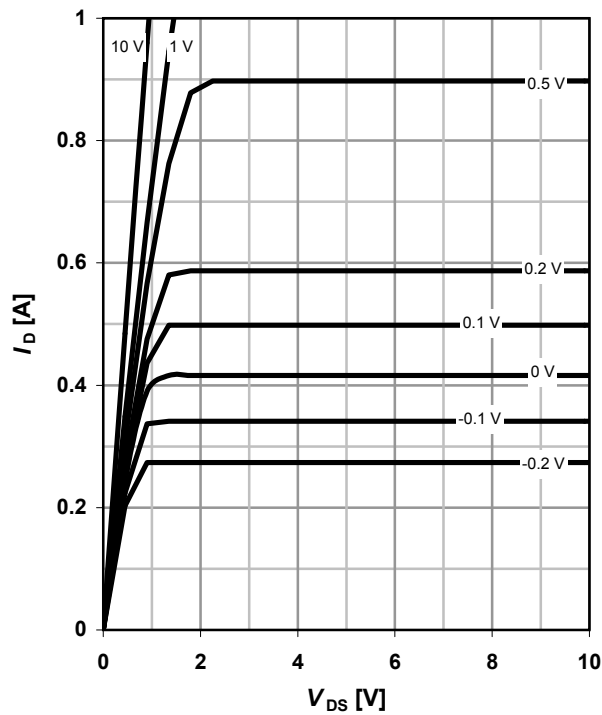
parameter: $D = t_p/T$



5 Typ. output characteristics

$$I_D = f(V_{DS}); T_j = 25^\circ\text{C}$$

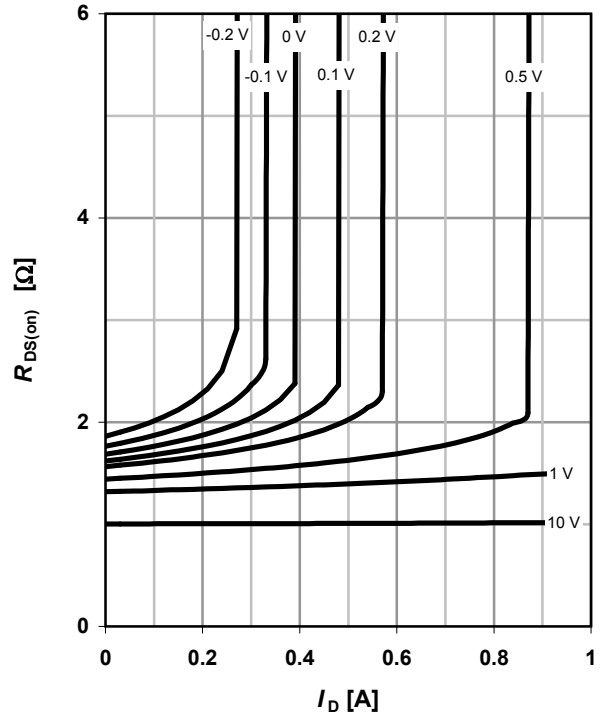
parameter: V_{GS}



6 Typ. drain-source on resistance

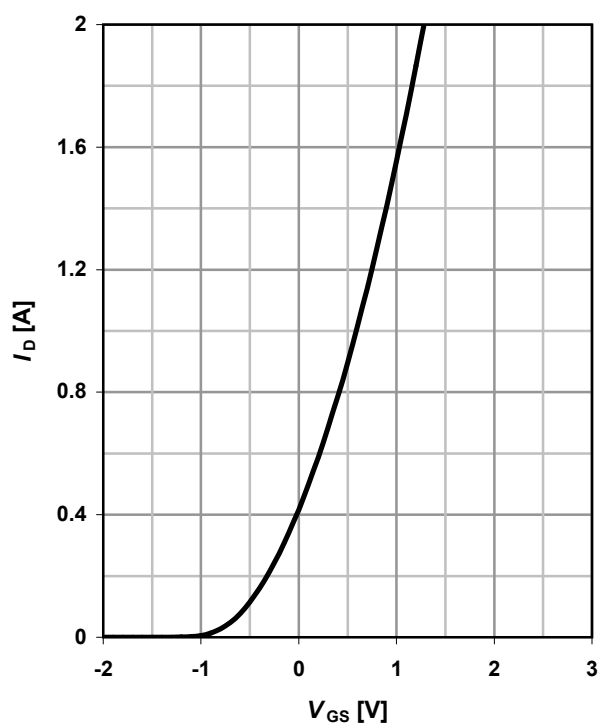
$$R_{DS(on)} = f(I_D); T_j = 25^\circ\text{C}$$

parameter: V_{GS}



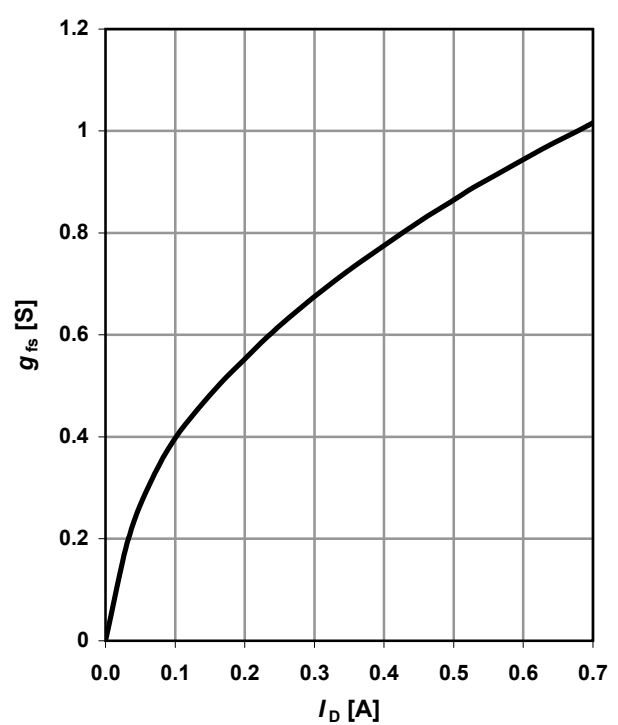
7 Typ. transfer characteristics

$$I_D = f(V_{GS}); |V_{DS}| > 2|I_D|R_{DS(on)max}$$



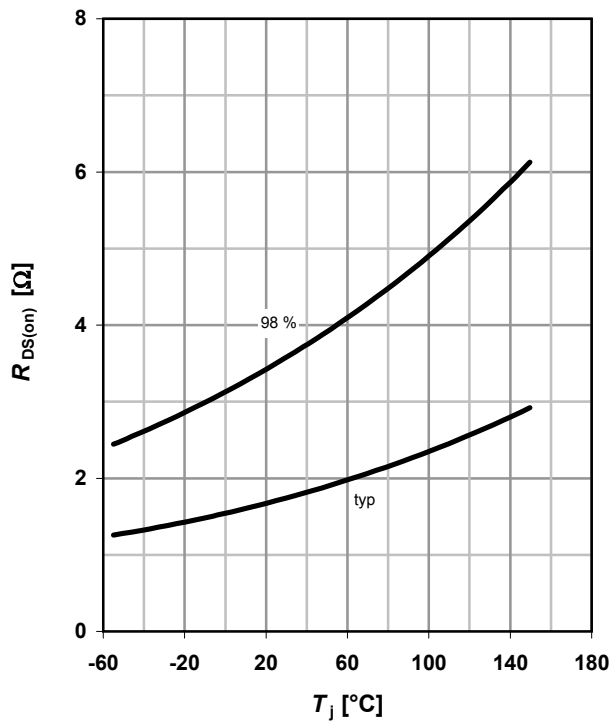
8 Typ. forward transconductance

$$g_{fs} = f(I_D); T_j = 25^\circ\text{C}$$



9 Drain-source on-state resistance

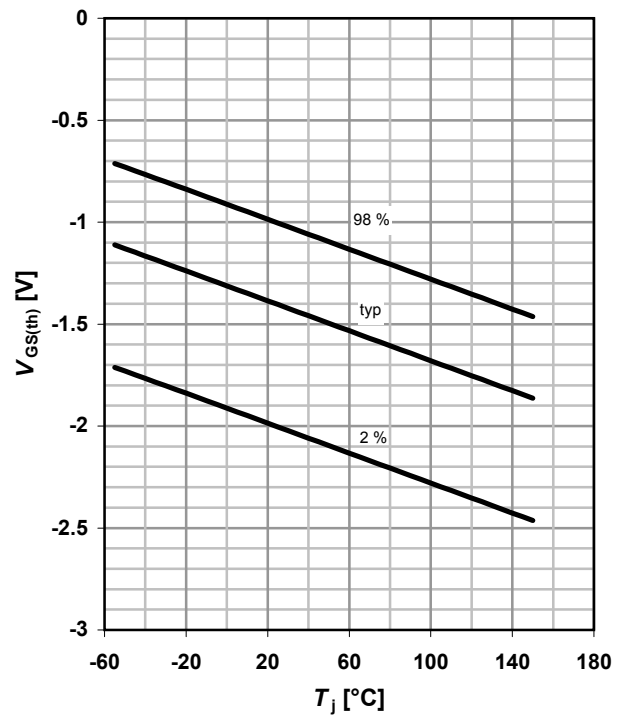
$$R_{DS(on)} = f(T_j); I_D = 0.07 \text{ A}; V_{GS} = 0 \text{ V}$$



10 Typ. gate threshold voltage

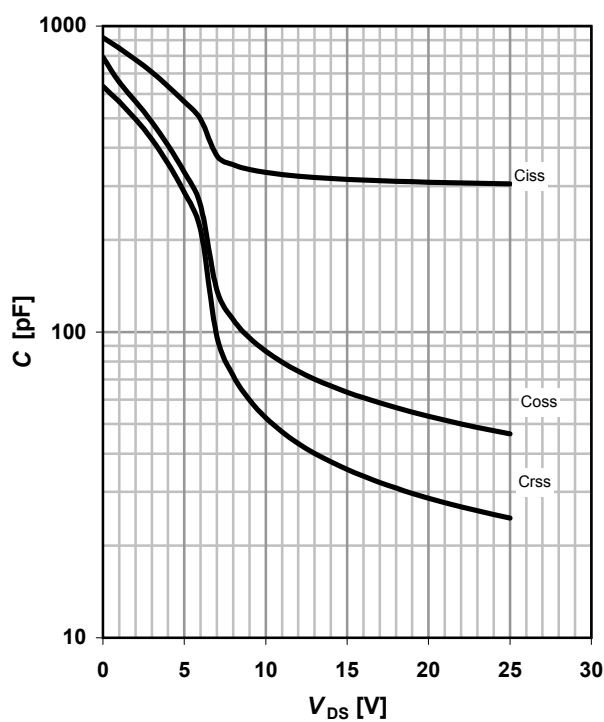
$$V_{GS(th)} = f(T_j); V_{DS} = 3 \text{ V}; I_D = 400 \text{ μA}$$

parameter: I_D



11 Typ. Capacitances

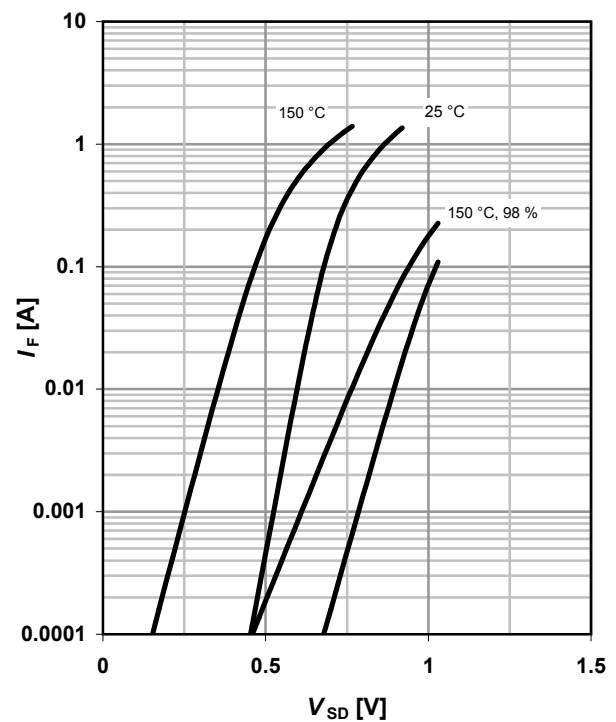
$$C = f(V_{DS}); V_{GS} = -3 \text{ V}; f = 1 \text{ MHz}$$



12 Forward characteristics of reverse diode

$$I_F = f(V_{SD})$$

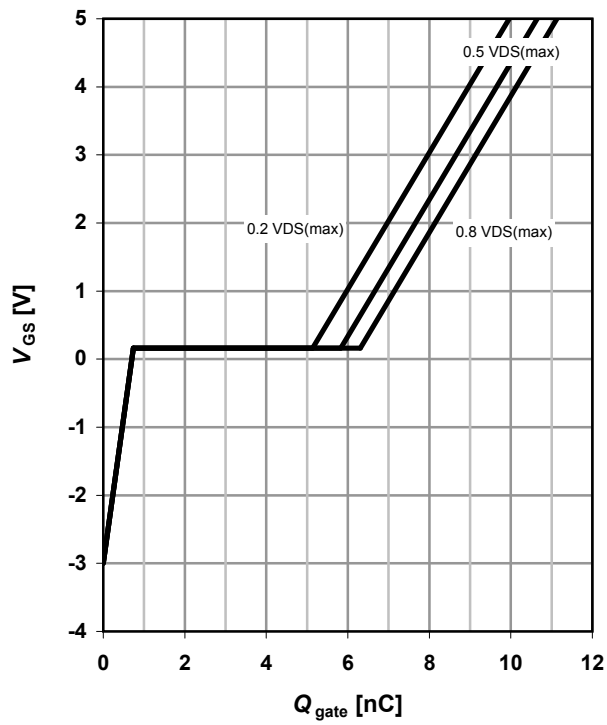
parameter: T_j



14 Typ. gate charge

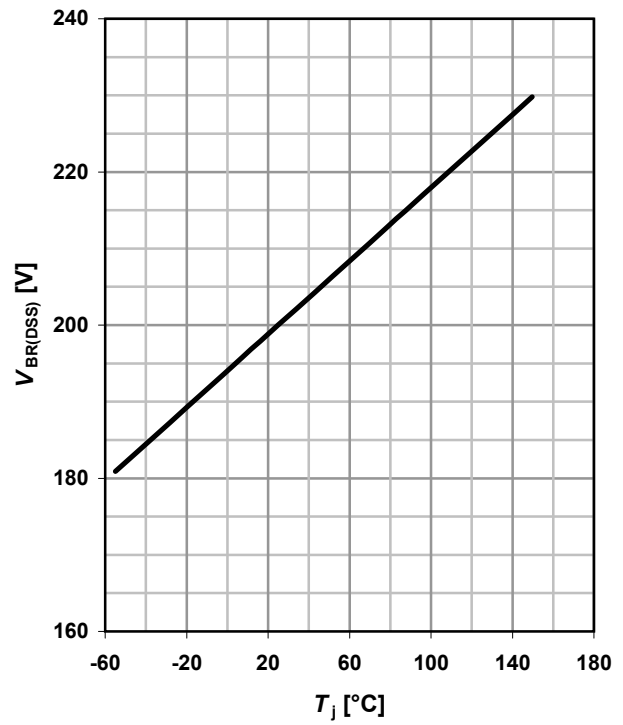
$V_{GS}=f(Q_{gate}); I_D=0.5\text{ A pulsed}$

parameter: V_{DD}



15 Drain-source breakdown voltage

$V_{BR(DSS)}=f(T_j); I_D=250\text{ }\mu\text{A}$



Technical drawing of a mechanical part, showing front and side views with dimensions and tolerances.

Front View Dimensions:

- Overall width: 6.5 ± 0.2
- Top flange width: 3 ± 0.1
- Top flange height: 4
- Bottom flange width: 4.6
- Bottom flange height: 2.3
- Distance between bottom flange centers: 0.7 ± 0.1
- Reference points A and B are marked on the left side.

Side View Dimensions:

- Overall height: 7 ± 0.3
- Top flange thickness: 1.6 ± 0.1
- Top flange chamfer: 0.1 max
- Top flange chamfer angle: 15° max
- Top flange width: 3.5 ± 0.2
- Bottom flange thickness: 0.5 min
- Bottom flange width: 0.28 ± 0.04

Surface Finish:

- Top surface: $\text{Ra } 0.25 \text{ (M) A}$
- Bottom surface: $\text{Ra } 0.25 \text{ (M) B}$

Material and Tolerances:

- Material: $\text{+0.2 acc. to DIN 6784}$

Technical drawing of the 1000 Series LED strip light showing top and side views with dimensions:

- Top View Dimensions:**
 - Overall width: 12 mm
 - Distance between mounting holes: 8 mm
 - Distance between LED modules: 6.8 mm
 - Module height: 7.55 mm
- Side View Dimensions:**
 - Maximum thickness: 0.3 mm
 - Mounting tab width: 1.75 mm

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