

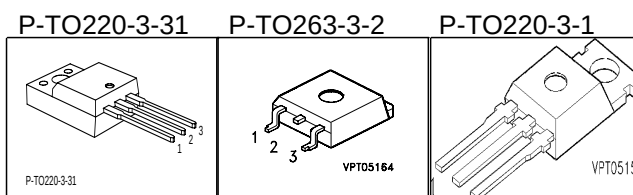
Cool MOS™ Power Transistor

Feature

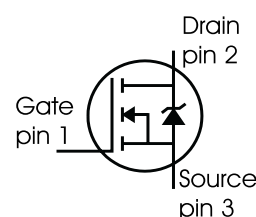
- New revolutionary high voltage technology
- Ultra low gate charge
- Periodic avalanche rated
- Extreme dv/dt rated
- Ultra low effective capacitances

Product Summary

$V_{DS} @ T_{jmax}$	650	V
$R_{DS(on)}$	0.38	Ω
I_D	11	A



Type	Package	Ordering Code	Marking
SPP11N60C2	P-TO220-3-1	Q67040-S4295	11N60C2
SPB11N60C2	P-TO263-3-2	Q67040-S4298	11N60C2
SPA11N60C2	P-TO220-3-31	Q67040-S4332	11N60C2



Maximum Ratings

Parameter	Symbol	Value		Unit
		SPP_B	SPA	
Continuous drain current	I_D			A
$T_C = 25\text{ °C}$		11	11 ¹⁾	
$T_C = 100\text{ °C}$		7	7 ¹⁾	
Pulsed drain current, t_p limited by T_{jmax}	$I_{D\text{ puls}}$	22	22	A
Avalanche energy, single pulse	E_{AS}	340	340	mJ
$I_D=5.5\text{ A}$, $V_{DD}=50\text{ V}$				
Avalanche energy, repetitive t_{AR} limited by T_{jmax} ²⁾	E_{AR}	0.6	0.6	
$I_D=11\text{ A}$, $V_{DD}=50\text{ V}$				
Avalanche current, repetitive t_{AR} limited by T_{jmax}	I_{AR}	11	11	A
Reverse diode dv/dt	dv/dt	6	6	V/ns
$I_S = 11\text{ A}$, $V_{DS} < V_{DD}$, di/dt=100A/μs, $T_{jmax}=150\text{ °C}$				
Gate source voltage	V_{GS}	±20	±20	V
Gate source voltage AC (f >1Hz)	V_{GS}	±30	±30	
Power dissipation, $T_C = 25\text{ °C}$	P_{tot}	125	33	W
Operating and storage temperature	T_j, T_{stg}	-55...+150		°C

Thermal Characteristics

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Characteristics					
Thermal resistance, junction - case	R_{thJC}	-	-	1	K/W
Thermal resistance, junction - case, FullPAK	$R_{thJC \text{ FP}}$	-	-	3.8	
Thermal resistance, junction - ambient, leaded	R_{thJA}	-	-	62	
Thermal resistance, junction - ambient, FullPAK	$R_{thJA \text{ FP}}$	-	-	80	
SMD version, device on PCB: @ min. footprint @ 6 cm ² cooling area ³⁾	R_{thJA}	- -	- 35	62 -	
Linear derating factor		-	-	1	W/K
Linear derating factor, FullPAK		-	-	0.26	
Soldering temperature, 1.6 mm (0.063 in.) from case for 10s	T_{sold}	-	-	260	°C

Electrical Characteristics, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Static Characteristics

Drain-source breakdown voltage $V_{GS}=0\text{V}$, $I_D=0.25\text{mA}$	$V_{(BR)DSS}$	600	-	-	V
Drain-source avalanche breakdown voltage $V_{GS}=0\text{V}$, $I_D=11\text{A}$	$V_{(BR)DS}$	-	700	-	
Gate threshold voltage, $V_{GS} = V_{DS}$ $I_D=0.5\text{mA}$	$V_{GS(th)}$	3.5	4.5	5.5	
Zero gate voltage drain current $V_{DS} = 600\text{V}$, $V_{GS} = 0\text{V}$, $T_j = 25^\circ\text{C}$ $V_{DS} = 600\text{V}$, $V_{GS} = 0\text{V}$, $T_j = 150^\circ\text{C}$	I_{DSS}	- -	- -	25 250	μA
Gate-source leakage current $V_{GS}=20\text{V}$, $V_{DS}=0\text{V}$	I_{GSS}	-	-	100	
Drain-source on-state resistance $V_{GS}=10\text{V}$, $I_D=7\text{A}$, $T_j=25^\circ\text{C}$	$R_{DS(on)}$	-	0.34	0.38	Ω
Gate input resistance $f = 1\text{MHz}$, open drain	R_G	-	0.86	-	

Electrical Characteristics

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

Characteristics

Transconductance	g_{fs}	$V_{DS} \geq 2 \cdot I_D \cdot R_{DS(on)max}$, $I_D = 7A$	3	6	-	S
Input capacitance	C_{iss}	$V_{GS} = 0V$, $V_{DS} = 25V$, $f = 1MHz$	-	1460	-	pF
Output capacitance	C_{oss}		-	610	-	
Reverse transfer capacitance	C_{rss}		-	21	-	
Effective output capacitance, ⁴⁾ energy related	$C_{o(er)}$	$V_{GS} = 0V$, $V_{DS} = 0V$ to $480V$	-	45	-	
Effective output capacitance, ⁵⁾ time related	$C_{o(tr)}$		-	85	-	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 380V$, $V_{GS} = 0/13V$, $I_D = 11A$, $R_G = 6.8\Omega$, $T_J = 125^\circ C$	-	13	-	ns
Rise time	t_r		-	40	-	
Turn-off delay time	$t_{d(off)}$		-	48	72	
Fall time	t_f		-	9	13.5	

Gate Charge Characteristics

Gate to source charge	Q_{gs}	$V_{DD} = 350V$, $I_D = 11A$	-	10.5	-	nC
Gate to drain charge	Q_{gd}		-	24	-	
Gate charge total	Q_g	$V_{DD} = 350V$, $I_D = 11A$, $V_{GS} = 0$ to $10V$	-	41.5	54	
Gate plateau voltage	$V_{(plateau)}$	$V_{DD} = 350V$, $I_D = 11A$	-	8	-	V

¹Limited only by maximum temperature

²Repetitive avalanche causes additional power losses that can be calculated as $P_{AV} = E_{AR} \cdot f$.

³Device on 40mm*40mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70 μm thick) copper area for drain connection. PCB is vertical without blown air.

⁴ $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 80% V_{DSS} .

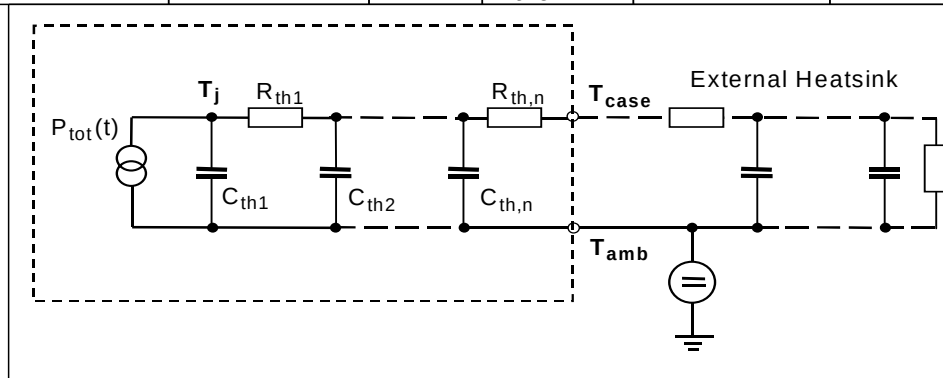
⁵ $C_{o(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 to 80% V_{DSS} .

Electrical Characteristics

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Characteristics						
Inverse diode continuous forward current	I_S	$T_C=25^{\circ}\text{C}$	-	-	11	A
Inverse diode direct current, pulsed	I_{SM}		-	-	22	
Inverse diode forward voltage	V_{SD}	$V_{GS}=0\text{V}, I_F=I_S$	-	1	1.2	V
Reverse recovery time	t_{rr}	$V_R=350\text{V}, I_F=I_S$, $di_F/dt=100\text{A}/\mu\text{s}$	-	650	1105	ns
Reverse recovery charge	Q_{rr}		-	7.9	-	μC
Peak reverse recovery current	I_{rrm}		-	30	-	A
Peak rate of fall of reverse recovery current	di_{rr}/dt	$T_j=25^{\circ}\text{C}$	-	600	-	$\text{A}/\mu\text{s}$

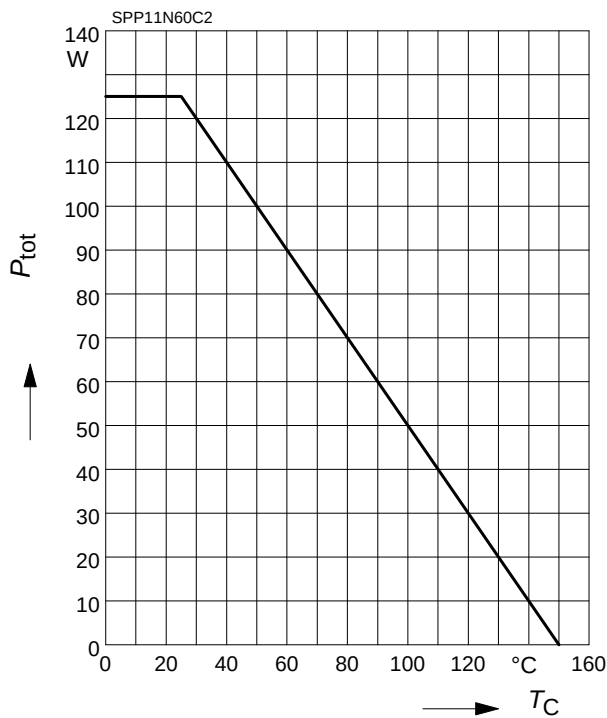
Typical Transient Thermal Characteristics

Symbol	Value		Unit	Symbol	Value		Unit
	SPP_B	SPA			SPP_B	SPA	
R_{th1}	0.015	0.015	K/W	C_{th1}	0.0002121	0.00012	Ws/K
R_{th2}	0.034	0.03		C_{th2}	0.0007091	0.000455	
R_{th3}	0.042	0.043		C_{th3}	0.001184	0.000638	
R_{th4}	0.116	0.119		C_{th4}	0.001527	0.00144	
R_{th5}	0.149	0.35		C_{th5}	0.011	0.00737	
R_{th6}	0.059	2.499		C_{th6}	0.089	0.412	



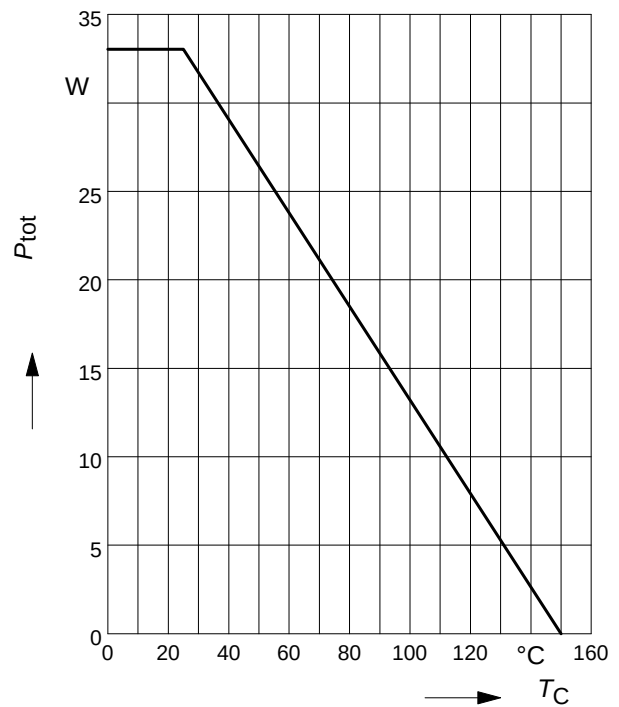
1 Power dissipation

$$P_{\text{tot}} = f(T_C)$$



2 Power dissipation FullPAK

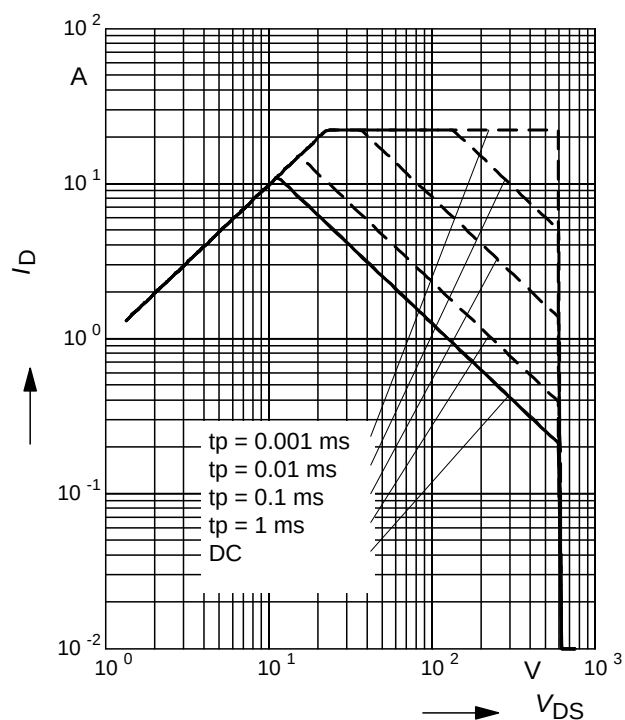
$$P_{\text{tot}} = f(T_C)$$



3 Safe operating area

$$I_D = f(V_{DS})$$

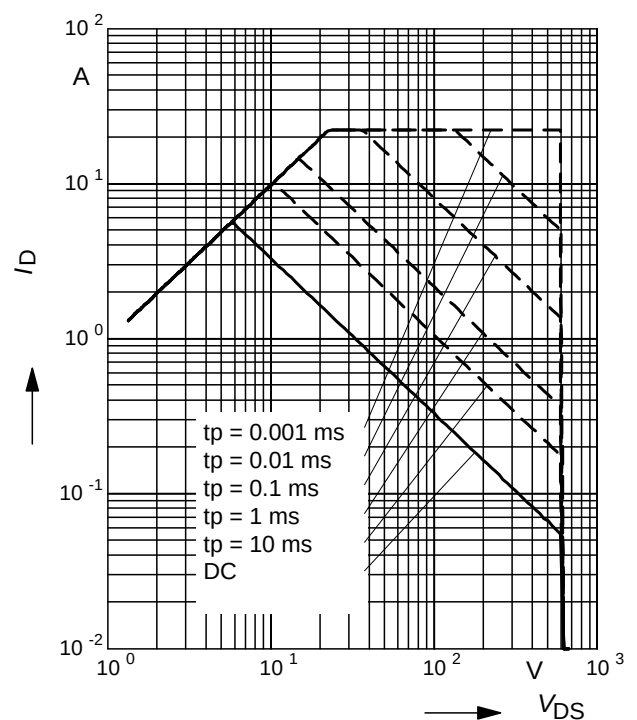
parameter: $D = 0$, $T_C = 25^\circ\text{C}$



4 Safe operating area FullPAK

$$I_D = f(V_{DS})$$

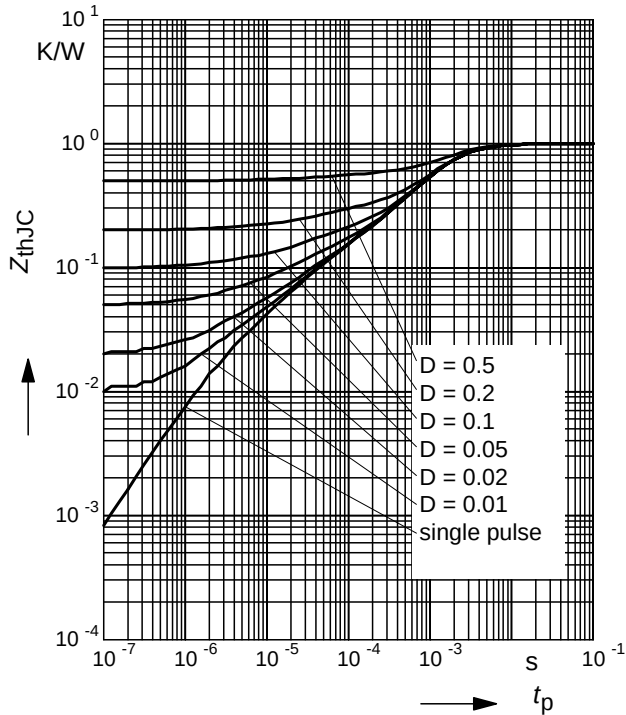
parameter: $D = 0$, $T_C = 25^\circ\text{C}$



5 Transient thermal impedance

$$Z_{thJC} = f(t_p)$$

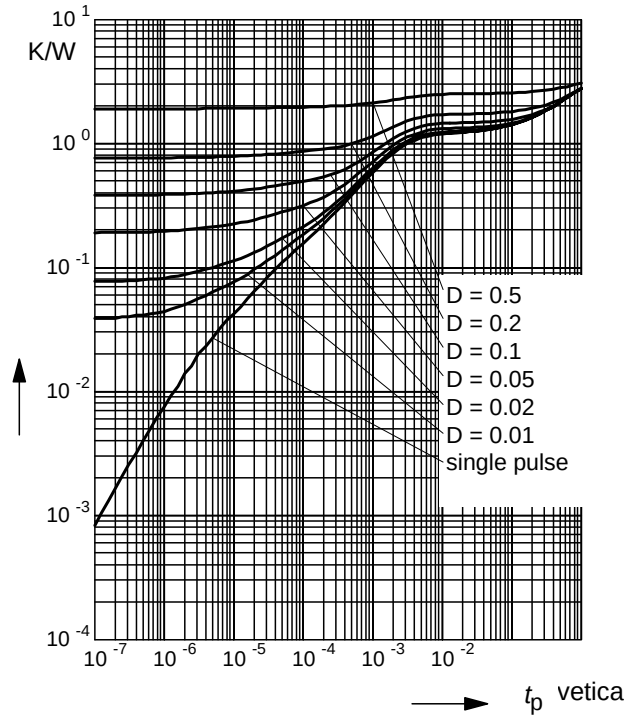
parameter: $D = t_p/T$



6 Transient thermal impedance FullPAK

$$Z_{thJC} = f(t_p)$$

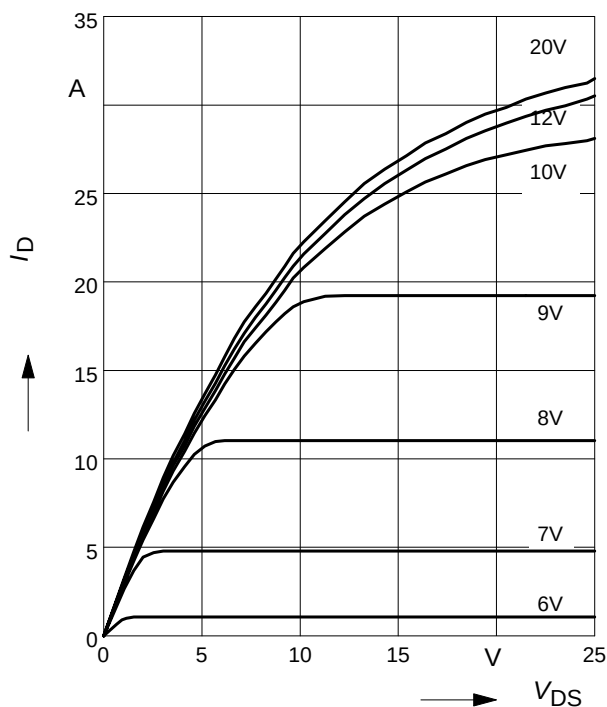
parameter: $D = t_p/t$



7 Typ. output characteristic

$$I_D = f(V_{DS}); T_j = 25^\circ\text{C}$$

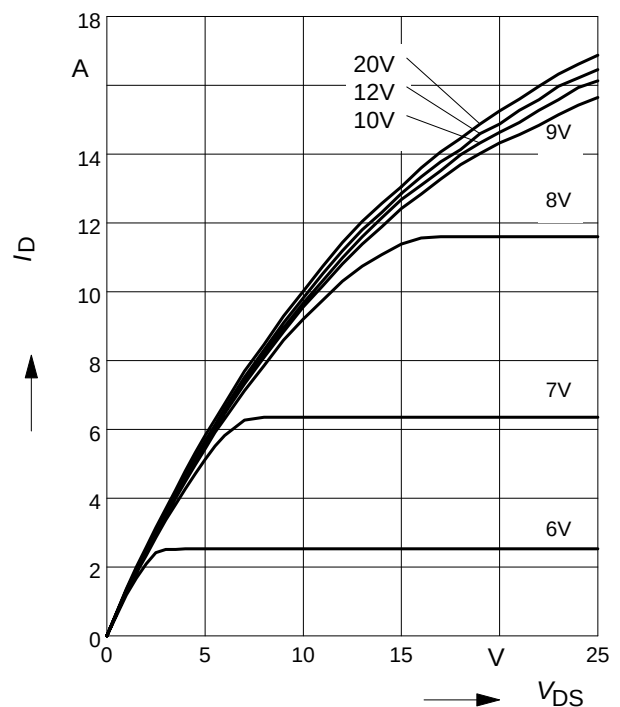
parameter: $t_p = 10 \mu\text{s}, V_{GS}$



8 Typ. output characteristic

$$I_D = f(V_{DS}); T_j = 150^\circ\text{C}$$

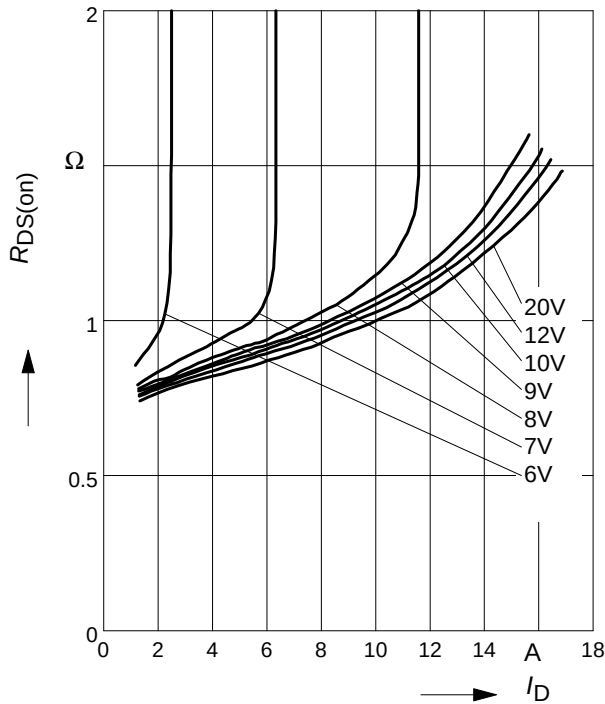
parameter: $t_p = 10 \mu\text{s}, V_{GS}$



9 Typ. drain-source on resistance

$$R_{DS(on)} = f(I_D)$$

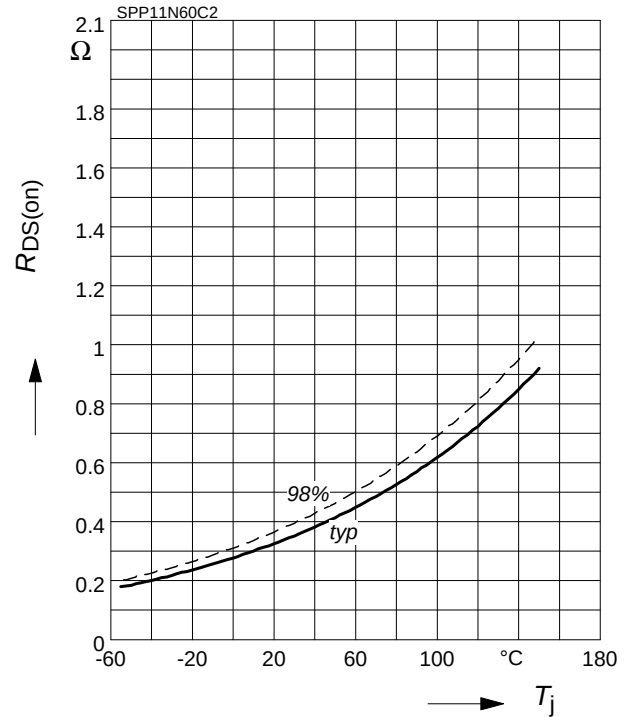
parameter: $T_j = 150^\circ\text{C}$, V_{GS}



10 Drain-source on-state resistance

$$R_{DS(on)} = f(T_j)$$

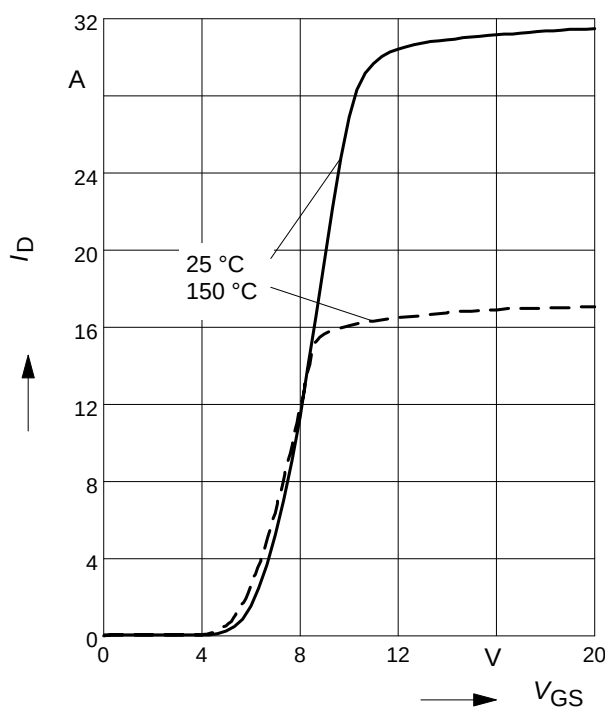
parameter: $I_D = 7\text{ A}$, $V_{GS} = 10\text{ V}$



11 Typ. transfer characteristics

$$I_D = f(V_{GS}); V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$$

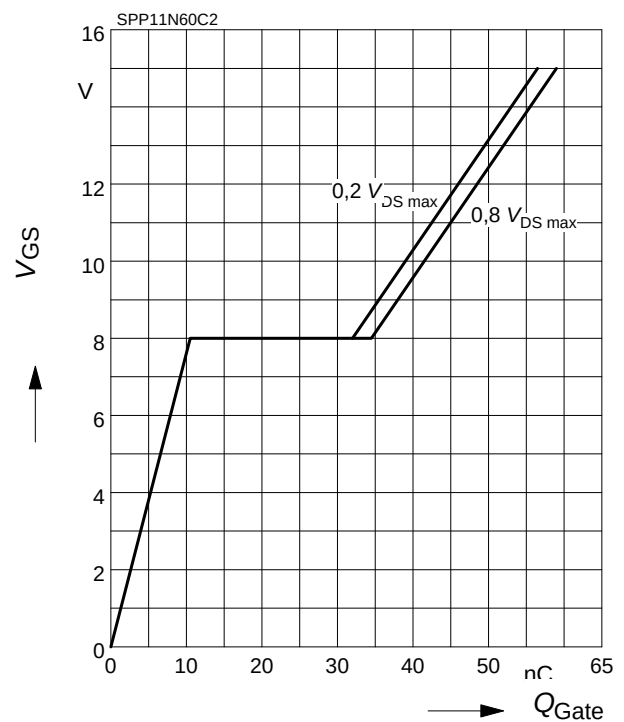
parameter: $t_p = 10\text{ }\mu\text{s}$



12 Typ. gate charge

$$V_{GS} = f(Q_{Gate})$$

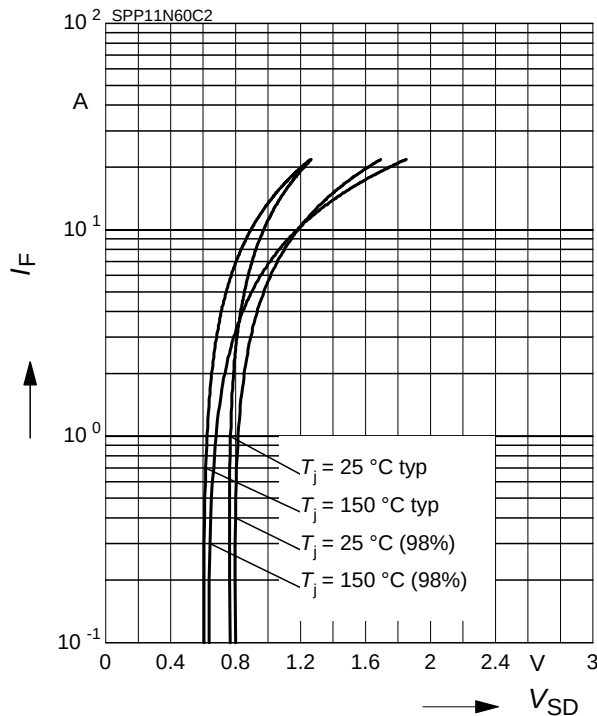
parameter: $I_D = 11\text{ A}$ pulsed



13 Forward characteristics of body diode

$$I_F = f(V_{SD})$$

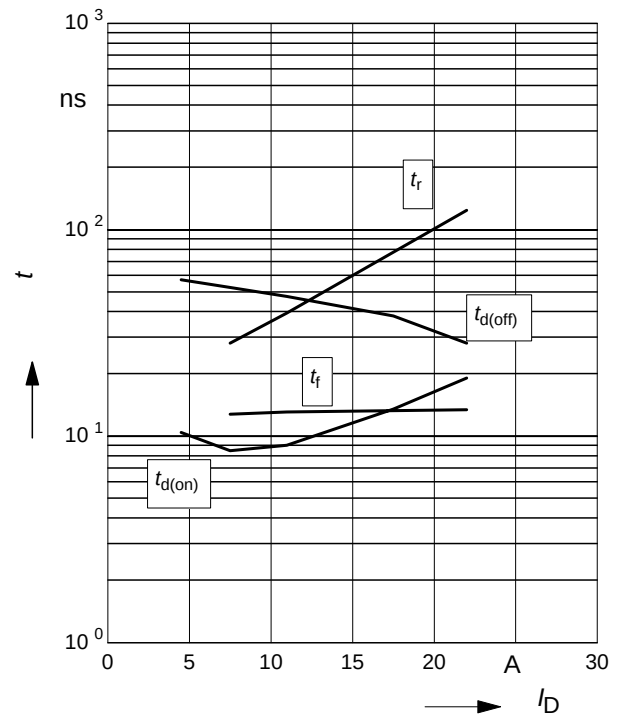
parameter: T_j , $t_p = 10 \mu s$



14 Typ. switching time

$$t = f(I_D), \text{ inductive load, } T_j=125^\circ\text{C}$$

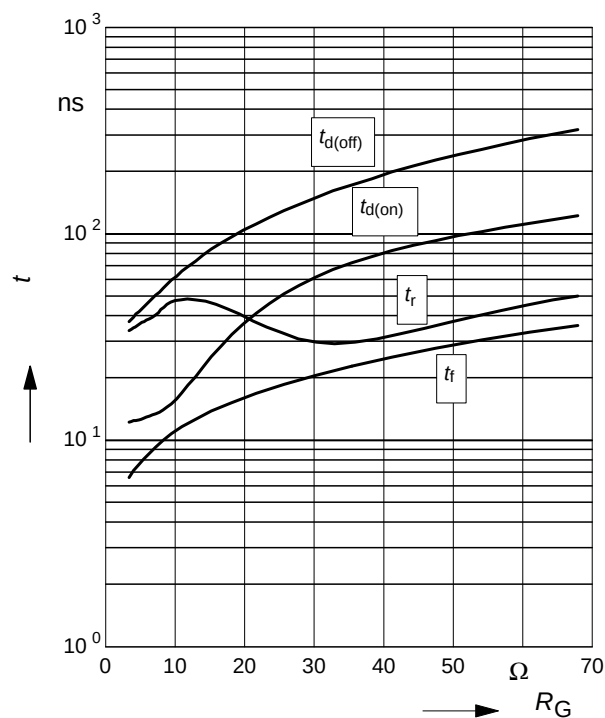
par.: $V_{DS}=380\text{V}$, $V_{GS}=0/+13\text{V}$, $R_G=6.8\Omega$



15 Typ. switching time

$$t = f(R_G), \text{ inductive load, } T_j=125^\circ\text{C}$$

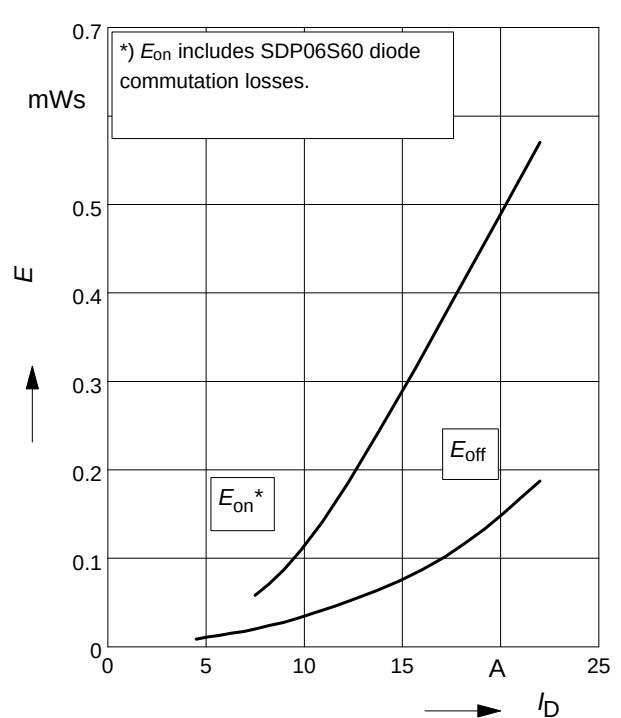
par.: $V_{DS}=380\text{V}$, $V_{GS}=0/+13\text{V}$, $I_D=11\text{ A}$



16 Typ. switching losses

$$E = f(I_D), \text{ inductive load, } T_j=125^\circ\text{C}$$

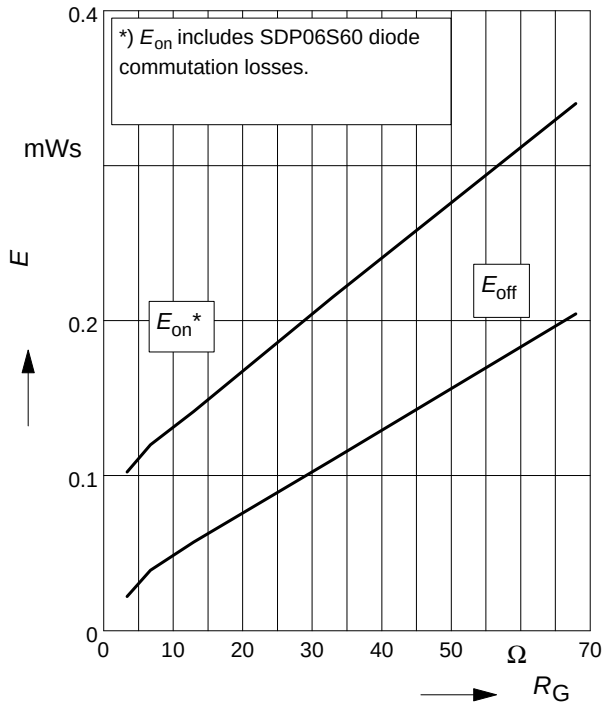
par.: $V_{DS}=380\text{V}$, $V_{GS}=0/+13\text{V}$, $R_G=6.8\Omega$



17 Typ. switching losses

$E = f(R_G)$, inductive load, $T_j = 125^\circ\text{C}$

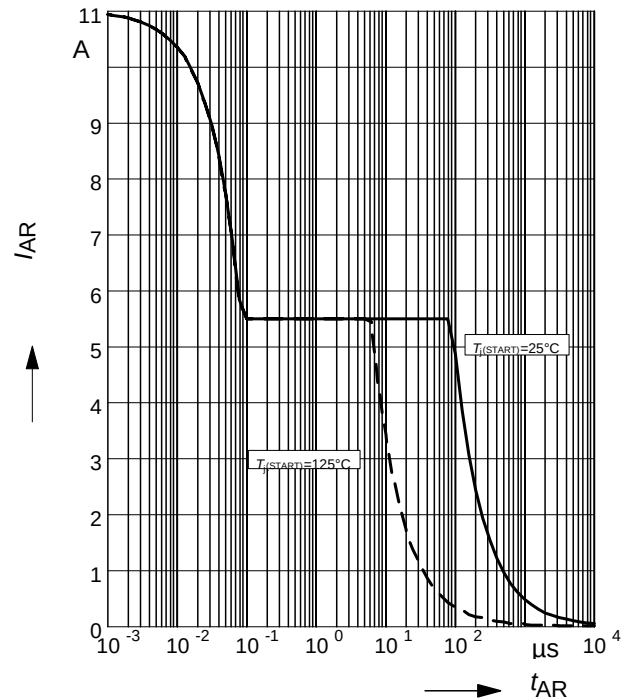
par.: $V_{DS} = 380\text{V}$, $V_{GS} = 0/+13\text{V}$, $I_D = 11\text{A}$



18 Avalanche SOA

$I_{AR} = f(t_{AR})$

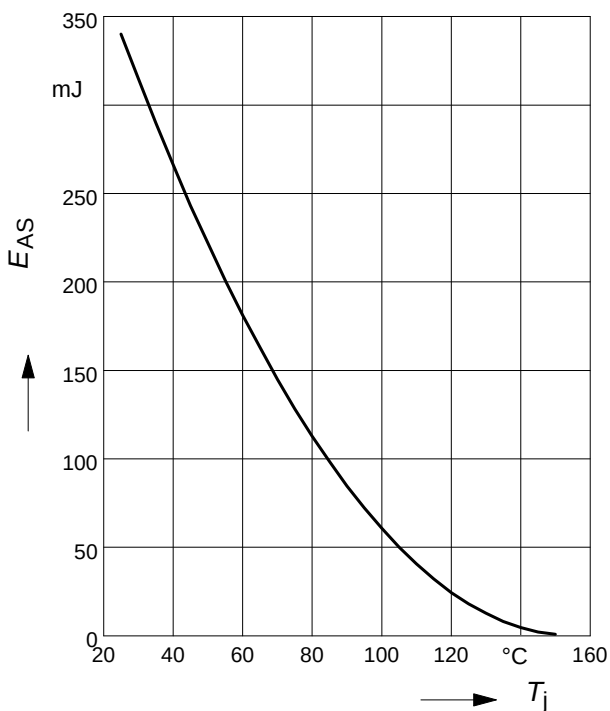
par.: $T_j \leq 150^\circ\text{C}$



19 Avalanche energy

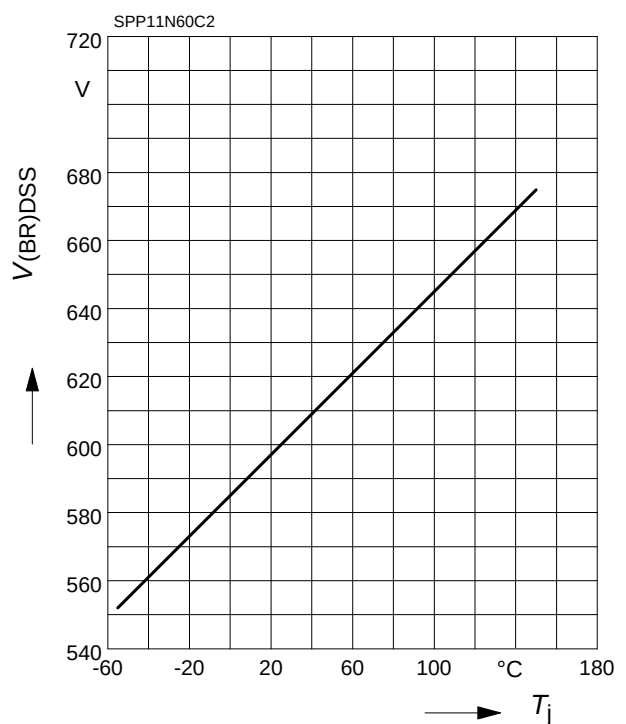
$E_{AS} = f(T_j)$

par.: $I_D = 5.5\text{A}$, $V_{DD} = 50\text{V}$



20 Drain-source breakdown voltage

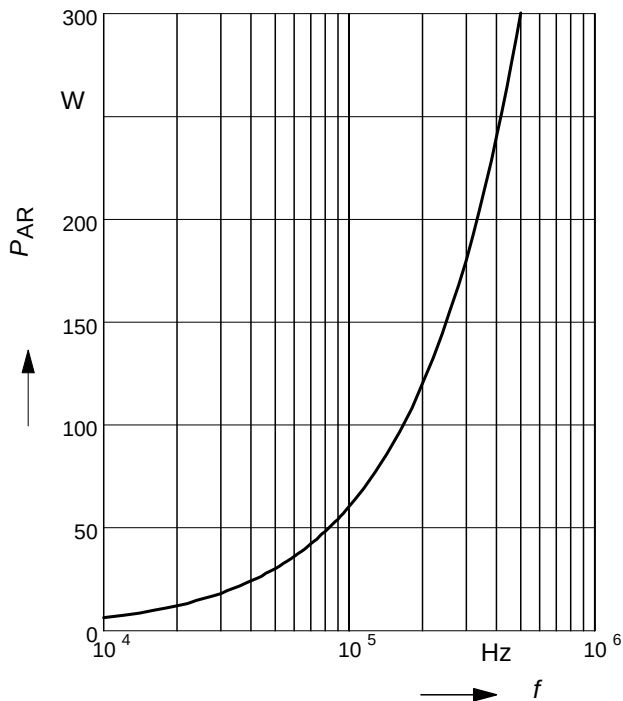
$V_{(BR)DSS} = f(T_j)$



21 Avalanche power losses

$$P_{AR} = f(f)$$

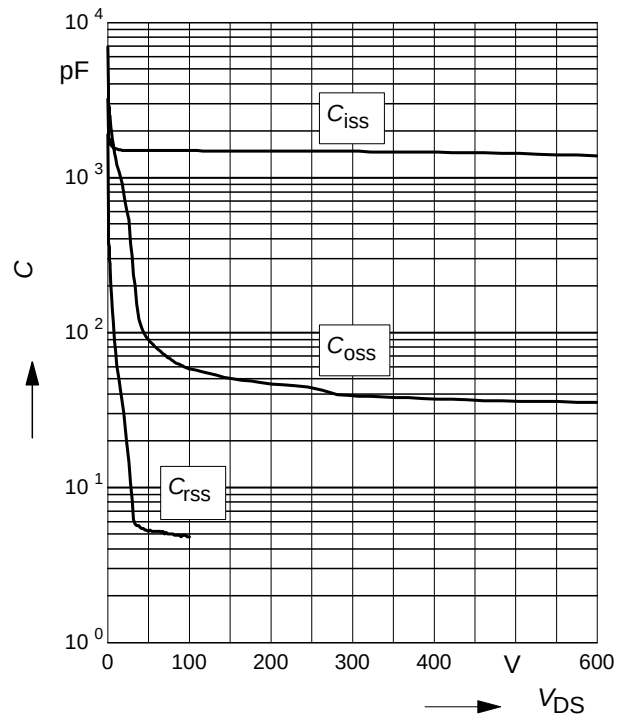
parameter: $E_{AR}=0.6\text{mJ}$



22 Typ. capacitances

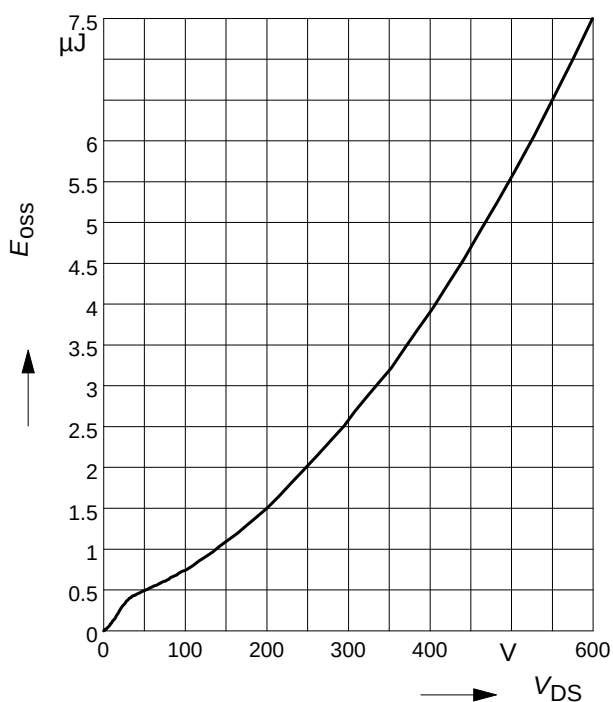
$$C = f(V_{DS})$$

parameter: $V_{GS}=0\text{V}$, $f=1\text{ MHz}$

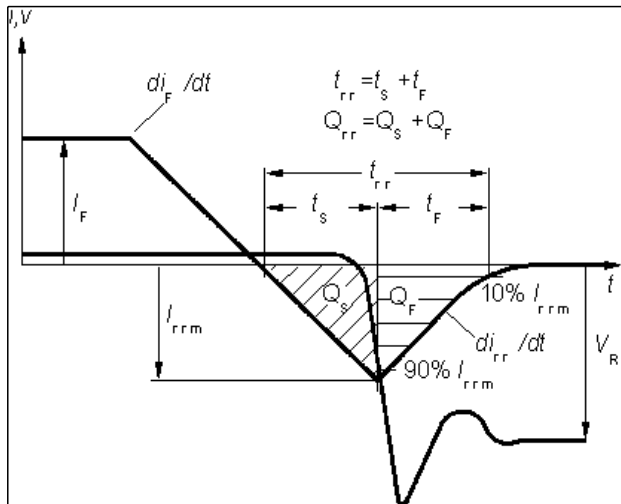


23 Typ. C_{oss} stored energy

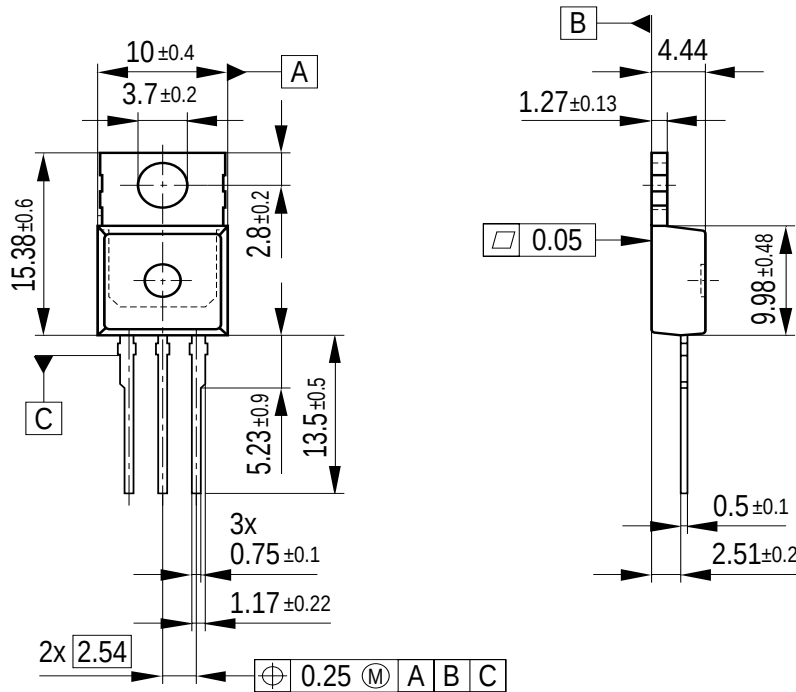
$$E_{oss}=f(V_{DS})$$



Definition of diodes switching characteristics

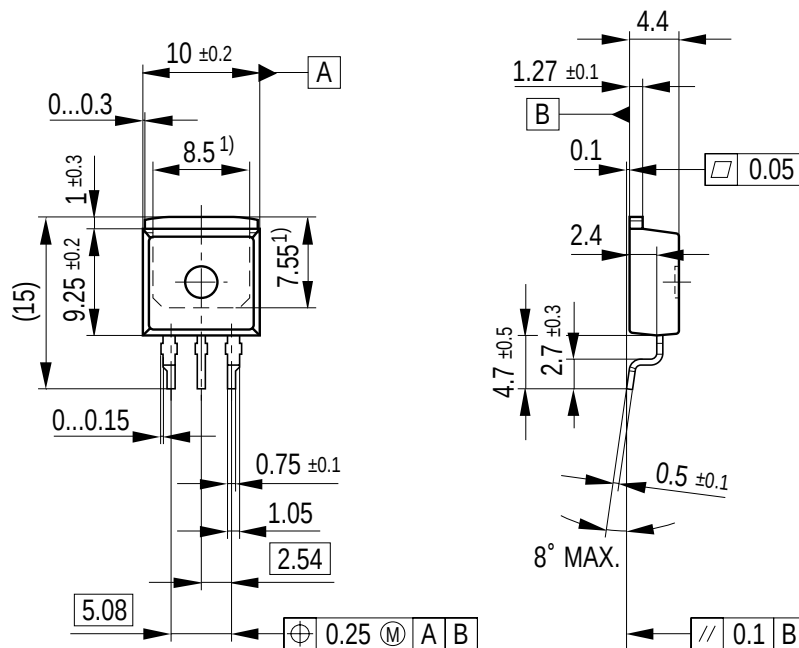


P-TO-220-3-1



All metal surfaces tin plated, except area of cut.
Metal surface min. x=7.25, y=12.3

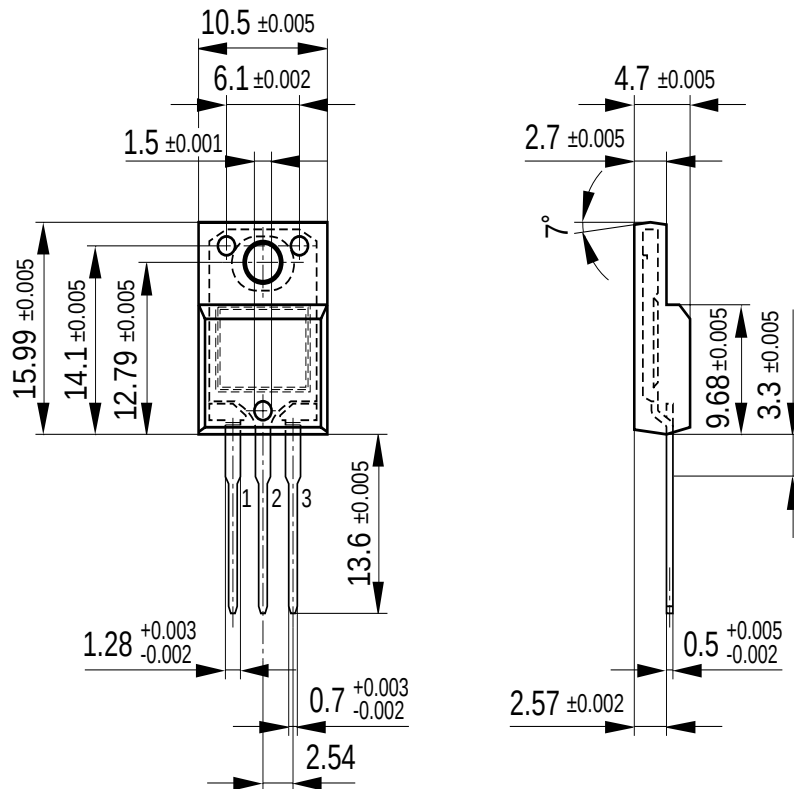
P-TO-263-3-1 (D²-PAK)



¹⁾ Typical

All metal surfaces: tin plated, except area of cut.
Metal surface min. x=7.25, y=6.9

P-TO-220-3-31 (FullPAK)



Please refer to mounting instructions (application note AN-TO220-3-31-01)

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