



STP9NC60 STP9NC60FP

N-CHANNEL 600V - 0.6Ω - 9A - TO-220/TO-220FP
PowerMesh™ II MOSFET

TYPE	V _{DSS}	R _{DS(on)}	I _D
STP9NC60	600 V	< 0.75 Ω	9.0 A
STP9NC60FP	600 V	< 0.75 Ω	9.0 A (*)

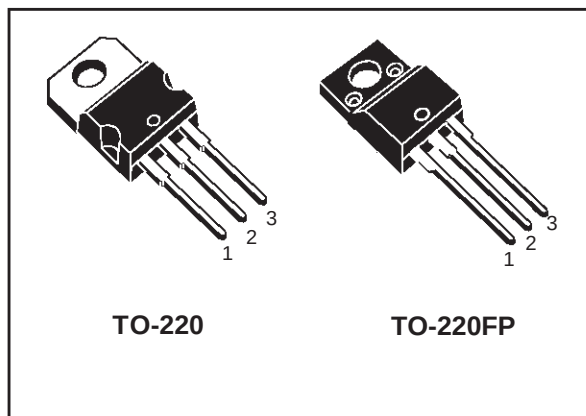
- TYPICAL R_{DS(on)} = 0.6 Ω
- EXTREMELY HIGH dv/dt CAPABILITY
- 100% AVALANCHE TESTED
- NEW HIGH VOLTAGE BENCHMARK
- GATE CHARGE MINIMIZED

DESCRIPTION

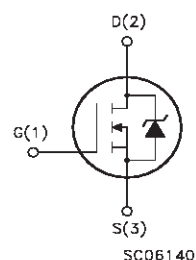
The PowerMESH™ II is the evolution of the first generation of MESH OVERLAY™. The layout refinements introduced greatly improve the Ron*area figure of merit while keeping the device at the leading edge for what concerns switching speed, gate charge and ruggedness.

APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- SWITCH MODE POWER SUPPLIES (SMPS)
- DC-AC CONVERTERS FOR WELDING EQUIPMENT AND UNINTERRUPTIBLE POWER SUPPLIES AND MOTOR DRIVER



INTERNAL SCHEMATIC DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value		Unit
		STP9NC60	STP9NC60FP	
V _{DS}	Drain-source Voltage (V _{GS} = 0)	600		V
V _{DGR}	Drain-gate Voltage (R _{GS} = 20 kΩ)	600		V
V _{GS}	Gate- source Voltage	±30		V
I _D	Drain Current (continuous) at T _C = 25°C	9	9 (*)	A
I _D	Drain Current (continuous) at T _C = 100°C	5.7	5.7 (*)	A
I _{DM} (1)	Drain Current (pulsed)	36	36 (*)	A
P _{TOT}	Total Dissipation at T _C = 25°C	125	40	W
	Derating Factor	1.0	0.32	W/°C
dv/dt	Peak Diode Recovery voltage slope	3.5		V/ns
V _{ISO}	Insulation Withstand Voltage (DC)	-	2500	V
T _{stg}	Storage Temperature	- 55 to 150		°C
T _j	Max. Operating Junction Temperature			

(*) Pulse width limited by safe operating area

(1) I_{SD} ≤ 9A, di/dt ≤ 100A/μs, V_{DD} ≤ V_{(BR)DSS}, T_j ≤ T_{JMAX}

(*) Limited only by Maximum Temperature Allowed

STP9NC60 / STP9NC60FP**THERMAL DATA**

		TO-220	TO-220FP	
Rthj-case	Thermal Resistance Junction-case Max	1.0	3.12	°C/W
Rthj-amb	Thermal Resistance Junction-ambient Max	62.5		°C/W
T _l	Maximum Lead Temperature For Soldering Purpose	300		°C

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I _{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T _j max)	9	A
E _{AS}	Single Pulse Avalanche Energy (starting T _j = 25 °C, I _D = I _{AR} , V _{DD} = 50 V)	850	mJ

ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C UNLESS OTHERWISE SPECIFIED)**OFF**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{(BR)DSS}	Drain-source Breakdown Voltage	I _D = 250 μA, V _{GS} = 0	600			V
I _{DSS}	Zero Gate Voltage Drain Current (V _{GS} = 0)	V _{DS} = Max Rating V _{DS} = Max Rating, T _C = 125 °C			1 50	μA μA
I _{GSS}	Gate-body Leakage Current (V _{DS} = 0)	V _{GS} = ±30V			±100	nA

ON (1)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2	3	4	V
R _{DS(on)}	Static Drain-source On Resistance	V _{GS} = 10V, I _D = 4.5 A		0.6	0.75	Ω

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
g _{fs} (1)	Forward Transconductance	V _{DS} = 20 V, I _D = 4.5A		9		S
C _{iss}	Input Capacitance	V _{DS} = 25V, f = 1 MHz, V _{GS} = 0		1420		pF
C _{oss}	Output Capacitance			205		pF
C _{rss}	Reverse Transfer Capacitance			35		pF

ELECTRICAL CHARACTERISTICS (CONTINUED)

SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = 300V, I_D = 4.5 A$ $R_G = 4.7\Omega, V_{GS} = 10V$		20		ns
t_r	Rise Time	(see test circuit, Figure 3)		16		ns
Q_g	Total Gate Charge	$V_{DD} = 480V, I_D = 9.0 A,$ $V_{GS} = 10V$		55	77	nC
Q_{gs}	Gate-Source Charge			4.5		nC
Q_{gd}	Gate-Drain Charge			31		nC

SWITCHING OFF

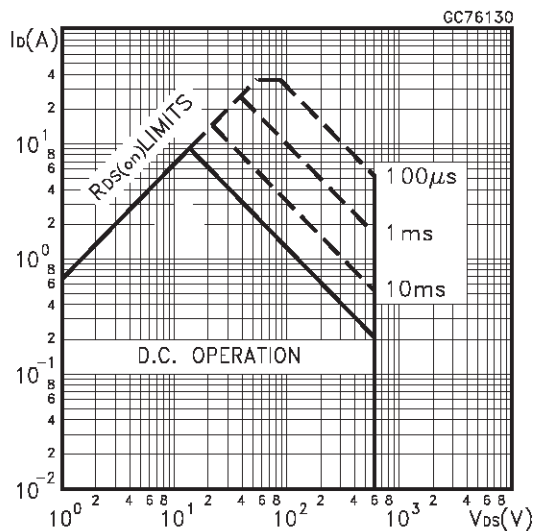
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(off)}$	Turn-off Delay Time	$V_{DD} = 300 V, I_D = 4.5 A$ $R_G = 4.7\Omega, V_{GS} = 10 V$		64		ns
t_f	Fall Time	(Resistive Load see, Figure 3)		32		ns
$t_{r(Voff)}$	Off-voltage Rise Time	$V_{DD} = 480V, I_D = 9.0 A,$ $R_G = 4.7\Omega, V_{GS} = 10V$		19		ns
t_f	Fall Time	(Inductive Load see, Figure 5)		13		ns
t_c	Cross-over Time			32		ns

SOURCE DRAIN DIODE

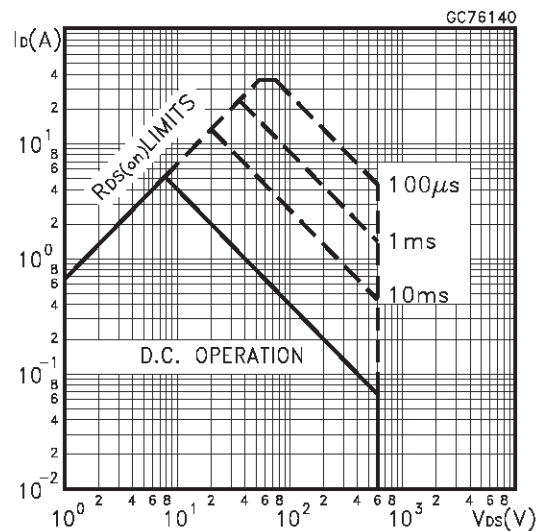
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{SD}	Source-drain Current				9.0	A
$I_{SDM(2)}$	Source-drain Current (pulsed)				36	A
$V_{SD(1)}$	Forward On Voltage	$I_{SD} = 9 A, V_{GS} = 0$			1.6	V
t_{rr}	Reverse Recovery Time	$I_{SD} = 9 A, di/dt = 100A/\mu s,$ $V_{DD} = 100V, T_J = 150^\circ C$		600		ns
Q_{rr}	Reverse Recovery Charge	(see test circuit, Figure 5)		4.7		μC
I_{RRM}	Reverse Recovery Current			15.5		A

Note: 1. Pulsed: Pulse duration = 300 μs , duty cycle 1.5 %.
2. Pulse width limited by safe operating area.

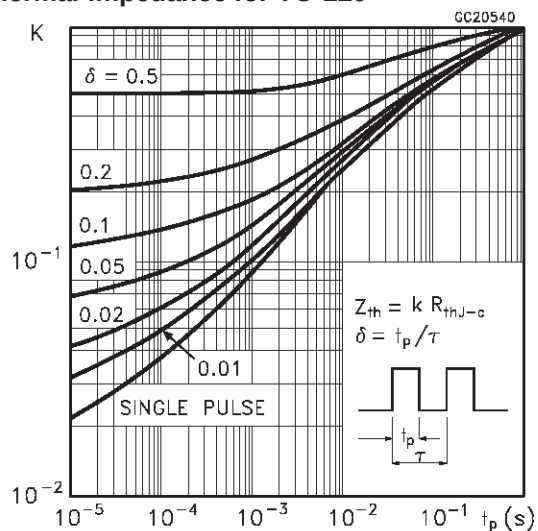
Safe Operating Area for TO-220



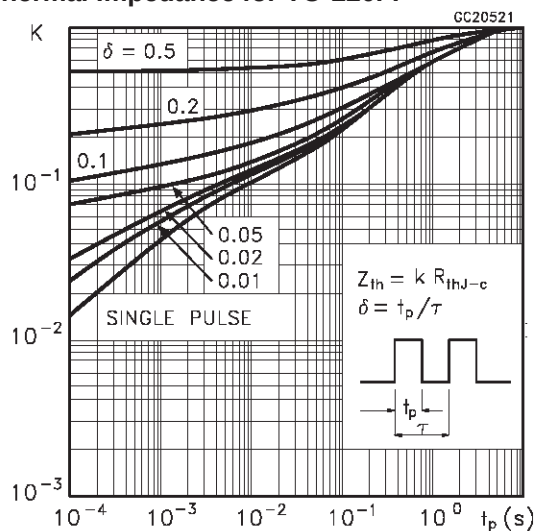
Safe Operating Area for TO-220FP



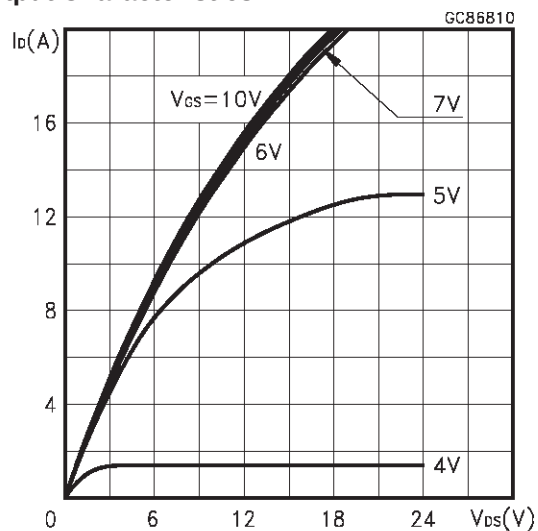
Thermal Impedance for TO-220



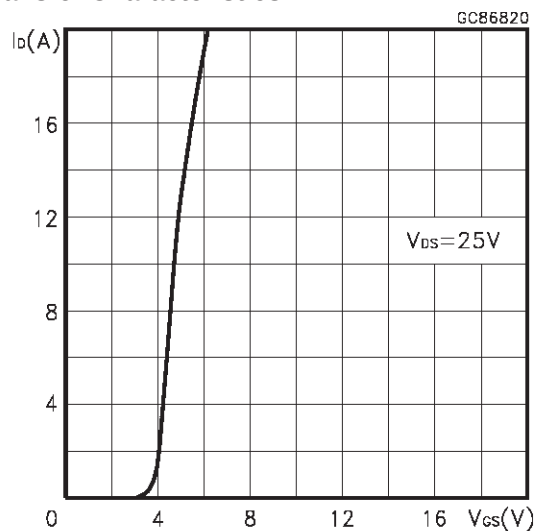
Thermal Impedance for TO-220FP



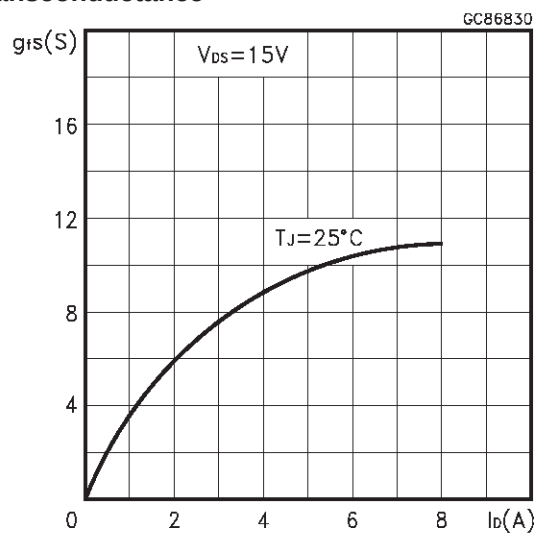
Output Characteristics



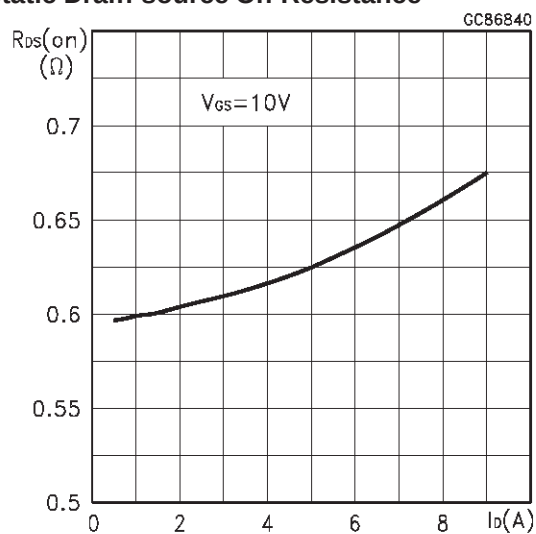
Transfer Characteristics



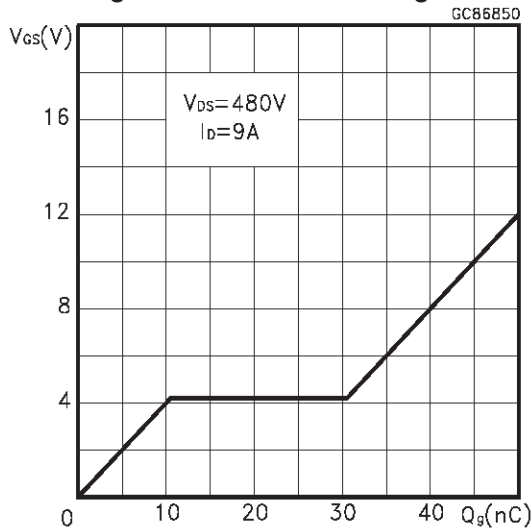
Transconductance



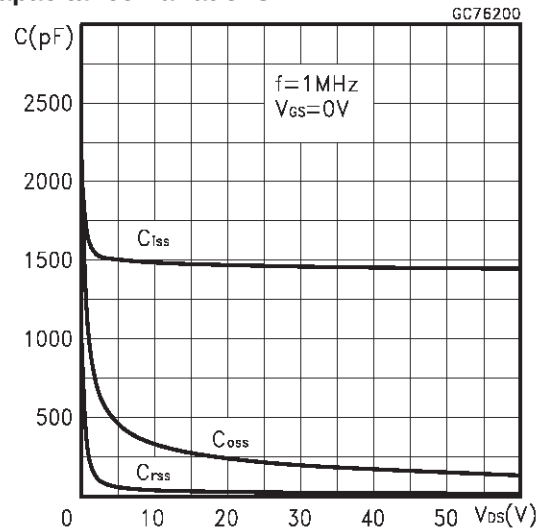
Static Drain-source On Resistance



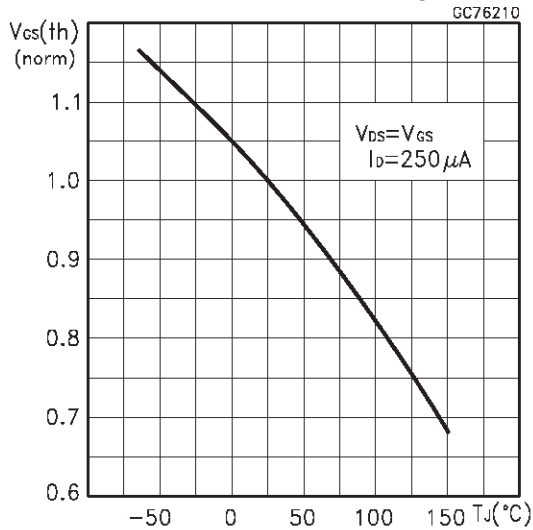
Gate Charge vs Gate-source Voltage



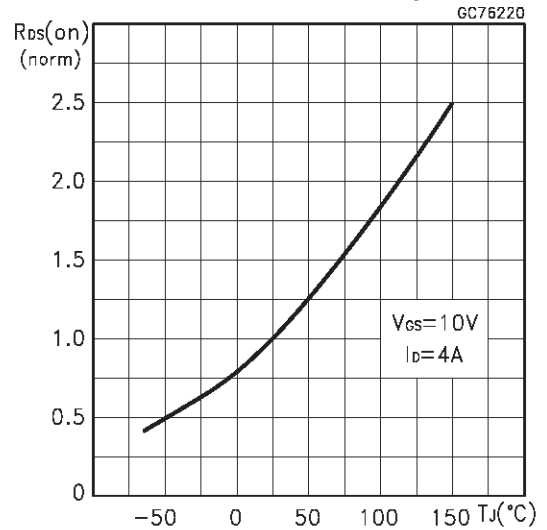
Capacitance Variations



Normalized Gate Threshold Voltage vs Temp.



Normalized On Resistance vs Temperature



Source-drain Diode Forward Characteristics

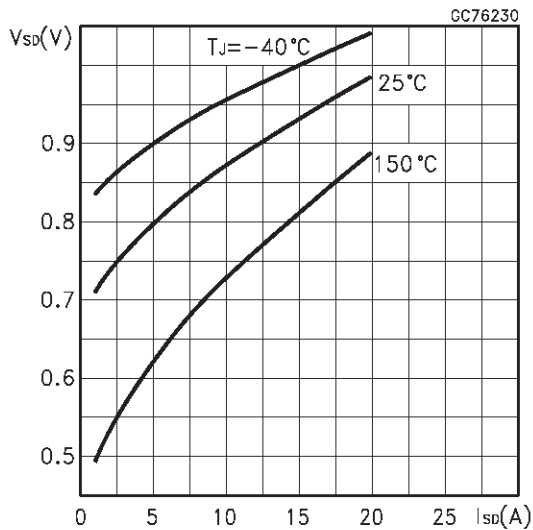


Fig. 1: Unclamped Inductive Load Test Circuit

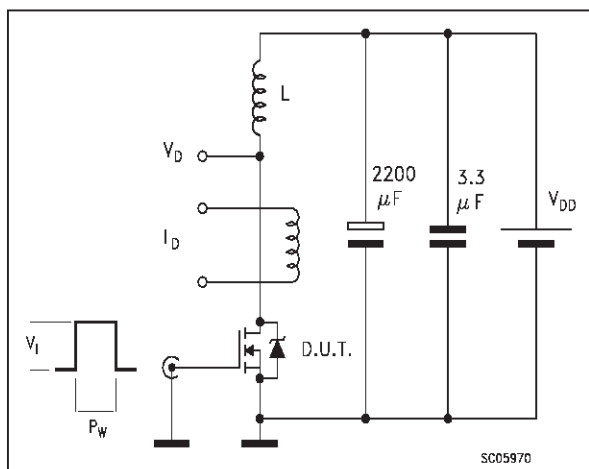


Fig. 2: Unclamped Inductive Waveform

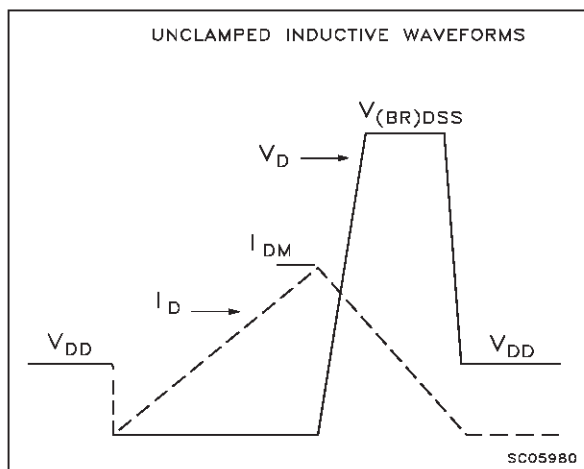


Fig. 3: Switching Times Test Circuit For Resistive Load

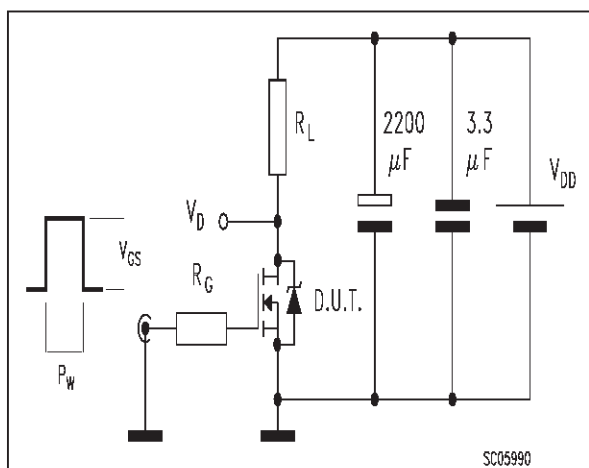


Fig. 4: Gate Charge test Circuit

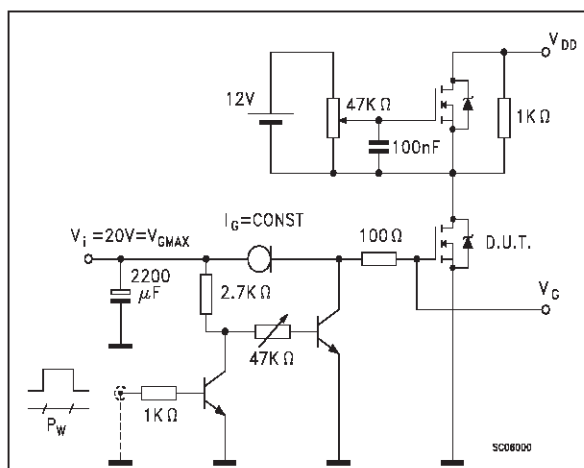
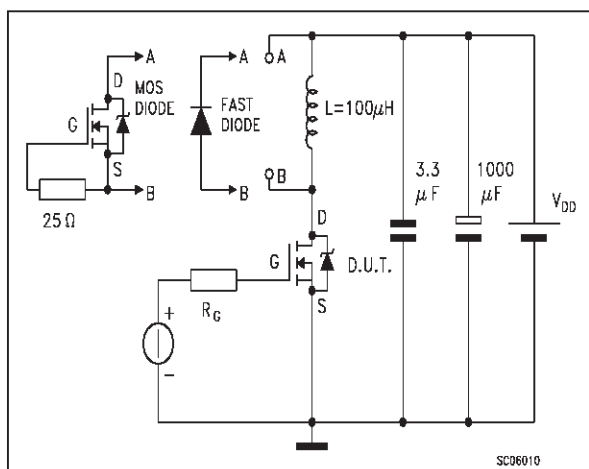


Fig. 5: Test Circuit For Inductive Load Switching And Diode Recovery Times



TO-220 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.40		4.60	0.173		0.181
C	1.23		1.32	0.048		0.051
D	2.40		2.72	0.094		0.107
D1		1.27			0.050	
E	0.49		0.70	0.019		0.027
F	0.61		0.88	0.024		0.034
F1	1.14		1.70	0.044		0.067
F2	1.14		1.70	0.044		0.067
G	4.95		5.15	0.194		0.203
G1	2.4		2.7	0.094		0.106
H2	10.0		10.40	0.393		0.409
L2		16.4			0.645	
L4	13.0		14.0	0.511		0.551
L5	2.65		2.95	0.104		0.116
L6	15.25		15.75	0.600		0.620
L7	6.2		6.6	0.244		0.260
L9	3.5		3.93	0.137		0.154
DIA.	3.75		3.85	0.147		0.151

