



QUAD HIGH SIDE SMART POWER SOLID STATE RELAY

* per Channel
** at $T_1 = 85^\circ\text{C}$

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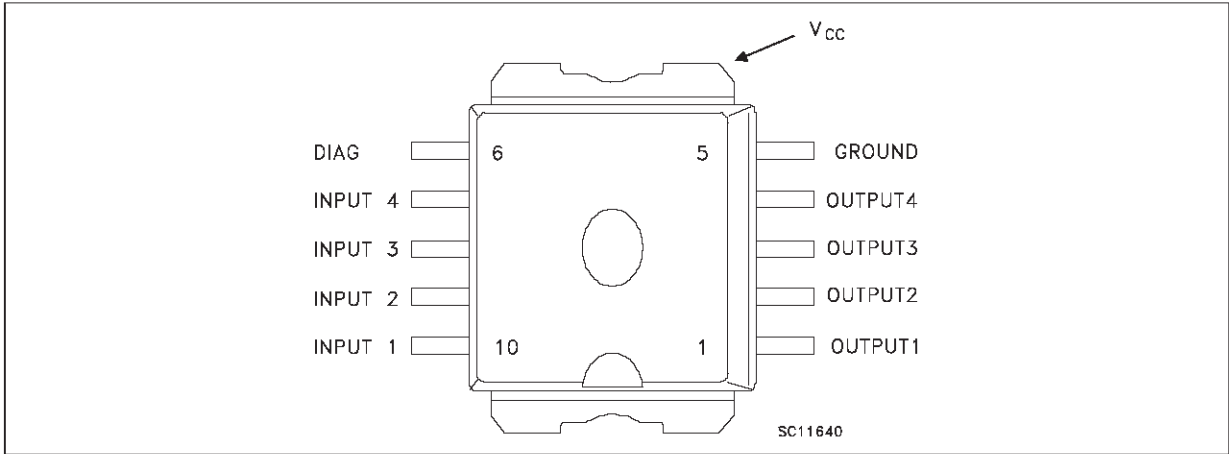
Active current limitation avoids dropping the system power supply in case of shorted load. Built-in thermal shut-down protects the chip from over temperature and short circuit. The open drain diagnostic output indicates short circuit and overtemperature conditions.

The diagram illustrates the internal architecture of the SC07940 microcontroller. It features a central **CONTROL LOGIC** block that manages four input channels (**INPUT 1** through **INPUT 4**) and four overtemperature sensors (**OVERTEMP 1** through **OVERTEMP 4**). The control logic also interfaces with an **UNDERVOLTAGE** monitor and four output drivers (**DRIVER 1** through **DRIVER 4**). Each driver is associated with a current limit block (**I lim 1** through **I lim 4**) and a power output stage consisting of a MOSFET and a BJT. The output stages drive **OUTPUT 1** through **OUTPUT 4**. The entire system is powered by **V_{CC}** and **GROUND**.

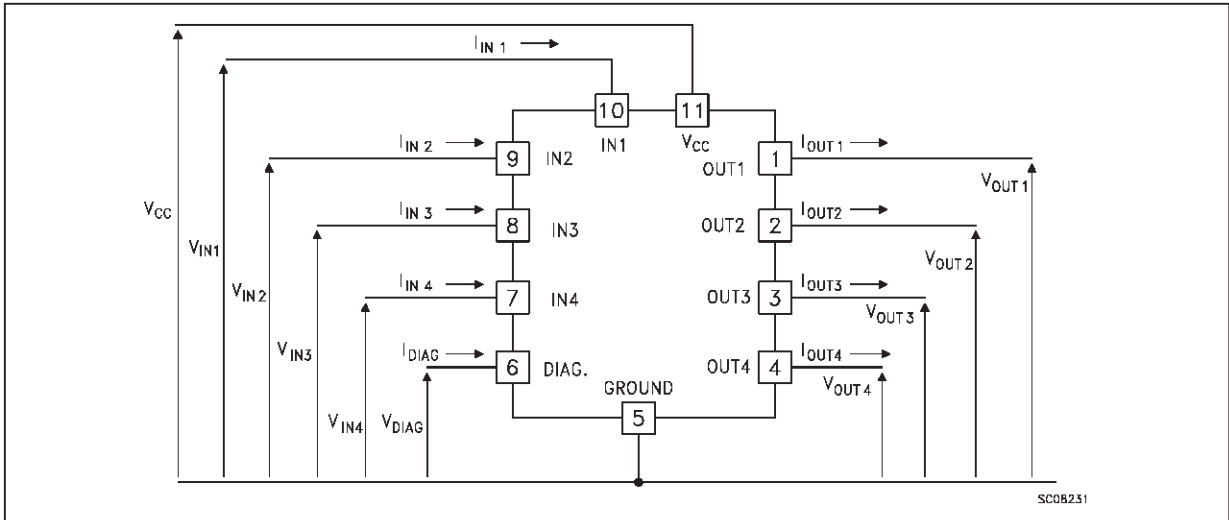
ABSOLUTE MAXIMUM RATING

Symbol	Parameter	Value	Unit
V_{CC}	Power Supply Voltage	45	V
$-V_{CC}$	Reverse Supply Voltage	-0.3	V
I_{OUT}	Output Current (cont.)	Internally Limited	A
I_R	Reverse Output Current (per channel)	-6	A
I_{IN}	Input Current (per channel)	± 10	mA
I_{DIAG}	DIAG Pin Current	± 10	mA
V_{ESD}	Electrostatic Discharge (1.5 k Ω , 100 pF)	2000	V
E_{AS}	Single Pulse Avalanche Energy per Channel Not Symultaneously (see figure 1)	400	mJ
P_{tot}	Power Dissipation at $T_c \leq 25\text{ }^{\circ}\text{C}$	Internally Limited	W
T_j	Junction Operating Temperature	Internally Limited	$^{\circ}\text{C}$
T_{stg}	Storage Temperature	-55 to 150	$^{\circ}\text{C}$

CONNECTION DIAGRAM



CURRENT AND VOLTAGE CONVENTIONS



THERMAL DATA

R _{thj-case}	Thermal Resistance Junction-case (1)	Max	2	°C/W
R _{thj-amb}	Thermal Resistance Junction-ambient (\$)	Max	50	°C/W

(1) All channels ON

(\$) When mounted using minimum recommended pad size on FR-4 board

ELECTRICAL CHARACTERISTICS (10 V < V_{CC} < 36 V; -25 °C < T_J < 125 °C unless otherwise specified)

POWER

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{CC}	Supply Voltage		10		36	V
R _{on}	On State Resistance	I _{OUT} = 0.5 A T _J = 125 °C I _{OUT} = 0.5 A T _J = 85 °C			0.4 0.32	Ω Ω
I _S	Supply Current	All Channels Off On State (T _J = 125 °C) I _{out1} .. I _{out4} = 0			1 10	mA mA
V _{demag}	Output Voltage at Turn-Off	I _{out} = 0.5 A L _{LOAD} = 1 mH	V _{CC} -65	V _{CC} -55	V _{CC} -45	V

SWITCHING (V_{CC} = 24 V)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
t _{d(on)}	Turn-on Delay Time Of Output Current	I _{OUT} = 0.5 A Resistive Load Input Rise Time < 0.1 μs T _J = 25 °C T _J = 125 °C		30	40 60	μs μs
t _r	Rise Time Of Output Current	I _{OUT} = 0.5 A Resistive Load Input Rise Time < 0.1 μs T _J = 25 °C T _J = 125 °C		50	100 115	μs μs
t _{d(off)}	Turn-off Delay Time Of Output Current	I _{OUT} = 0.5 A Resistive Load Input Rise Time < 0.1 μs T _J = 25 °C T _J = 125 °C		20	30 40	μs μs
t _f	Fall Time Of Output Current	I _{OUT} = 0.5 A Resistive Load Input Rise Time < 0.1 μs T _J = 25 °C T _J = 125 °C		8	15 20	μs μs
(di/dt) _{on}	Turn-on Current Slope	I _{OUT} = 0.5 A I _{OUT} = I _{lim} T _J = 25 °C			0.5 2	A/μs A/μs
(di/dt) _{off}	Turn-off Current Slope	I _{OUT} = 0.5 A I _{OUT} = I _{lim} T _J = 25 °C			2 4	A/μs A/μs

ELECTRICAL CHARACTERISTICS (continued)**LOGIC INPUT** (Each Channel)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_{IL}	Input Low Level Voltage				2	V
V_{IH}	Input High Level Voltage (see note 1)		3.5			V
$V_{I(hyst.)}$	Input Hysteresis Voltage			0.5		V
I_{IN}	Input Current	$V_{IN} = 0 \text{ to } 30 \text{ V}$ $V_{IN} = 0 \text{ to } 2 \text{ V}$	25		600	μA μA
I_{LGND}	Output Current in Ground Disconnection	$V_{CC} = V_{INX} = \text{GND} = \text{DIAG} = 24 \text{ V}$ $T_J = 25 \text{ }^\circ\text{C}$			25	mA
V_{ICL}	Input Clamp Voltage (see note 1)	$I_{IN} = 1 \text{ mA}$ $I_{IN} = -1 \text{ mA}$	32	36 -0.7		V V

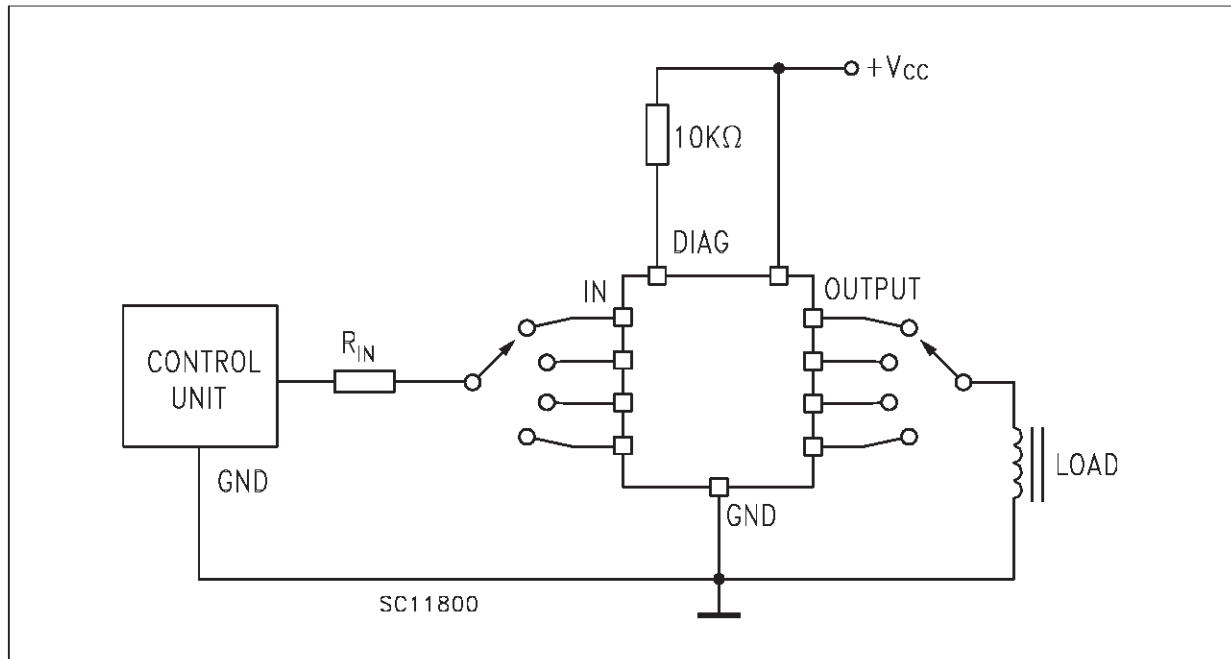
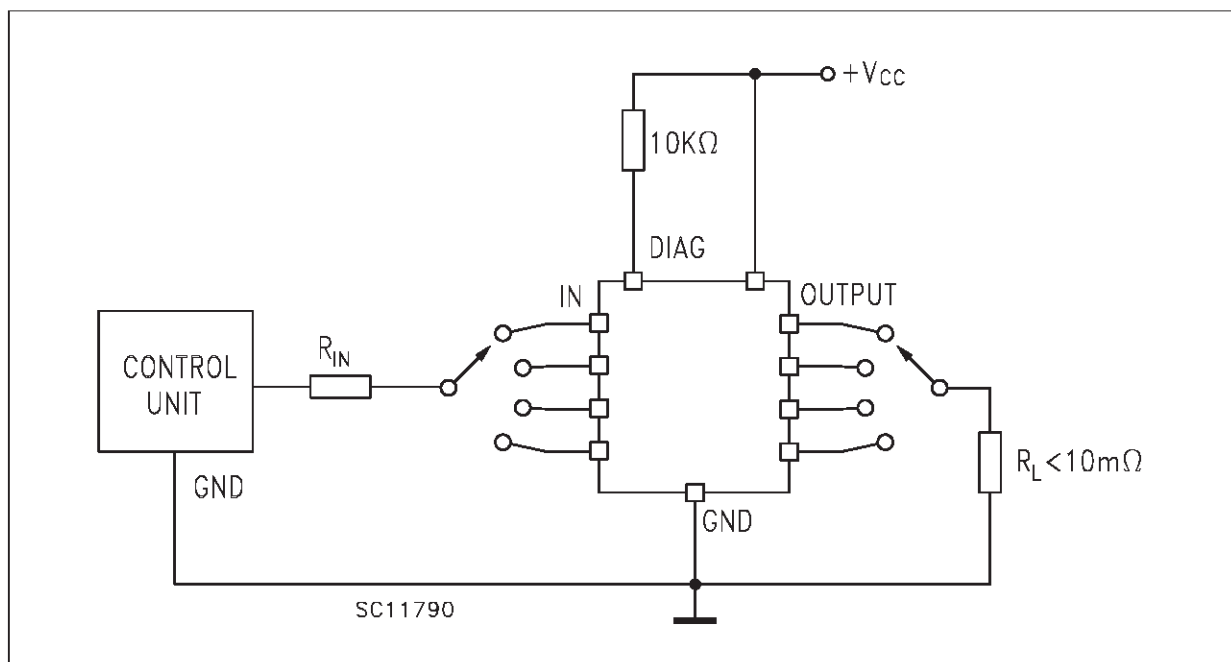
note 1 : The input voltage is internally clamped at 32V minimum, it is possible to connect the input pins to an higher voltage via an external resistor calculate to not exceed 10 mA.

PROTECTION AND DIAGNOSTICS

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{STAT} (\bullet)$	Status Voltage Output Low	$I_{STAT} = 5 \text{ mA}$ (Fault Condition)			1	V
$V_{SCL} (\bullet)$	Status Clamp Voltage	$I_{STAT} = 1 \text{ mA}$ $I_{STAT} = -1 \text{ mA}$	32	36 -0.7		V V
V_{USD}	Under Voltage Shut Down		5		8	V
V_{OL}	Low State Out Voltage	$V_{IN} = V_{ILj} \quad R_{LOAD} \geq 10 \text{ M}\Omega$			1.5	V
I_{LIM}	DC Short Circuit Current	$V_{CC} = 24 \text{ V} \quad R_{LOAD} < 10 \text{ m}\Omega$	0.7		2.5	A
I_{OVPK}	Peak Short Circuit Current	$V_{CC} = 24 \text{ V} \quad V_{in} = 30\text{V}$ $R_{LOAD} < 10 \text{ m}\Omega$ (see figure 2)			4	A
I_{DIAGH}	Leakage on diag pin in high state	$V_{DIAG} = 24 \text{ V}$			100	μA
I_{LOAD}	Output Leakage Current	$V_{CC} = 10 \text{ to } 36\text{V} \quad V_{IN} = V_{IL}$			50	μA
t_{SC}	Delay Time of Current Limiter				100	μs
T_{TSD}	Thermal Shut-down Temperature		150	170		$^\circ\text{C}$
T_R	Reset Temperature		135	155		$^\circ\text{C}$

($\bullet$) Status determination > 100 μs after the switching edge.

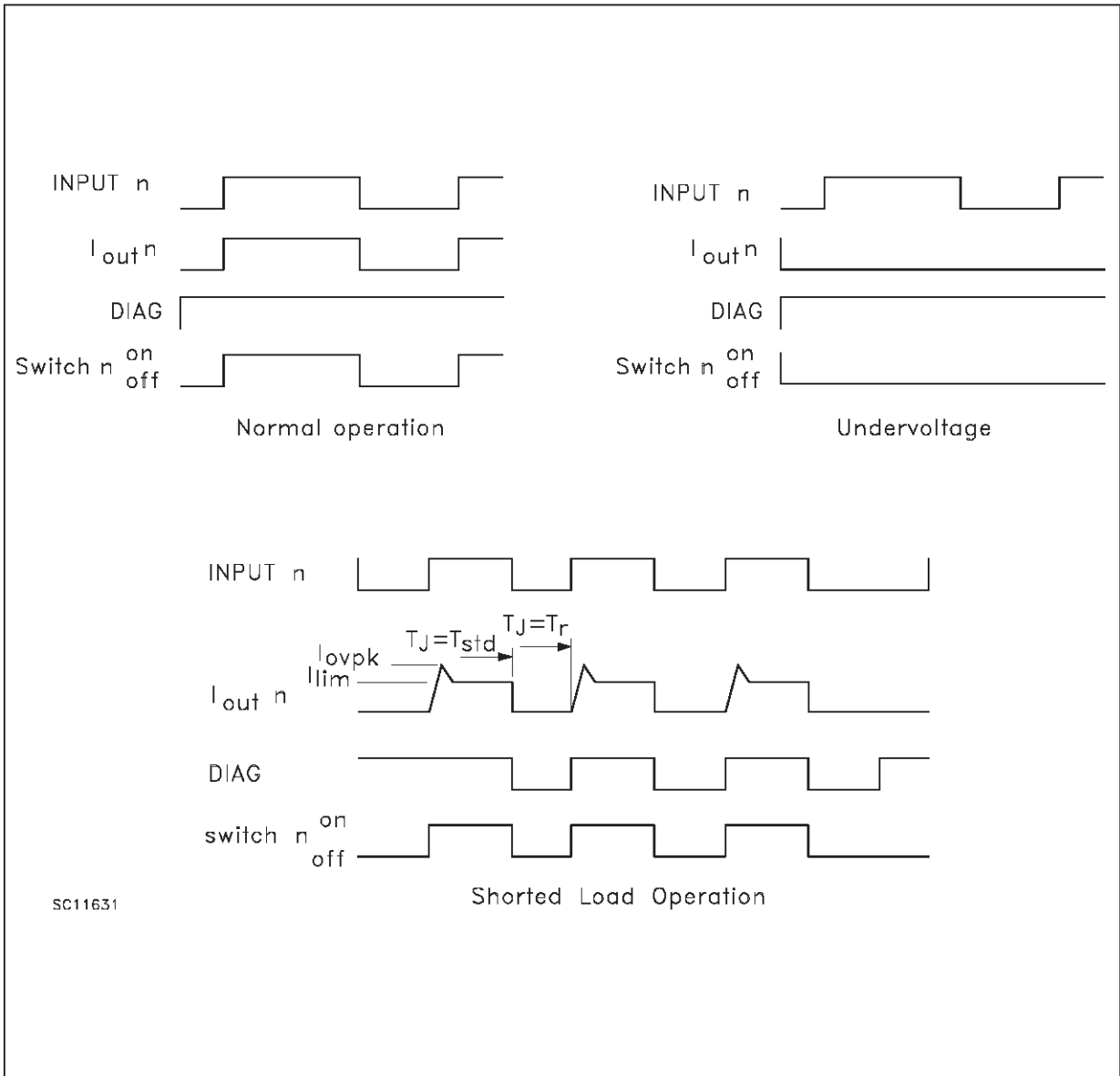
Note: If INPUTn pin is left floating the corresponding channel will automatically switch off. If GND pin is disconnected, all channels will switch off provided V_{CC} does not exceed 36V

FIGURE 1: Avalanche Energy Test Circuit**FIGURE 2:** Peak Short Circuit Current Test Circuit

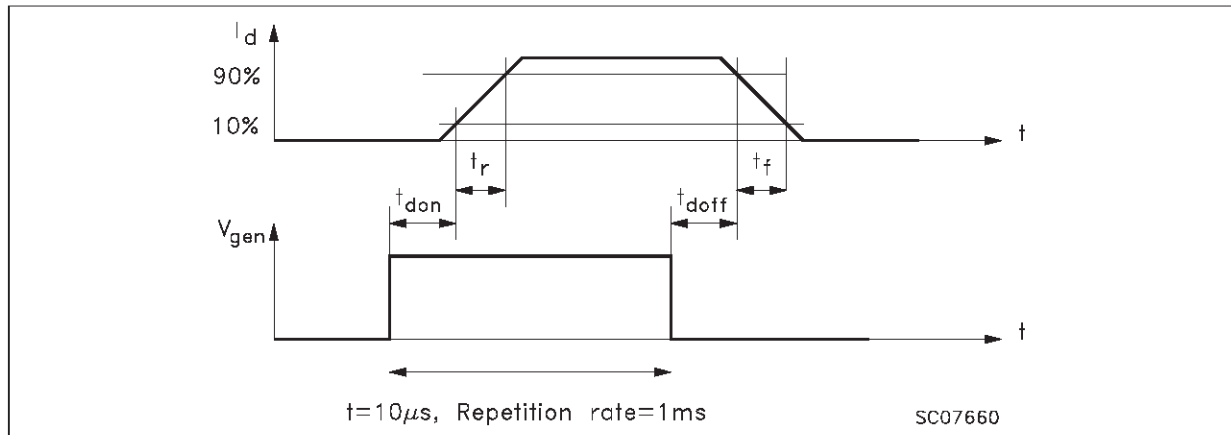
TRUTH TABLE

	INPUTn	OUTPUTn	DIAGNOSTIC
Normal Operation	L	L	H
	H	H	H
Over-temperature	L	L	H
	H	L	L
Under-voltage	L	L	H
	H	L	H
Shorted Load (current limitation)	L	L	H
	H	H	H

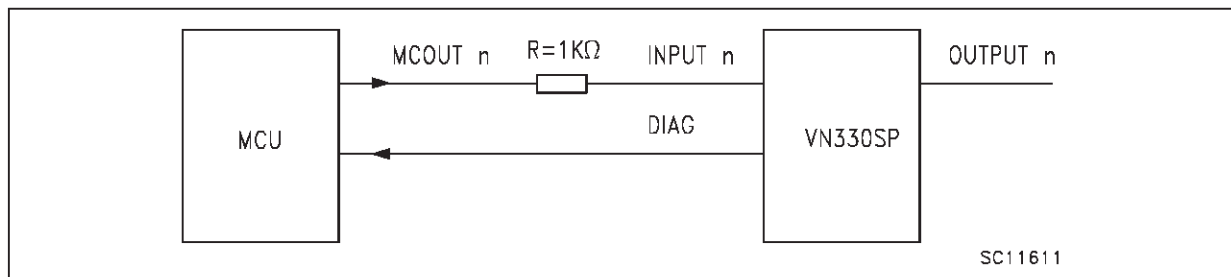
FIGURE 3: Switching Waveforms



SWITCHING PARAMETERS TEST CONDITIONS

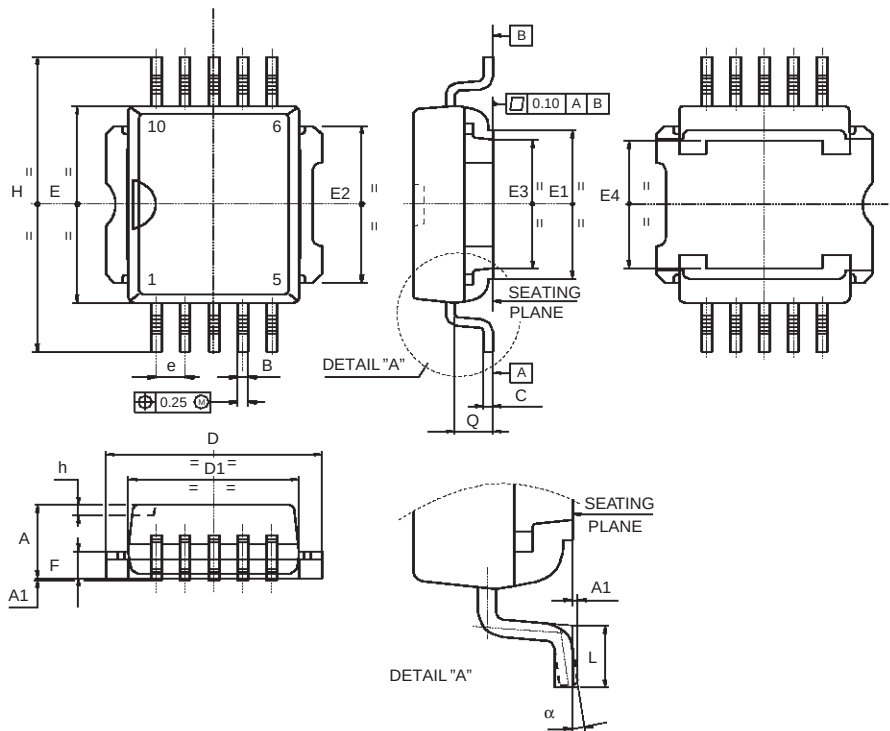


DRIVING CIRCUIT



PowerSO-10 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	3.35		3.65	0.132		0.144
A1	0.00		0.10	0.000		0.004
B	0.40		0.60	0.016		0.024
c	0.35		0.55	0.013		0.022
D	9.40		9.60	0.370		0.378
D1	7.40		7.60	0.291		0.300
E	9.30		9.50	0.366		0.374
E1	7.20		7.40	0.283		0.291
E2	7.20		7.60	0.283		0.300
E3	6.10		6.35	0.240		0.250
E4	5.90		6.10	0.232		0.240
e		1.27			0.050	
F	1.25		1.35	0.049		0.053
H	13.80		14.40	0.543		0.567
h		0.50			0.002	
L	1.20		1.80	0.047		0.071
q		1.70			0.067	
α	0°		8°			



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