

# Digital transistors (built-in resistors)

## DTD113EK / DTD113ES

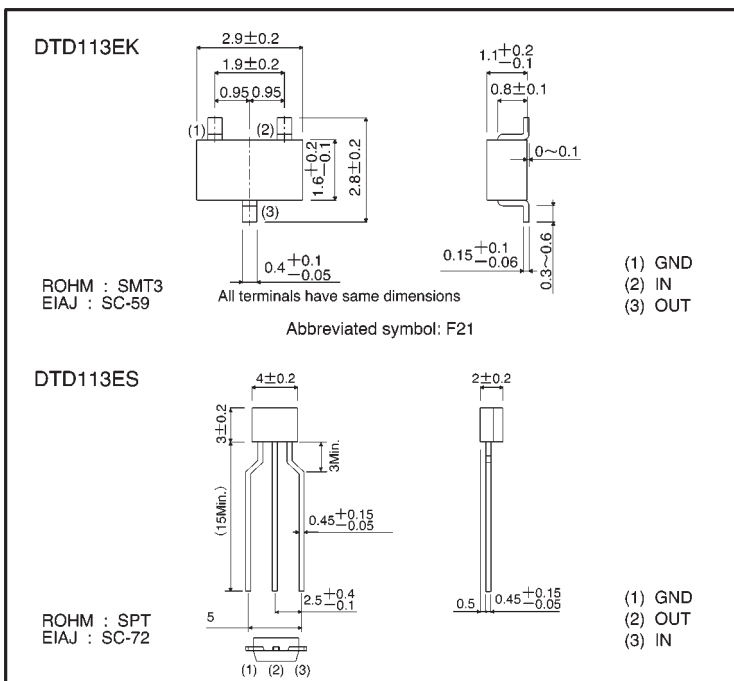
### ●Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/off conditions need to be set for operation, making device design easy.

### ●Structure

NPN digital transistor  
(Built-in resistor type)

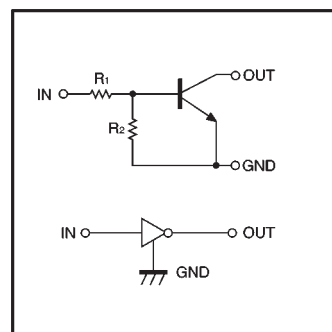
### ●External dimensions (Units: mm)



### ●Absolute maximum ratings (Ta = 25°C)

| Parameter            | Symbol           | Limits(DTD113E□) |     | Unit |
|----------------------|------------------|------------------|-----|------|
|                      |                  | K                | S   |      |
| Supply voltage       | V <sub>CC</sub>  | 50               |     | V    |
| Input voltage        | V <sub>IN</sub>  | -10~+10          |     | V    |
| Output current       | I <sub>C</sub>   | 500              |     | mA   |
| Power dissipation    | P <sub>d</sub>   | 200              | 300 | mW   |
| Junction temperature | T <sub>j</sub>   | 150              |     | °C   |
| Storage temperature  | T <sub>stg</sub> | -55~+150         |     | °C   |

### ●Equivalent circuit



## ●Electrical characteristics (Ta = 25°C)

| Parameter            | Symbol       | Min. | Typ. | Max. | Unit       | Conditions                               |
|----------------------|--------------|------|------|------|------------|------------------------------------------|
| Input voltage        | $V_{I(off)}$ | —    | —    | 0.5  | V          | $V_{CC}=5V$ , $I_o=100\mu A$             |
|                      | $V_{I(on)}$  | 3    | —    | —    |            | $V_o=0.3V$ , $I_o=20mA$                  |
| Output voltage       | $V_{O(on)}$  | —    | 0.1  | 0.3  | V          | $I_o/I_i=50mA/2.5mA$                     |
| Input current        | $I_i$        | —    | —    | 7.2  | mA         | $V_i=5V$                                 |
| Output current       | $I_{O(off)}$ | —    | —    | 0.5  | $\mu A$    | $V_{CC}=50V$ , $V_i=0V$                  |
| DC current gain      | $G_i$        | 33   | —    | —    | —          | $V_o=5V$ , $I_o=50mA$                    |
| Input resistance     | $R_i$        | 0.7  | 1    | 1.3  | k $\Omega$ | —                                        |
| Resistance ratio     | $R_2/R_1$    | 0.8  | 1    | 1.2  | —          | —                                        |
| Transition frequency | $f_T$        | —    | 200  | —    | MHz        | $V_{CE}=10V$ , $I_E=-5mA$ , $f=100MHz$ * |

\* Transition frequency of the device

## ●Packaging specifications

|          |                              |                                                                                   |                                                                                   |
|----------|------------------------------|-----------------------------------------------------------------------------------|-----------------------------------------------------------------------------------|
| Part No. | Package                      | SMT3                                                                              | SPT                                                                               |
|          | Packaging type               | Taping                                                                            | Taping                                                                            |
|          | Code                         | T146                                                                              | TP                                                                                |
|          | Basic ordering unit (pieces) | 3000                                                                              | 5000                                                                              |
| DTD113EK |                              |  | —                                                                                 |
| DTD113ES |                              | —                                                                                 |  |

## ●Electrical characteristic curves

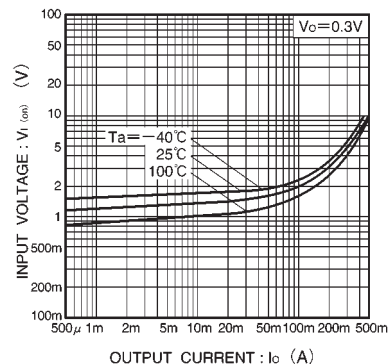


Fig.1 Input voltage vs. output current (ON characteristics)

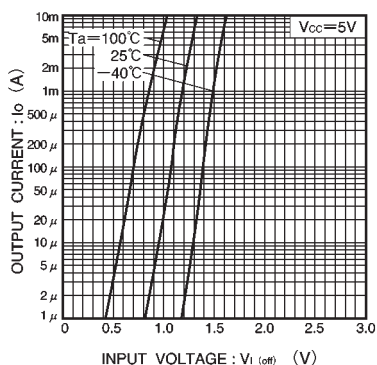


Fig.2 Output current vs. input voltage (OFF characteristics)

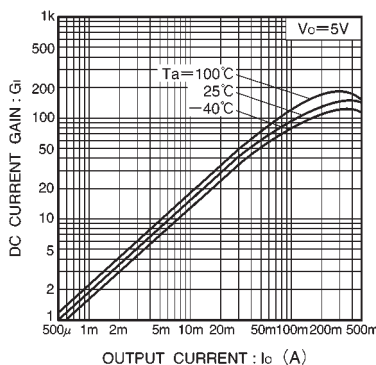


Fig.3 DC current gain vs. output current

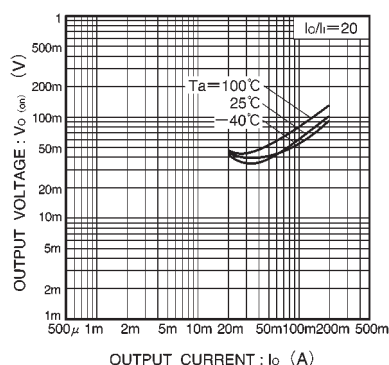


Fig.4 Output voltage vs. output current