

General purpose (dual digital transistors)

UMA1N / UMB1N / FMA1A / IMB1A / IMB5A

●Feature

1) Two DTA124E chips in a UMT or SMT package.

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	V_{CC}	-50	V
Input voltage	V_{IN}	-40	V
Output current	I_O	10	mA
Power dissipation	P_d	150(TOTAL) 300(TOTAL)	mW *1
Junction temperature	T_J	150	°C
Storage temperature	T_{stg}	-55 ~ +150	°C

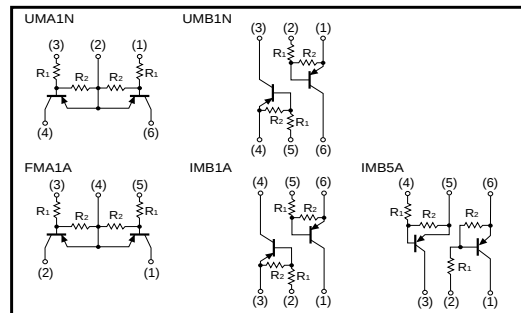
*1 120mW per element must not be exceeded.

*2 200mW per element must not be exceeded.

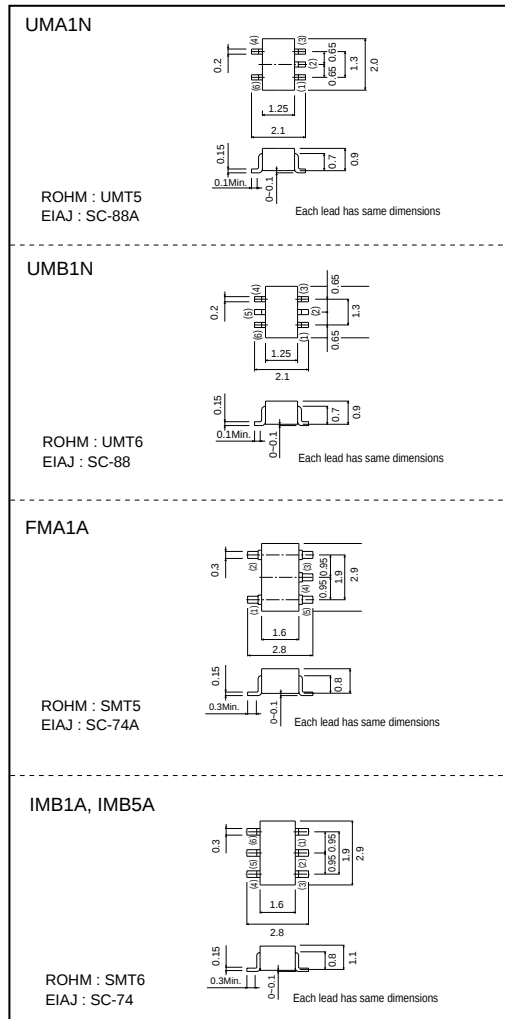
●Package, marking, and packaging specifications

Part No.	UMA1N	UMB1N	FMA1A	IMB1A	IMB5A
Package	UMT5	UMT6	SMT5	SMT6	SMT6
Marking	A1	B1	A1	B1	B5
Code	TR	TN	T148	T110	T110
Basic ordering unit (pieces)	3000	3000	3000	3000	3000

●Equivalent circuits



●External dimensions (Units : mm)



●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	$V_{I(off)}$	-	-	-0.5	V	$V_{CC} = -5V, I_O = -100\mu A$
	$V_{I(on)}$	-3	-	-	V	$V_O = -0.2V, I_O = -5mA$
Output voltage	$V_{O(on)}$	-	-0.1	-0.3	V	$I_O/I_C = -0.5mA/-10mA$
Input current	I_i	-	-	-0.36	mA	$V_i = -5V$
Output current	$I_{O(off)}$	-	-	-0.5	μA	$V_{CC} = -50V, V_i = 0V$
DC current gain	G_i	56	-	-	-	$V_O = -5V, I_O = -5mA$
Input resistance	R_1	15.4	22	28.6	k Ω	-
Resistance ratio	R_2/R_1	0.8	1	1.2	-	-