

160MHz Rail-to-Rail Amplifier with Disable

AD8041

1.0 <u>SCOPE</u>

This specification documents the detailed requirements for Analog Devices space qualified die including die qualification as described for Class K in MIL-PRF-38534, Appendix C, Table C-II except as modified herein.

The manufacturing flow described in the STANDARD DIE PRODUCTS PROGRAM brochure <u>http://www.analog.com/marketSolutions/militaryAerospace/pdf/Die Broc.pdf</u> is to be considered a part of this specification.

This data sheet specifically details the space grade version of this product. A more detailed operational description and a complete data sheet for commercial product grades can be found at www.analog.com/AD8041

2.0 <u>Part Number</u>. The complete part number(s) of this specification follow:

Part Number

AD8041-000C

Description

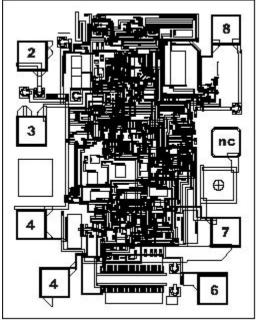
160MHz Rail-to-Rail Amplifier with Disable

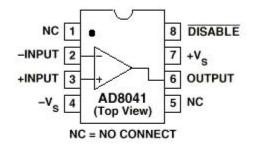
3.0 <u>Die Information</u>

3.1 <u>Die Dimensions</u>

Die Size	Die Thickness	Bond Pad Metalization
42 x 50 mil	19 mil ± 2 mil	Al/Cu

3.2 <u>Die Picture</u>





ASD0012808

Rev. F

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3.3 Absolute Maximum Ratings 1/

Supply Voltage (V _S)	+12.6V
Input Common Mode Range (V _{IN})	±Vs
Storage Temperature	
Junction Temperature (T _J)	+175°C
Operating Ambient Temperature Range (T _A)	55°C to +125°C
Absolute Maximum Ratings Notes:	

1/ Stresses above the absolute maximum rating may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.

4.0 Die Qualification

In accordance with class-K version of MIL-PRF-38534, Appendix C, Table C-II,

except as modified herein.

(a) Qual Sample Size and Qual Acceptance Criteria - 10/0

(b) Qual Sample Package - DIP

(c) Pre-screen electrical test over temperature performed post-assembly prior to die qualification.

Table I -Dice Electrical Characteristics							
Parameter	Symbol	Conditions <u>1/</u>	Limit Min	Limit Max	Units		
Input Offset Voltage	Vos			9.5	mV		
Input Bias Current	I _{IB}			3.4	μA		
Input Offset Current	los			0.7	μΑ		
Input Voltage Range	IVR		±1		V		
Common Mode Rejection Ratio	CMRR	$V_{CM} = IVR$	65		dB		
Power Supply Rejection Ratio	PSRR	$V_S=0V;+5V,\pm 1V$	65		dB		
Open Loop Gain	Aol	$V_{0} = \pm 1V$, $R_{L} = 1k\Omega$	15		kV/V		
Output Swing Voltage	Vout	$R_L = 2k\Omega$	±2		V		
Ouissant Supply Current	1			6.1	A		
Quiescent Supply Current	ls	Disabled		1.7	mA		

Table I Notes:

 $\underline{1/}$ V_S = ±2.5V, T_A = 25°C unless otherwise specified.

Table II - Electrical Characteristics for Qual Samples							
Parameter	Symbol	Conditions <u>1/</u>	Sub- groups	Limit Min	Limit Max	Units	
Input Offset Voltage	Vos		1, 2, 3		9.5	mV	
Input Bias Current	I _{IB}		1, 2, 3		3.4	μΑ	
Input Offset Current	los		1, 2, 3		0.7	μA	
Input Voltage Range	IVR		1, 2, 3	±1		V	
Common Mode Rejection Ratio	CMRR	$V_{CM} = IVR$	1, 2, 3	65		dB	
Power Supply Rejection Ratio	PSRR	$V_{s} = 0V; +5V, \pm 1V$	1, 2, 3	65		dB	
Open Loop Gain	Aol	$R_L = 1k\Omega$	1, 2, 3	15		kV/V	
Output Swing Voltage	Vout	$R_L = 2k\Omega$	1	±2		V	
Ouissesset Guardu Guarant			1 2 2		6.1	0	
Quiescent Supply Current	ls –	Disabled	1, 2, 3		1.7	mA	

Table II Notes:

 $\underline{1/}$ V_S = ±2.5V, V_{CM} = 0V, unless otherwise specified.

Table III -Life Test Endpoint and Delta Parameter (Product is tested in accordance with Table II with the following exceptions)								
Devenuedary	Symbol	Sub groups	Post Burn In Limit		Post Life Test Limit		Life	
Parameter			Min	Max	Min	Мах	Test Delta	Units
Input Offset Voltage	Vos	1		±9		±11	±2	μV
		2,3				±12		
Input Bias Current	I _{IB}	1		±2.6		±3.2	±0.6	μΑ
input bias current		2,3				±4.4		

5.0 Life Test/Burn-In Information

- 5.1 HTRB is not applicable for this drawing.
- 5.2 Burn-in is per MIL-STD-883 Method 1015 test condition B or C.
- 5.3 Steady state life test is per MIL-STD-883 Method 1005.

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Rev	Description of Change	Date
А	Initiate	7-Feb-02
В	Update 1.0 Scope Description	30 July 2007
С	Update header/footer and add to 1.0 Scope description	19-Feb-08
D	Add Junction Temperature (T _J)175°C to 3.3 Absolute Max. Ratings	March 31, 2008
Е	Updated Section 4.0c note to indicate pre-screen temp testing being performed.	6-JUN-2009
F	Update fonts and sizes to ADI standards	27-Sept-2011

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