



# 2SK1459LS

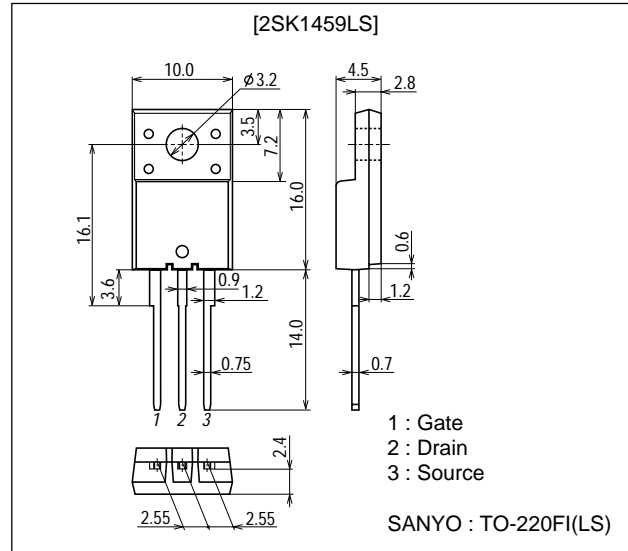
## Ultrahigh-Speed Switching Applications

### Features

- Low ON-resistance.
- Ultrahigh-speed switching.
- Micaless package facilitating mounting.

### Package Dimensions

unit : mm  
2078C



### Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		900	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±30	V
Drain Current (DC)	I <sub>D</sub>		2.5	A
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycle≤1%	5	A
Allowable Power Dissipation	P <sub>D</sub>		2.0	W
		T <sub>c</sub> =25°C	30	W
Channel Temperature	T <sub>ch</sub>		150	°C
Storage Temperature	T <sub>stg</sub>		-55 to +150	°C

### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> =1mA, V <sub>GS</sub> =0	900			V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =900V, V <sub>GS</sub> =0			1.0	mA
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0			±100	nA
Cutoff Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	2.0		3.0	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> =20V, I <sub>D</sub> =1.5A	0.8	1.5		S

Marking : K1459

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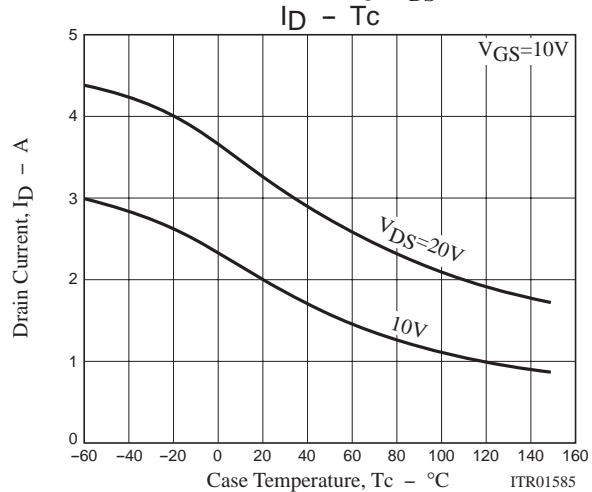
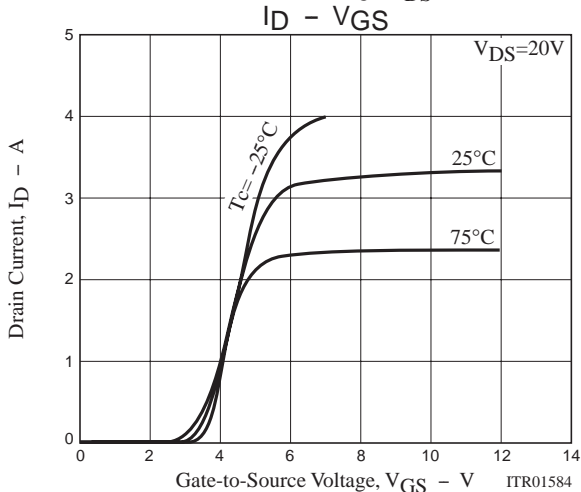
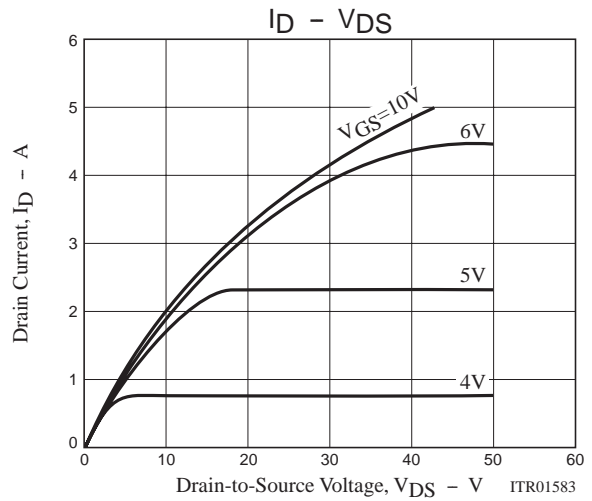
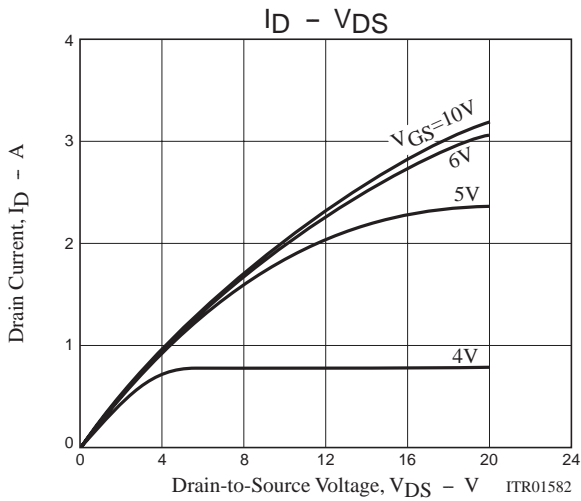
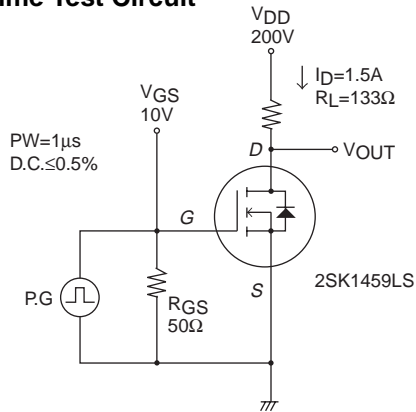
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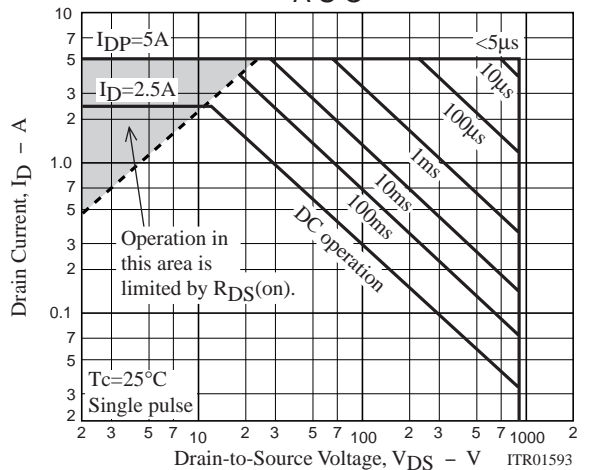
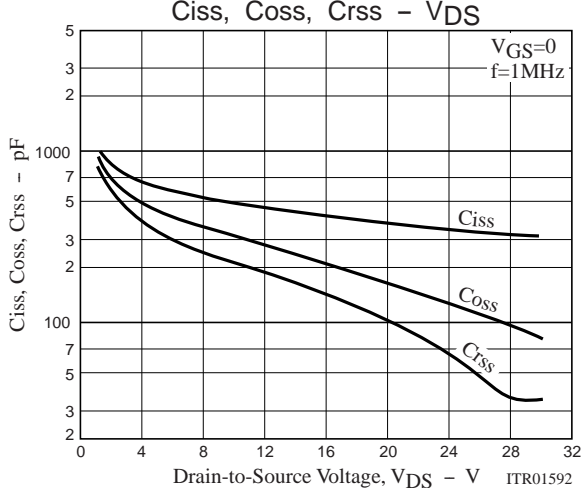
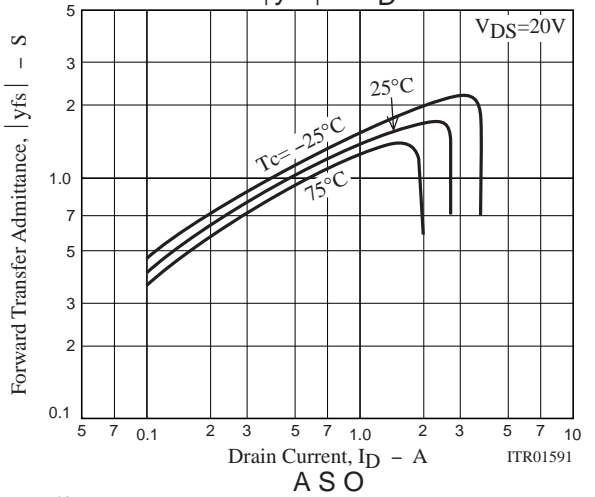
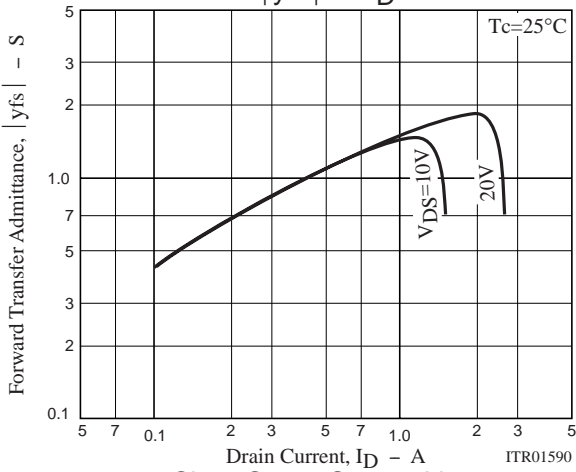
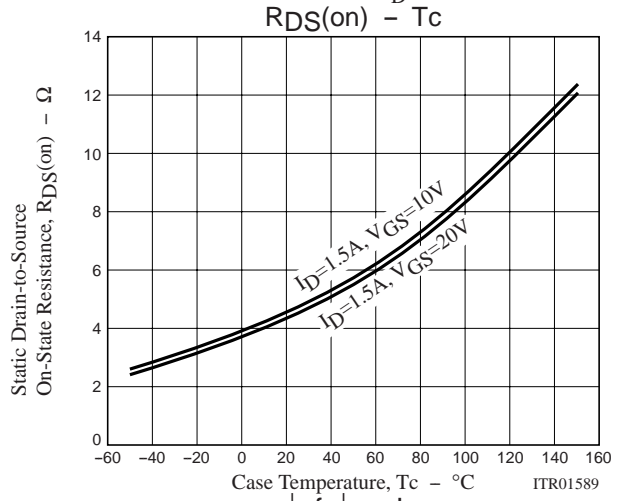
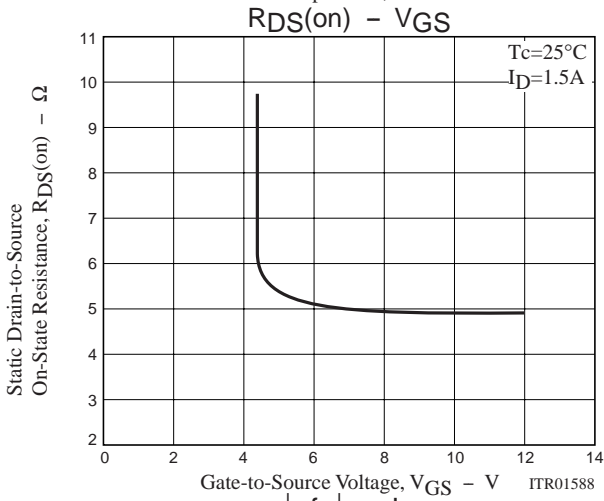
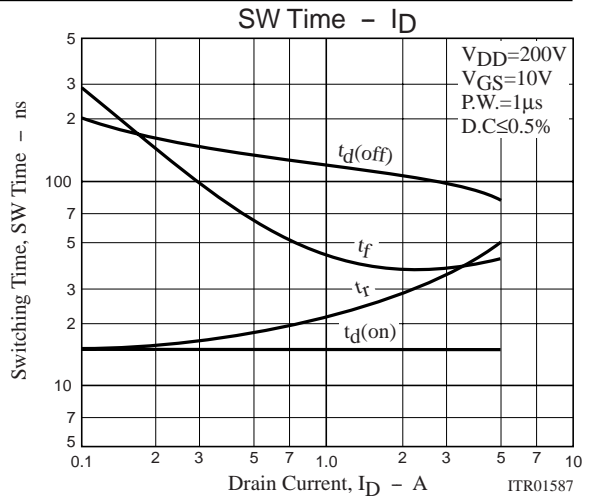
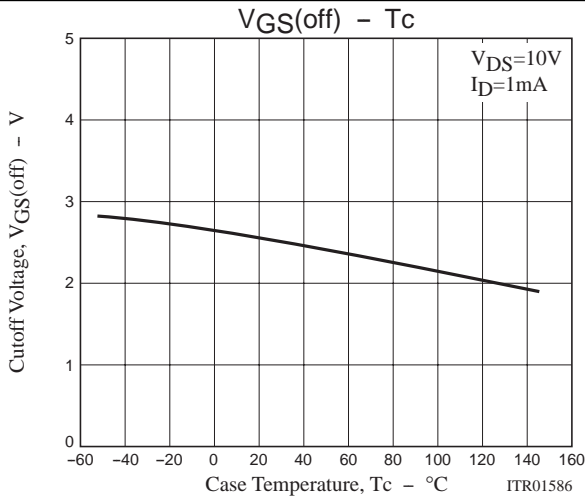
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Static Drain-to-Source On-State Resistance	$R_{DS(on)}$	$I_D=1.5A, V_{GS}=10V$		4.7	60	$\Omega$
Input Capacitance	$C_{iss}$	$V_{DS}=20V, f=1MHz$		350		pF
Output Capacitance	$C_{oss}$	$V_{DS}=20V, f=1MHz$		150		pF
Reverse Transfer Capacitance	$C_{rss}$	$V_{DS}=20V, f=1MHz$		100		pF
Turn-ON Delay Time	$t_d(on)$	$I_D=1.5A, V_{GS}=10V, V_{DD}=200V, R_{GS}=50\Omega$		15		ns
Rise Time	$t_r$	$I_D=1.5A, V_{GS}=10V, V_{DD}=200V, R_{GS}=50\Omega$		25		ns
Turn-OFF Delay Time	$t_d(off)$	$I_D=1.5A, V_{GS}=10V, V_{DD}=200V, R_{GS}=50\Omega$		120		ns
Fall Time	$t_f$	$I_D=1.5A, V_{GS}=10V, V_{DD}=200V, R_{GS}=50\Omega$		40		ns
Diode Forward Voltage	$V_{SD}$	$I_S=2.5A, V_{GS}=0$			1.8	V

(Note) Be careful in handling the 2SK1459LS because it has no protection diode between gate and source.

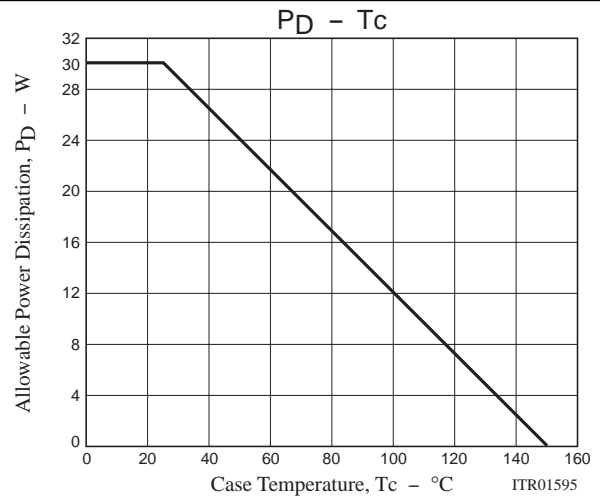
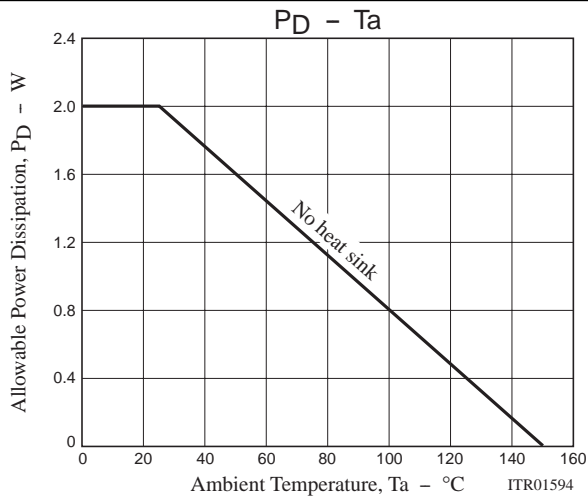
## Switching Time Test Circuit



# 2SK1459LS



## 2SK1459LS



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