

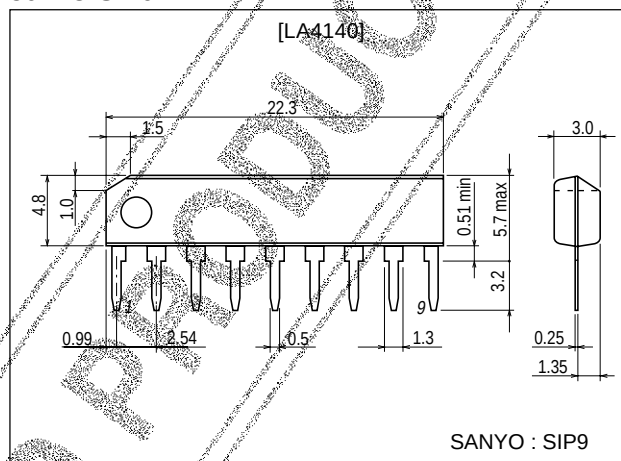
**SANYO****LA4140****0.5W AF Power Amplifier****Features**

- Output power 0.5W typ ( $V_{CC}=6V$ ,  $R_L=8\Omega$ , THD=10%).
- Low quiescent current.
- Wide operating voltage range :  $V_{CC}=3.5$  to 12V.
- 9-pin SIP permitting sets to be small-sized and eliminating the need to use a heat sink.

**Package Dimensions**

unit:mm

3017C-SIP9

**Specifications****Absolute Maximum Ratings** at  $T_a = 25^\circ C$ 

| Parameter                   | Symbol        | Conditions                    | Ratings     | Unit       |
|-----------------------------|---------------|-------------------------------|-------------|------------|
| Maximum Supply Voltage      | $V_{CC\ max}$ | Quiescent mode                | 14          | V          |
|                             |               | Operating mode $R_L=16\Omega$ | 14          | V          |
|                             |               | Operating mode $R_L=8\Omega$  | 12          | V          |
| Maximum Output Current      | $I_O$         |                               | 500         | mA         |
| Allowable Power Dissipation | $P_d\ max^*$  |                               | *750        | mW         |
| Operating Temperature       | $T_{opr}$     |                               | -20 to +70  | $^\circ C$ |
| Storage Temperature         | $T_{stg}$     |                               | -40 to +150 | $^\circ C$ |

\*  $P_d\ max$  : Installed on 50x50mm<sup>2</sup> PCB, See figure of  $P_d\ max - T_a$ .**Recommended Operating Conditions** at  $T_a = 25^\circ C$ 

| Parameter       | Symbol   | Conditions | Ratings | Unit     |
|-----------------|----------|------------|---------|----------|
| Supply Voltage  | $V_{CC}$ |            | 6       | V        |
| Load Resistance | $R_L$    |            | 8       | $\Omega$ |

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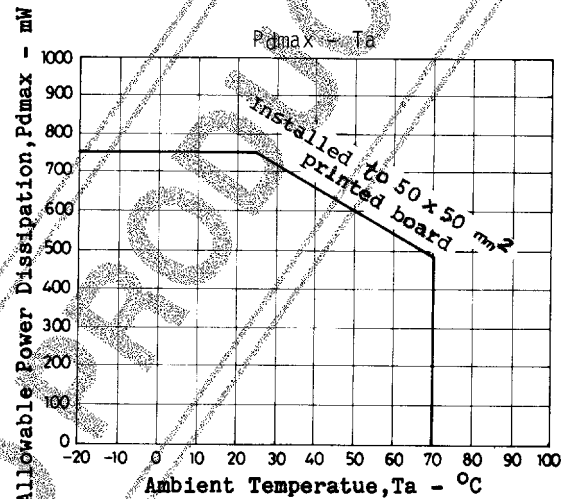
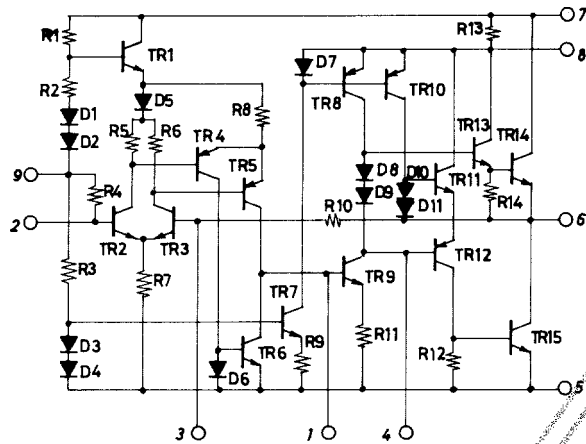
12800TH (KT)/O217KI/4195MW/9300KI, TS No.528-1/5

# LA4140

**Operating Characteristics** at  $T_a = 25^\circ\text{C}$ ,  $V_{CC}=6\text{V}$ ,  $R_L=8\Omega$ ,  $R_g=600\Omega$ ,  $R_f=47\Omega$ ,  $f=1\text{kHz}$

| Parameter                 | Symbol    | Conditions                                           | Ratings |     |     | Unit     |
|---------------------------|-----------|------------------------------------------------------|---------|-----|-----|----------|
|                           |           |                                                      | min     | typ | max |          |
| Quiescent Current         | $I_{CCO}$ |                                                      |         | 11  |     | mA       |
| Voltage Gain              | VG        |                                                      | 47      | 50  | 52  | dB       |
| Output Power              | $P_o$     | THD=1%                                               | 0.45    | 0.5 |     | V        |
| Total Harmonic Distortion | THD       | $P_o=100\text{mW}$                                   |         | 0.3 | 1.0 | %        |
| Input Resistance          | $r_i$     |                                                      |         | 15k |     | $\Omega$ |
| Output Noise Voltage      | $V_{NI}$  | $R_g=10\text{k}\Omega$ , via filter of 50Hz to 20kHz |         | 0.4 | 1.0 | mV       |

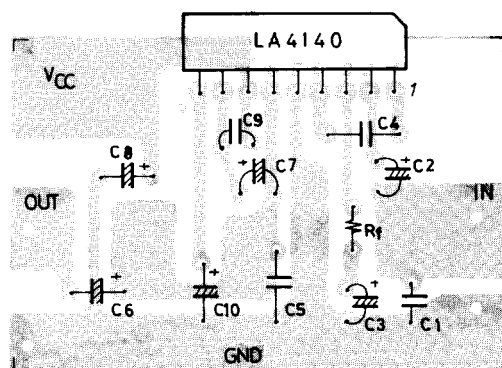
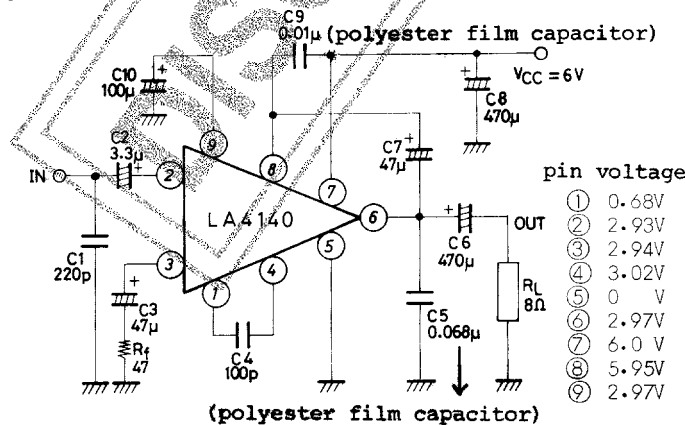
## Equivalent Circuit



## Notice for Using IC

- Maximum Ratings**  
Enough margin converting supply voltage drifting should be prepared and disigning over maximum ratings should be absolutely avoided because operation near these ratings causes going across the ratings and leading to destruction.
- Terminating Pins**  
Turning the circuit on leaving pin to pin of IC shorted causes destruction of failure. Turn on ascertaining that solder has never shorted pins when setting IC to printed board.
- Location**  
When used in a radio receiver, IC is designed to locate apart from a bar antenna enough.
- Printed Pattern Designing**  
As designing a printed pattern, lines of power supply, output adn ground are to be widely short and pattern and peripheral parts are considerably set not to feedback from output to input.  
Also, a capacitor C8 at power line, C5 and C9 of anti-oscillator are to be arranged near to pins of IC.

## Application

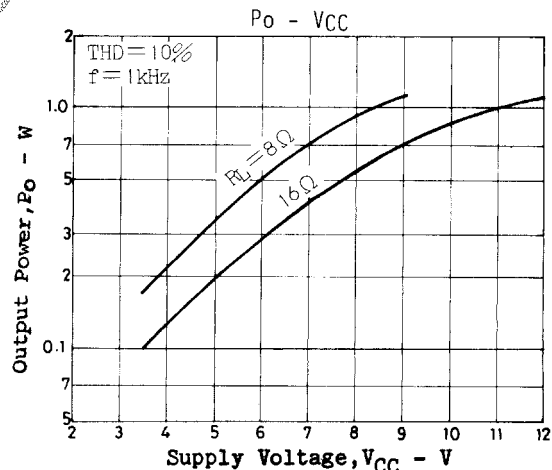
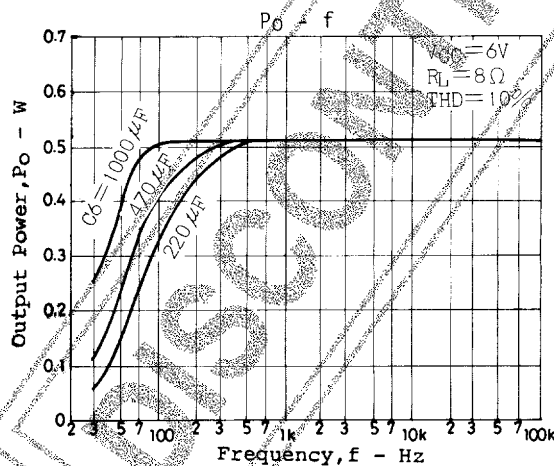
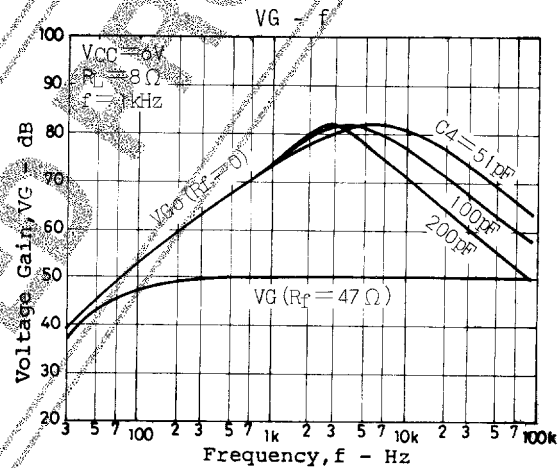
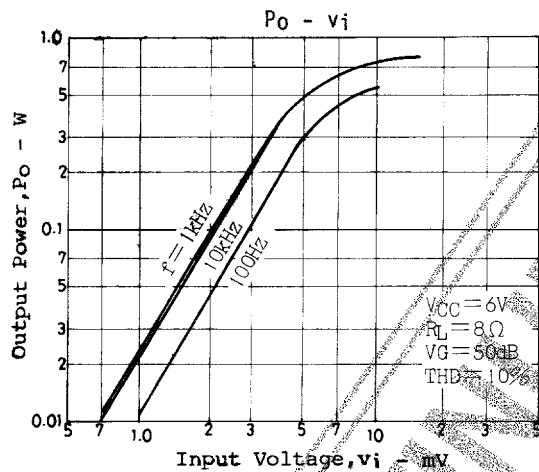


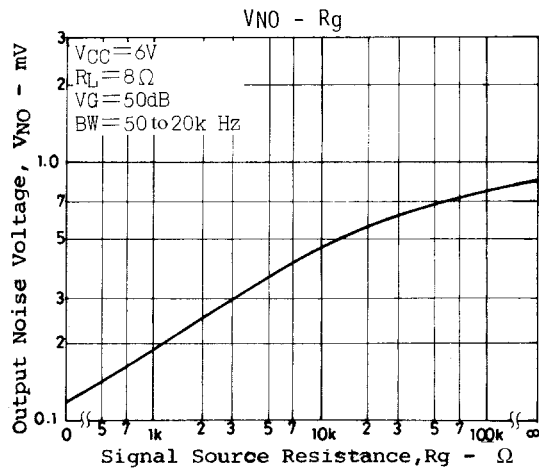
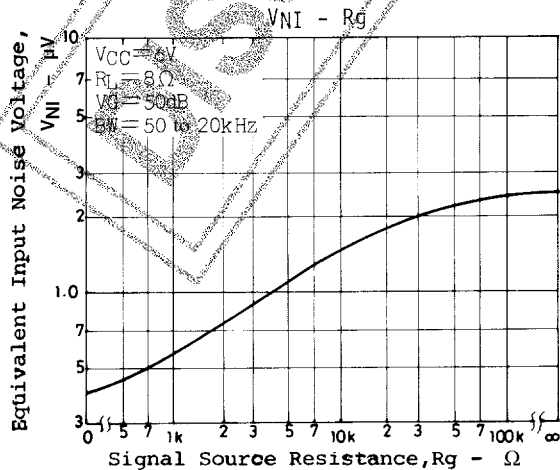
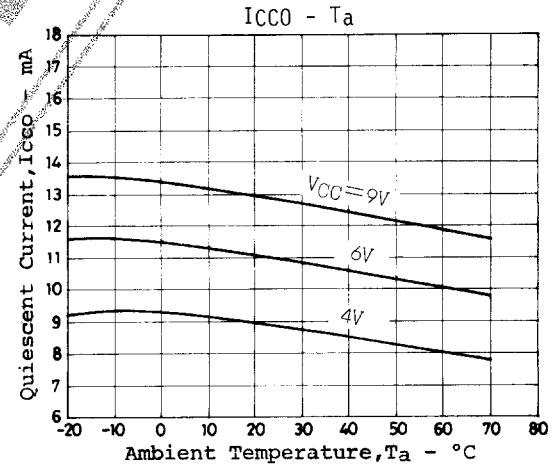
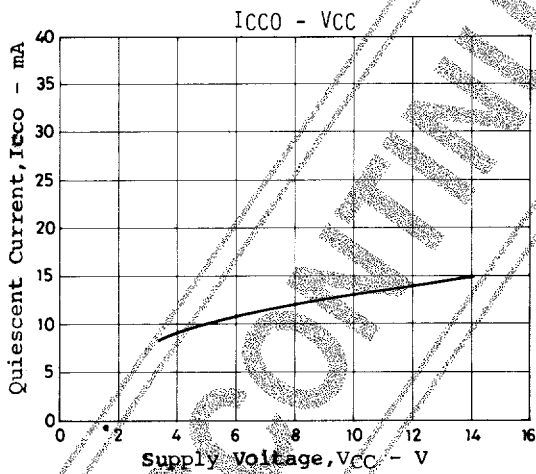
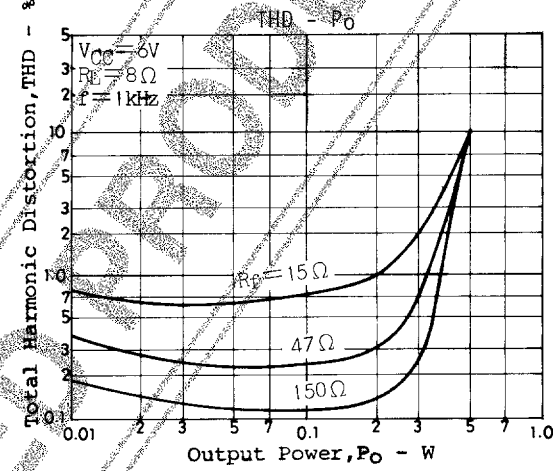
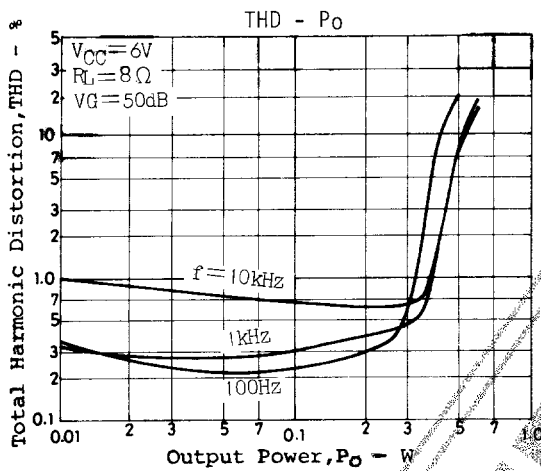
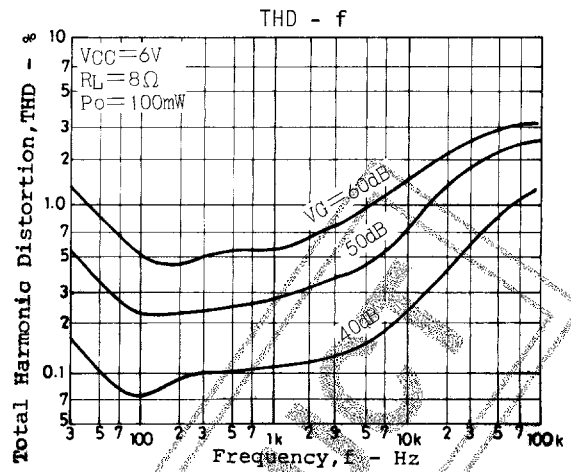
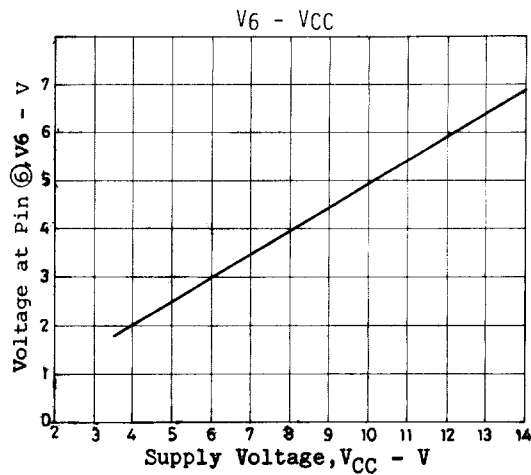
An Example of Printed Pattern  
(bottom view, 35 x 50 mm<sup>2</sup>)

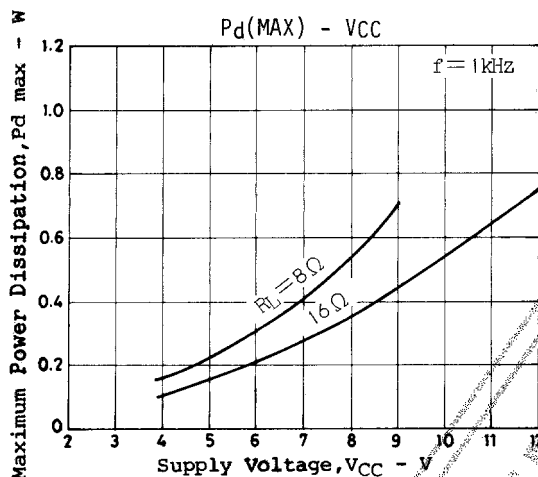
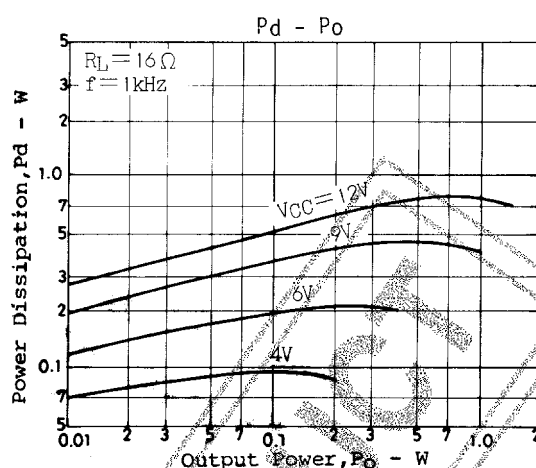
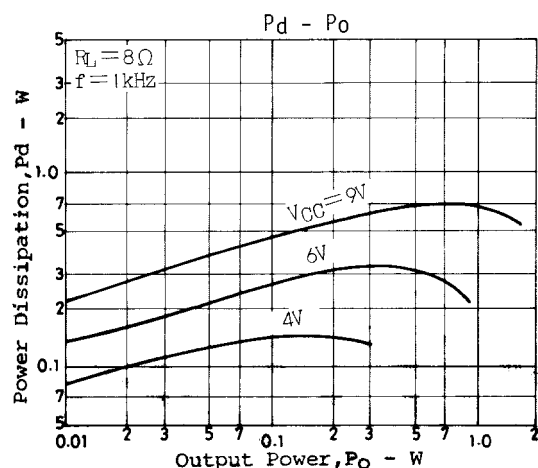
[Peripheral Parts]

- C1, 220pF : Anti-noise capacitor.  
 C2, 3.3μF : Coupling capacitor. Large C2 makes operating noise of variable resistor large. Small one makes frequency response of low frequency range narrow.  
 C3, 47μF : Feedback capacitor. Small C3 makes the starting time short, but frequency response of low range narrow.  
 C4, 100pF : For frequency response adjusting of high range, but excessive small one is apt to oscillate.  
 C5, 0.068μF : For anti-oscillation. Polyester film capacitor is available which has good thermal and high frequency characteristics.  
 C6, 470μF : Output capacitor. It decides power of low frequency.  
 C7, 47μF : Bootstrap capacitor. Excessive small C7 causes wave from clipping point to be unbalanced at low frequency range.  
 C8, 470μF : Filter capacitor.  
 C9, 0.01μF : Anti-oscillation. Polyester film capacitor is available.  
 C10, 100μF : Filter capacitor. Rejects power line hum. Small C10 decreases ripple rejection ratio.  
 $R_f$ , 47Ω : Decides voltage gain. Closed loop voltage gain is nearly calculated as follows. But about ±3dB tolerance should be prepared owing to scatter of internal resistance of IC.  

$$VG = 20 \log (15000/R_f [\Omega]) \text{ [dB]}$$







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