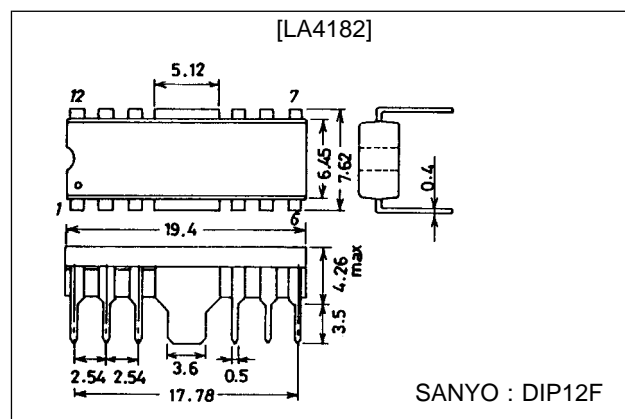


**SANYO****LA4182****2.3 W 2-Channel AF Power Amplifier****Features**

- Built-in 2 channels enabling use in stereo and bridge amplifier applications.
- High output: 2.3 W/channel,  $V_{CC} = 9\text{ V}$ ,  $R_L = 4\ \Omega$ , and 4.7 W/bridge,  $R_L = 8\ \Omega$ .
- Minimum number of external parts required : 9 pcs. min. (Stereo/bridge).
- Small pop noise at the time of power supply ON/OFF due to built-in muting circuit.
- Good ripple rejection ratio due to built-in ripple filter.
- Soft tone at the time of output saturation.
- Good channel separation.
- Voltage gain fixed at 45 dB (Bridge: 51 dB). Variable voltage gain available with external resistor added.
- Easy to design radiator fin.

**Package Dimensions**

unit : mm

**3022A-DIP12F**

Note) In general applications, heat generated in this package can be radiated through the Cu-foiled area of the printed circuit board, but since power dissipation  $P_d$  may be increased depending on the supply voltage and load conditions, it is recommended to use a fin additionally.

**Specifications****Maximum Ratings at  $T_a = 25^\circ\text{C}$** 

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	$V_{CC\text{ max}}$	With signal	11	V
		Quiescent	15	V
Allowable power dissipation	$P_d\text{ max}$	With printed circuit board (Refer to $P_d$ - $T_a$ characteristics)	4	W
Operating temperature	$T_{opr}$		-20 to +75	$^\circ\text{C}$
Storage temperature	$T_{stg}$		-55 to +150	$^\circ\text{C}$

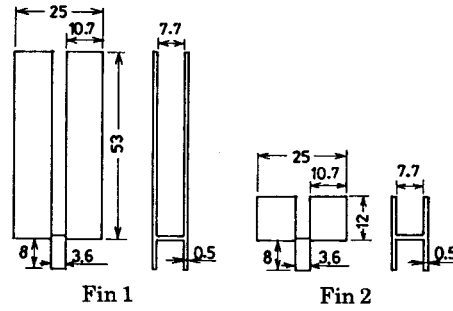
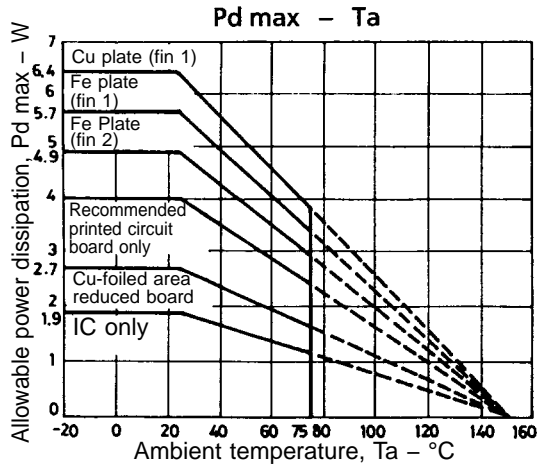
**Recommended Operating Conditions at  $T_a = 25^\circ\text{C}$** 

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	$V_{CC}$		9	V
Load resistance	$R_L$	Stereo	4 to 8	$\Omega$
		Bridge	8	$\Omega$

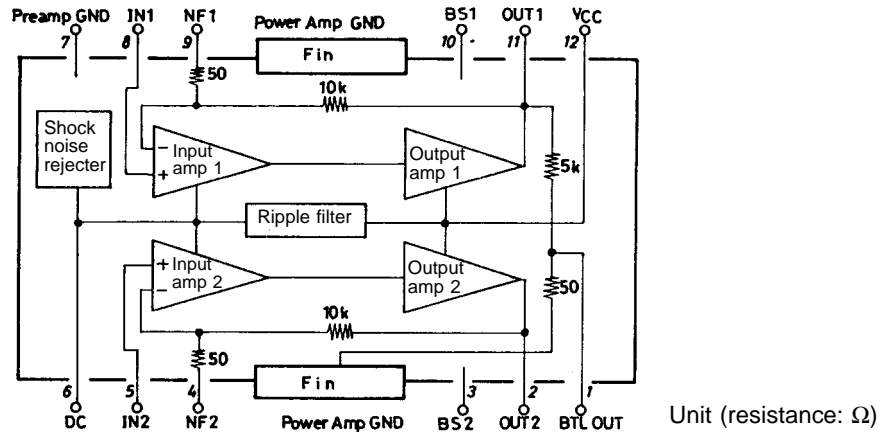
# LA4182

**Operating Characteristics at  $T_a = 25^\circ\text{C}$ ,  $V_{CC} = 9\text{ V}$ ,  $f = 1\text{ kHz}$ ,  $R_L = 4\ \Omega$ ,  $R_g = 600\ \Omega$ , ( ):  $8\ \Omega$ ,  
See specified Test Circuit.**

Parameter	Symbol	Conditions	min	typ	max	Unit
Quiescent current	$I_{CCO}$	Stereo Stereo		40	55	mA
Voltage gain	VG	Closed loop, $V_{IN} = -45\text{ dB}$ Stereo	43	45	47	dB
		Closed loop, $V_{IN} = -45\text{ dB}$ Bridge	49	51	53	dB
Voltage gain difference	$\Delta VG$	Stereo			$\pm 1$	dB
Output power	$P_O$	THD = 10% Stereo	1.7	2.3 (1.3)		W
		THD = 10% Bridge		(4.7)		W
Total harmonic distortion	THD	$P_O = 250\text{ mW}$ Stereo		0.3	1.5	%
		$P_O = 250\text{ mW}$ Bridge		0.5		%
Input resistance	$r_i$		21	30		k $\Omega$
Output noise voltage	$V_{NO}$	$R_g = 0$ Stereo		0.3	1.0	mV
		$R_g = 10\text{ k}\Omega$ Stereo		0.5	2.0	mV
Ripple rejection ratio	$R_r$	$R_g = 0$ , $V_R = 150\text{ mV}$ Stereo	40	46		dB
Channel separation	CHsep	$R_g = 10\text{ k}\Omega$ , $V_O = 0\text{ dB}$ Stereo	40	55		dB

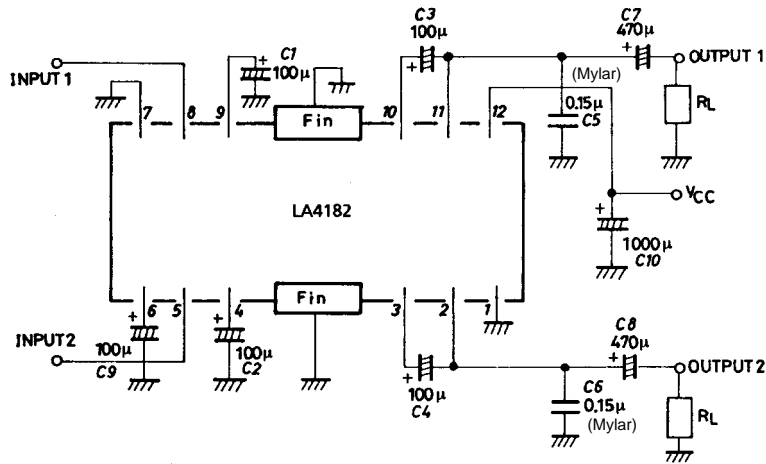


## Pin Assignment and Equivalent Circuit

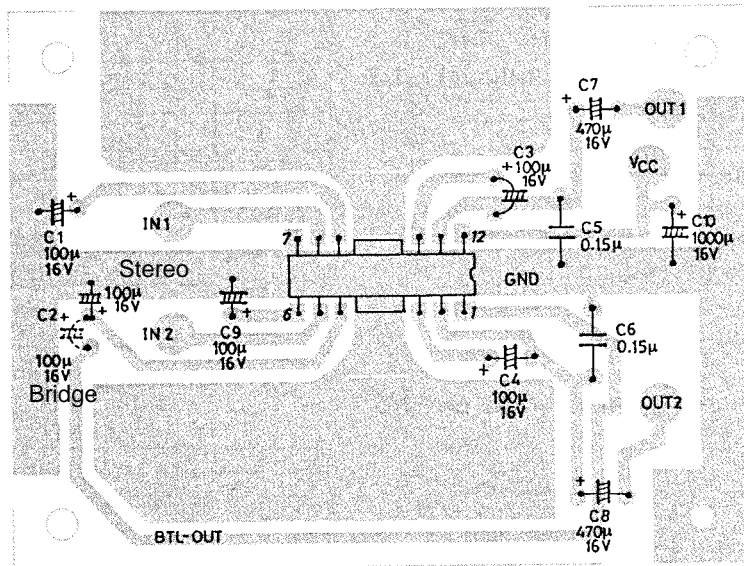


# LA4182

## Sample Application Circuit 1 : Stereo



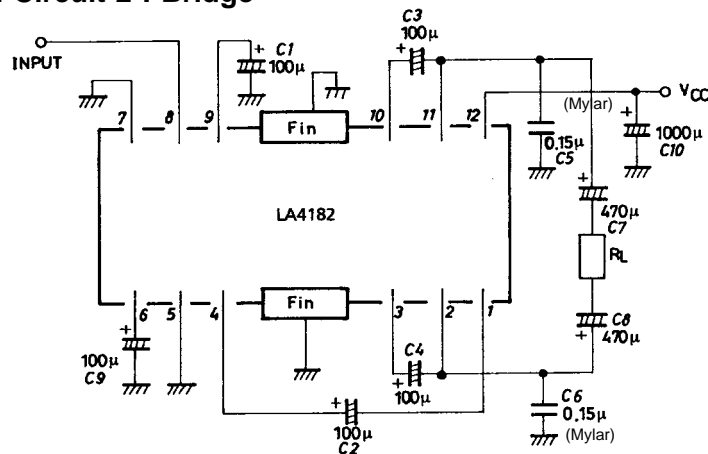
Unit (capacitance: F)



Unit (capacitance: F)

Example of printed pattern (bottom view) for use in stereo, bridge amplifier applications : 60 × 80 mm<sup>2</sup>

## Sample Application Circuit 2 : Bridge



Unit (capacitance: F)

**Description of External Parts**

C1(C2)	Feedback capacitor	The low cutoff frequency depends on this capacitor. If the capacity is increased, the starting time is delayed.
C3(C4)	Bootstrap capacitor	If the capacity is decreased, the output at low frequencies goes lower.
C5(C6)	Oscillation preventing capacitor	Polyester film capacitor, being good in temperature characteristic, frequency characteristic, is used. If an aluminum electrolytic capacitor or ceramic capacitor is used, oscillation may occur at low temperatures.
C7(C8)	Output capacitor	The low cutoff frequency depends on this capacitor. In order for the low frequency characteristic in the bridge amplifier to be equal to that in the stereo amplifier application, the capacity must be doubled.
C9	Decoupling capacitor	Used for the ripple filter. Since the rejection effect is saturated at a certain capacity, it is meaningless to increase the capacity more than needed. This capacitor, being also used for the time constant of the muting circuit, affects the starting time.
C10	Power source capacitor	

**Application Circuit**

**1. Voltage gain adjustment**

◆ Stereo

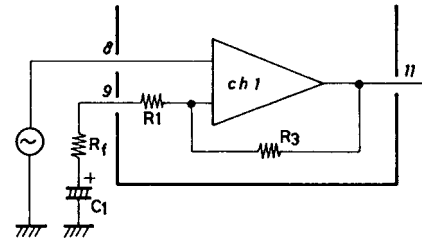
The voltage gain depends on built-in resistors R1 (R2), R3 (R4) as follows :

$$VG = 20\log \frac{R3 (R4)}{R1 (R2)} \text{ [dB]}$$

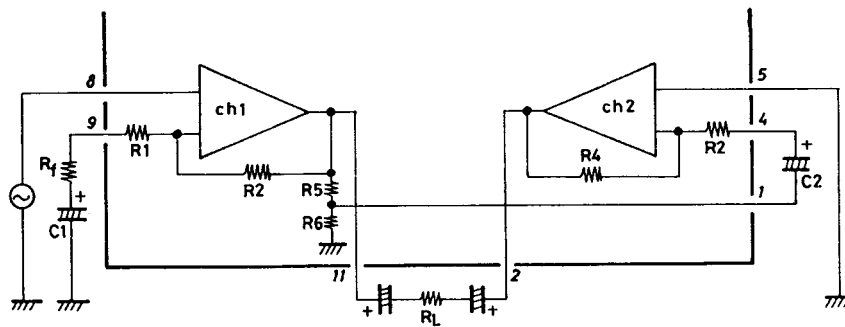
If the IC is used at a voltage gain less than this, the following equation with Rf added applies.

$$VG = 20\log \frac{R3 (R4)}{R1 (R2) + Rf} \text{ [dB]}$$

where R1 (R2) = 50 Ω, R3 (R4) = 10 kΩ



◆ Bridge



The above shows the bridge amplifier configuration, where ch.1 operates as a noninverting amplifier and ch.2 as an inverting amplifier. The output of ch.1 is divided with R5, R6 and led to pin 1 and then input to ch.2.

Since the attenuation degree (R5/R6) of ch. 1 output and the amplification degree (R4/R2 + R6) of ch. 2 are fixed at an equal value, the ch.2 output is in opposite phase with the ch. 1 output. Therefore, the total voltage gain gets apparently 6 dB higher than the voltage gain of ch.1 alone and is determined by the following equation.

$$VG = 20\log \frac{R3}{R1} + 6 \text{ [dB]}$$

If the IC is used at a voltage gain less than this, the following equation with Rf added applies.

$$VG = 20\log \frac{R3}{R1 + Rf} + 6 \text{ [dB]}$$



(2) AC power supply

$$P_{d \max} = \frac{V_{CC} (Pd)^2}{\pi^2 R_L} + I_{CC0} \cdot V_{CC} (Pd) \text{ (For stereo)}$$

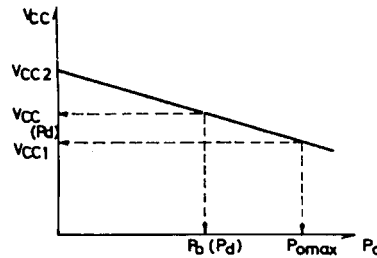
$V_{CC2}$  : Quiescent supply voltage

$$V_{CC} (Pd) : \text{Supply voltage at max. output, } V_{CC} (Pd) = \frac{(1+r)V_{CC1}}{1 + \frac{r \cdot V_{CC1}}{\sqrt{2} \cdot \pi \cdot R_L} \times \sqrt{\frac{R_L}{P_{O \max}}}}$$

$V_{CC1}$  : Supply voltage at max. output

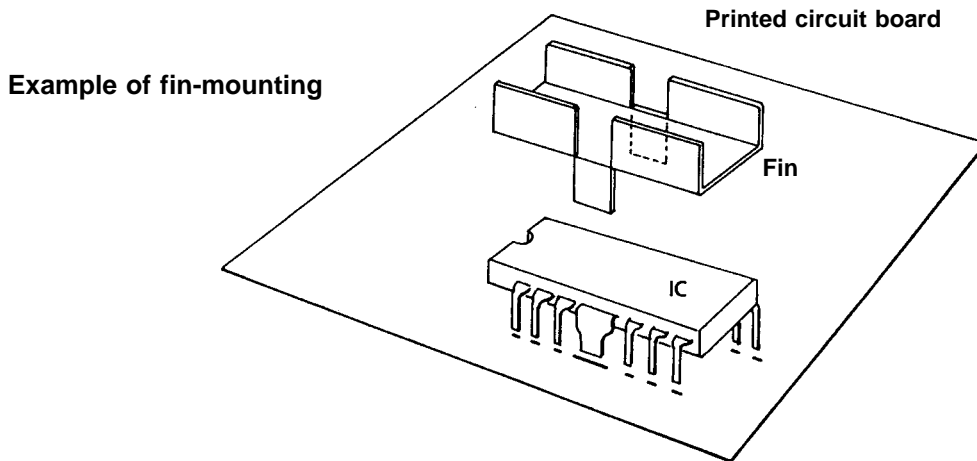
$r$  : Voltage regulation,  $\frac{V_{CC2} - V_{CC1}}{V_{CC1}}$

$I_{CC0}$  : Quiescent current



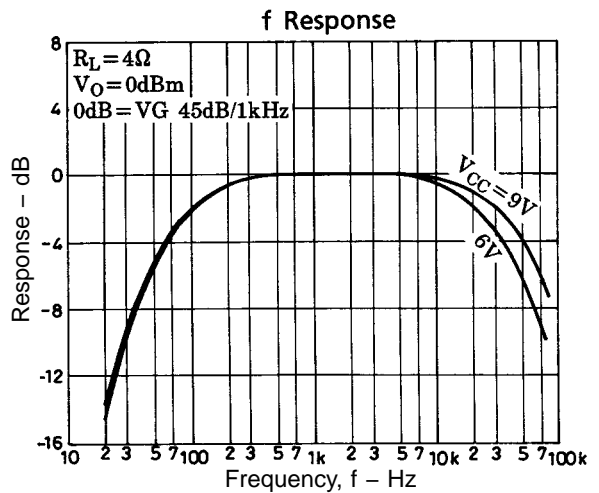
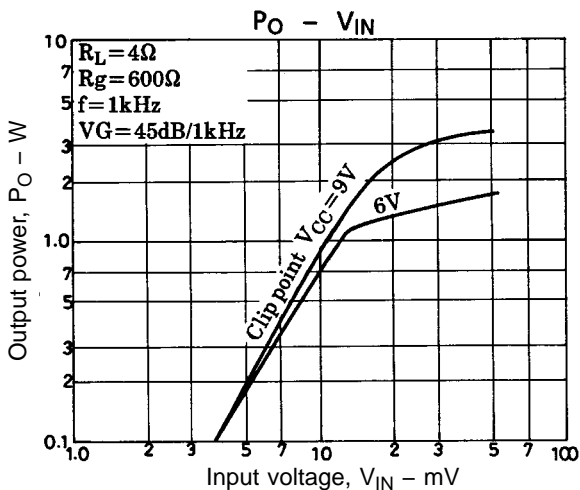
Example of fin mounting

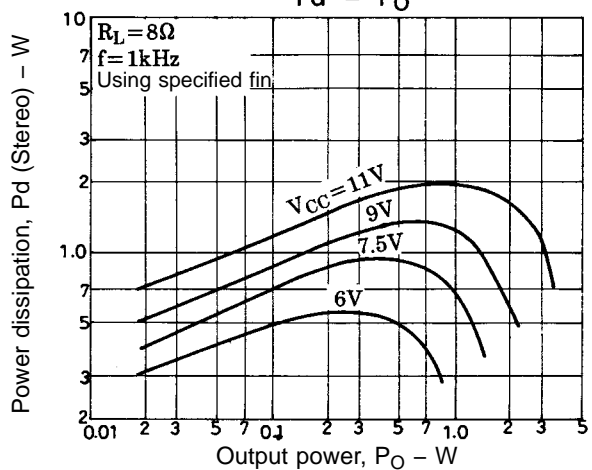
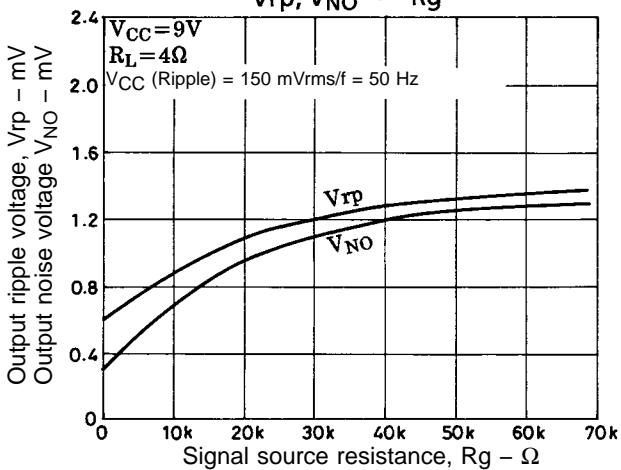
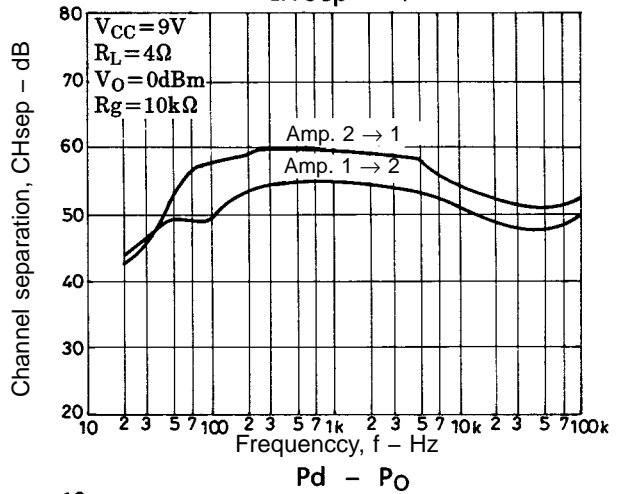
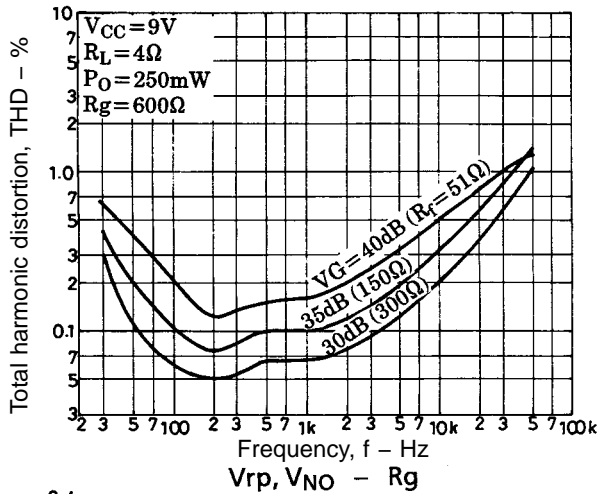
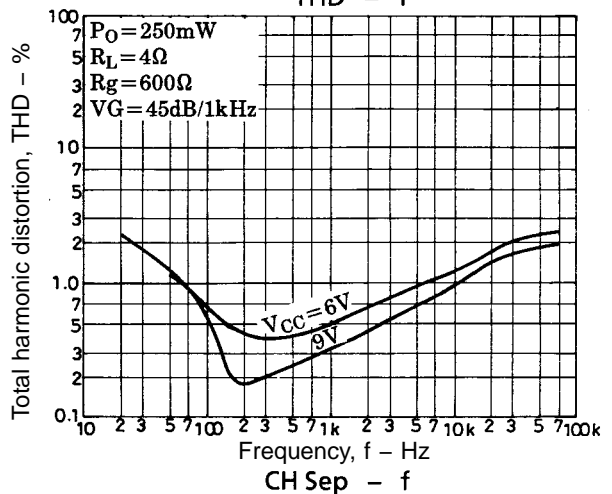
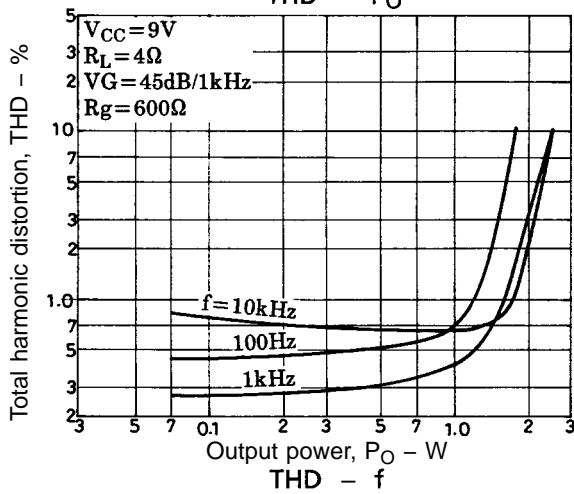
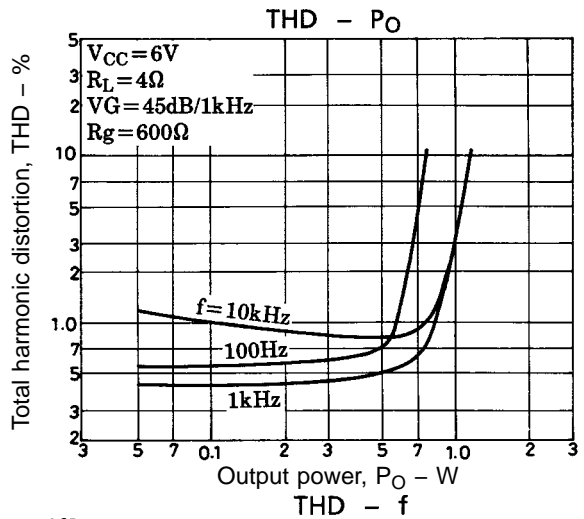
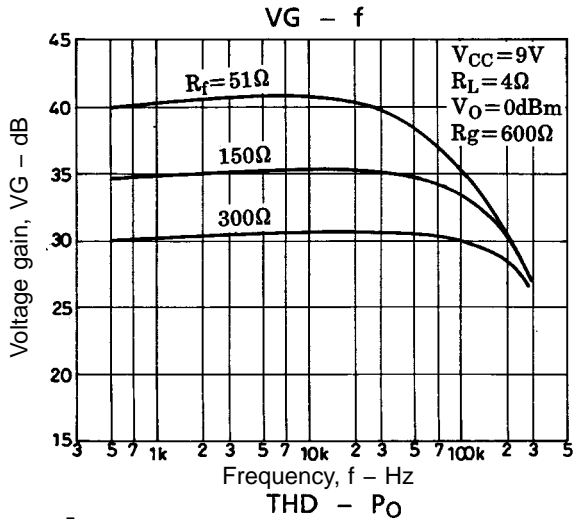
The fin is formed into such a shape as to be able to radiate heat from the plastic fin area of IC and the fin as shown below and is soldered to the printed circuit board. For the fin size, refer to the Pd max-Ta characteristics. The desirable material is copper or iron which is solderable. It is recommended to apply silicone grease, etc. to the plastic area of IC in order to minimize the thermal resistance.

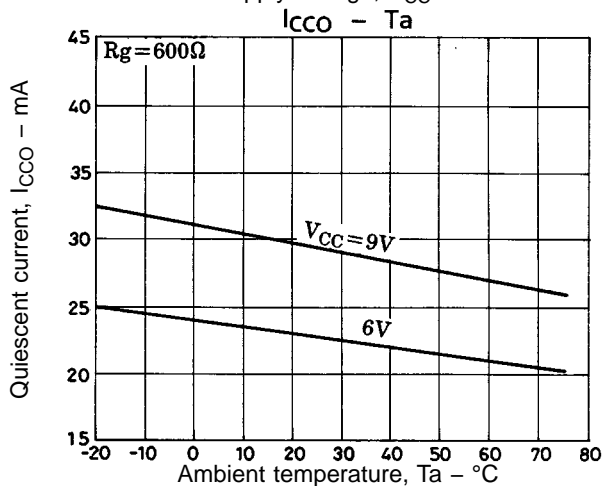
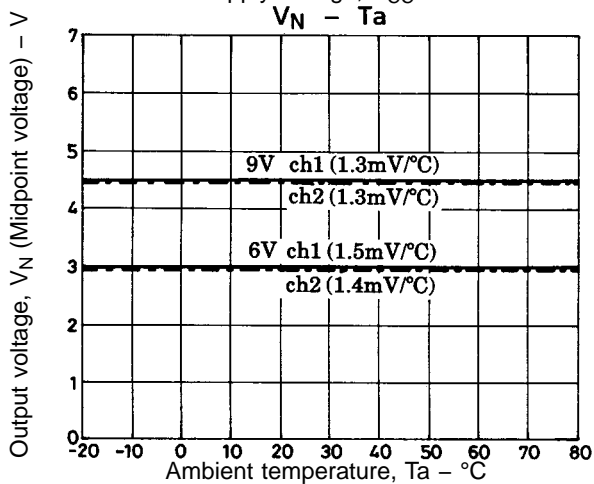
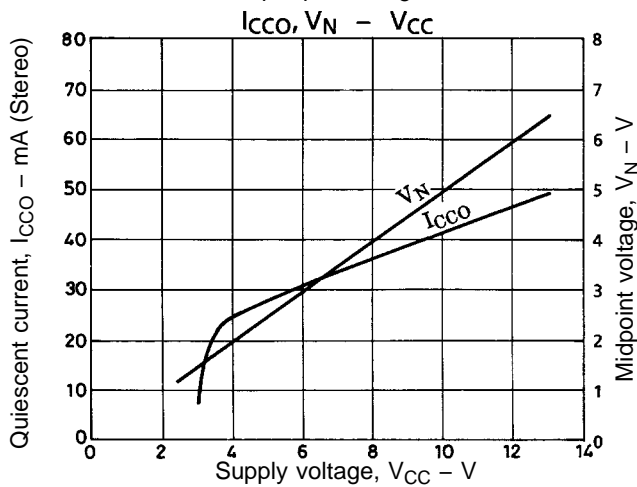
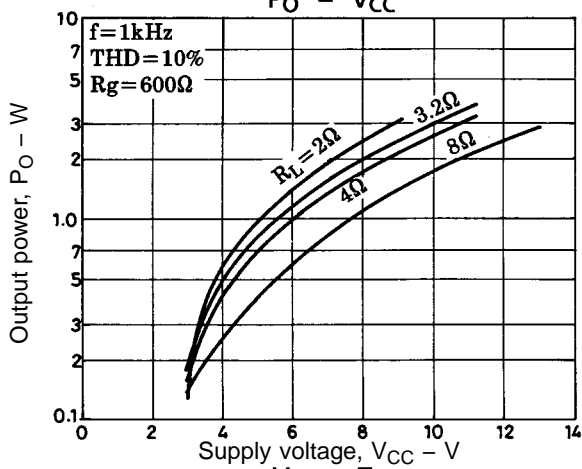
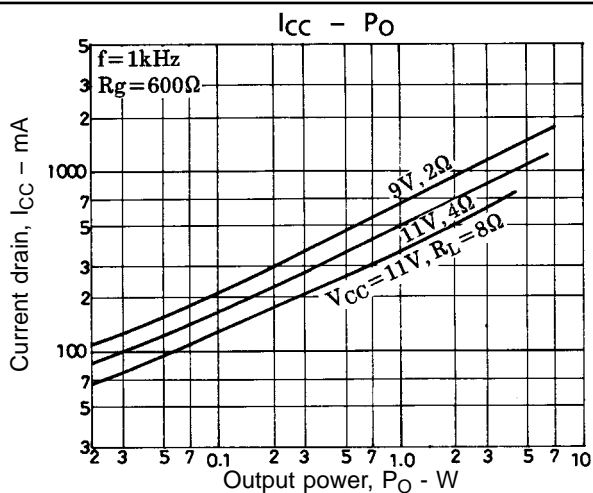
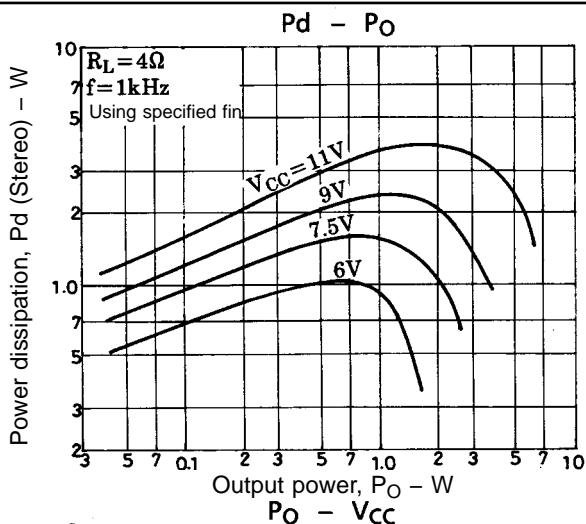


Example of fin-mounting

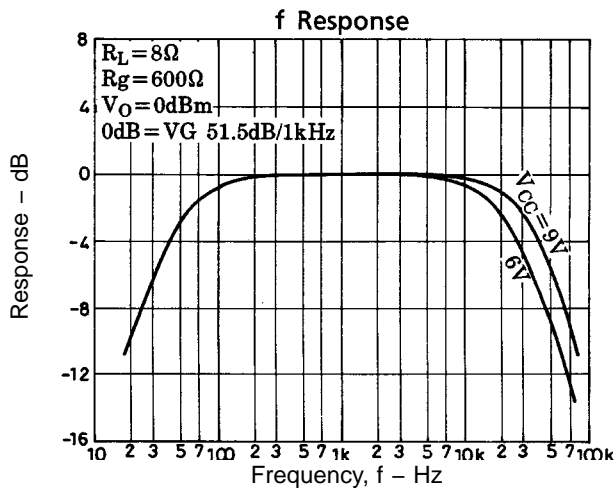
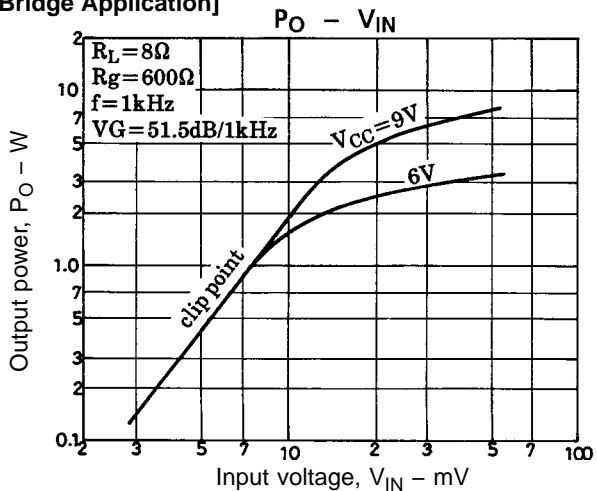
[Stereo Application]



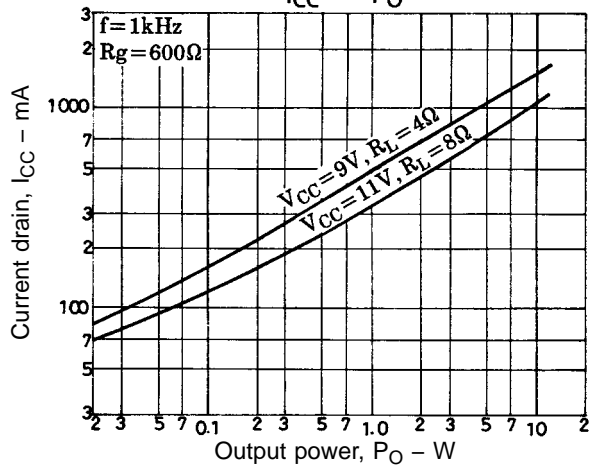
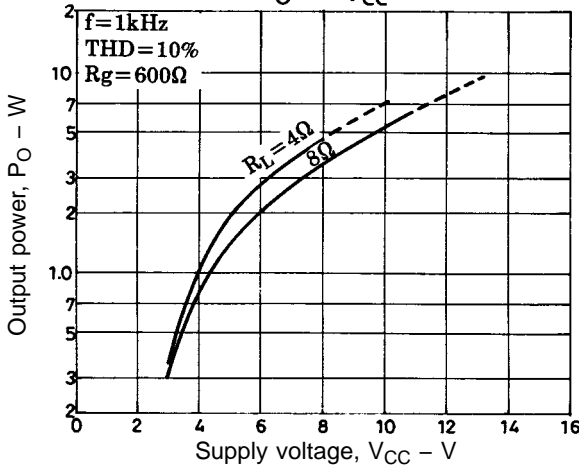
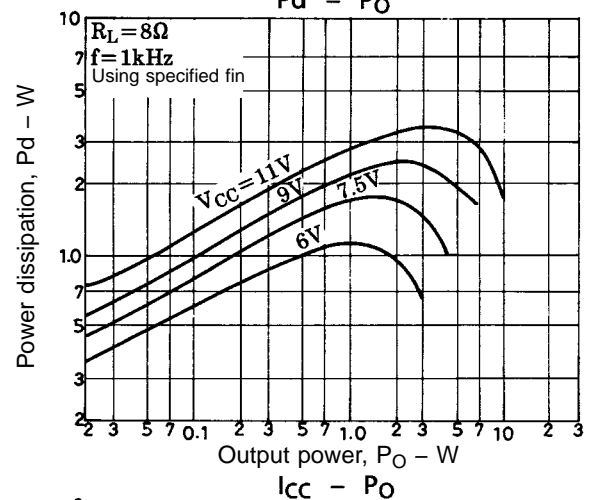
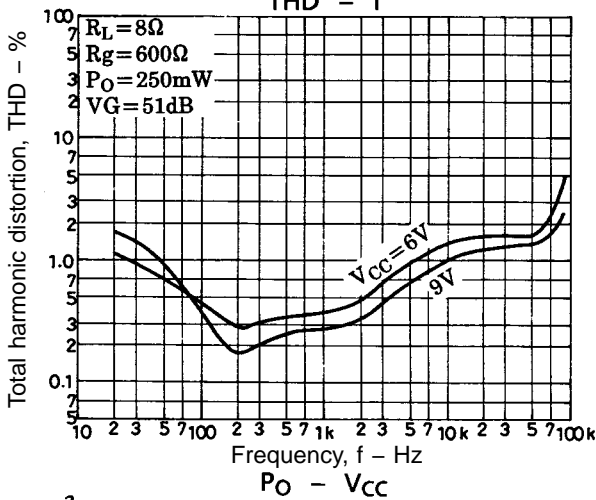
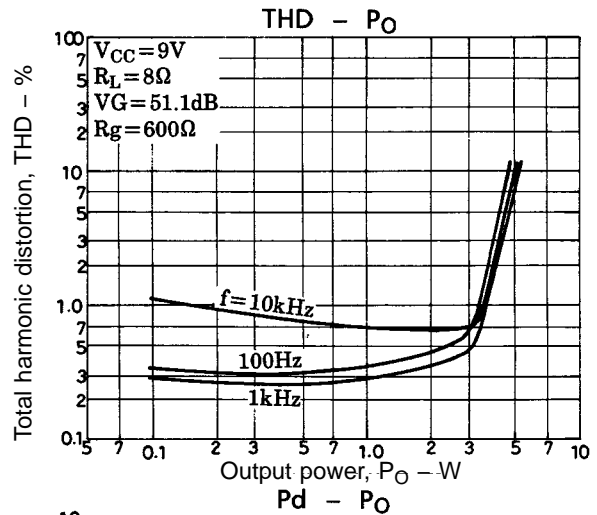
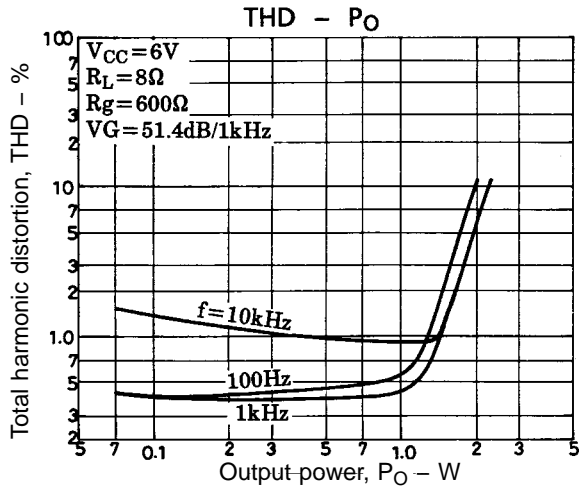




[Bridge Application]







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