

8:1 Mux/DeMux BusSwitch

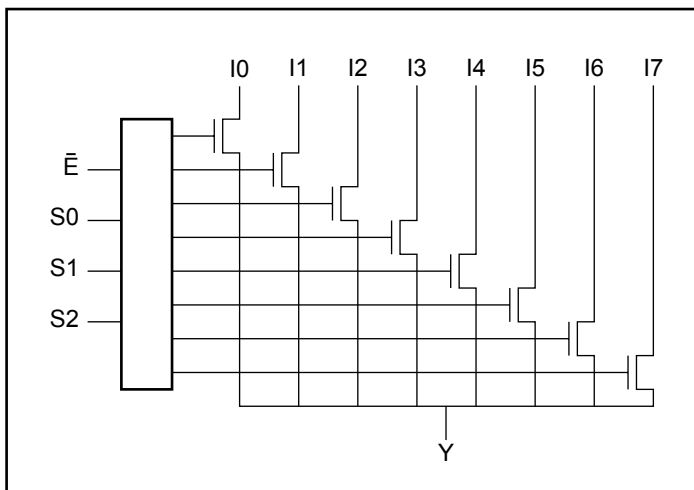
Features:

- Near-Zero propagation delay
- 5Ω switches connect inputs to outputs
- Direct bus connection when switches are ON
- Ultra Low Quiescent Power (0.2 μA Typical)
– Ideally suited for notebook applications
- Pin compatible with 74 series 251 logic devices
- Packaging (Pb-free & Green available):
– 16-pin 150-mil wide plastic QSOP (Q)
– 16-pin 300-mil wide plastic SOIC (S)
– 16-pin 150-mil wide plastic SOIC (W)
– 16-pin 173-mil wide plastic TSSOP (L)

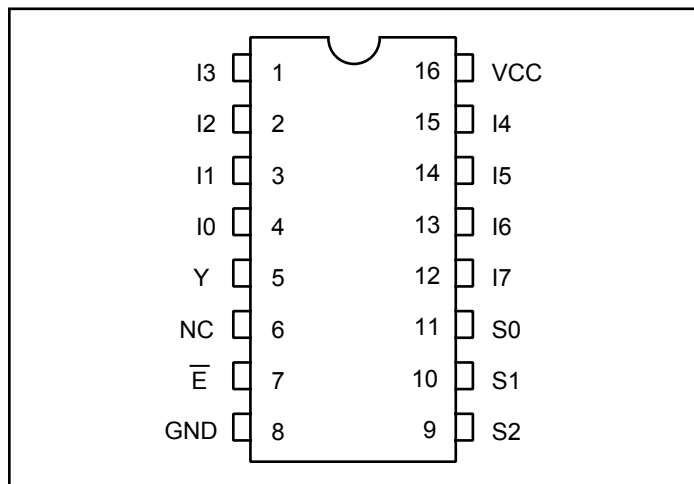
Description:

Pericom Semiconductor's PI5C3251 is a Dual 8:1 Multiplexer/demultiplexer with three-state outputs that is pinout compatible with the PI74FCT251T, 74F251, and 74ALS/AS/LS 251. Inputs can be connected to outputs with low on resistance (5Ω) with no additional ground bounce noise or propagation delay.

Block Diagram



Pin Configuration



Truth Table⁽¹⁾

Select				Y	Function
E	S2	S1	S0		
H	X	X	X	Hi-Z	Disable
L	L	L	L	I0	S2-0 = 0
L	L	L	H	I1	S2-0 = 1
L	L	H	L	I2	S2-0 = 2
L	L	H	H	I3	S2-0 = 3
L	H	L	L	I4	S2-0 = 4
L	H	L	H	I5	S2-0 = 5
L	H	H	L	I6	S2-0 = 6
L	H	H	H	I7	S2-0 = 7

Note:

1. H = High Voltage Level, L = Low Voltage Level

Pin Description

Pin Name	Description
I0–7	Data Inputs
S0–2	Select Inputs
$\bar{E}$	Enable
Y	Data Outputs
GND	Ground
VCC	Power

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential (Inputs & V _{CC} Only)	–0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)	–0.5V to +7.0V
DC Input Voltage	–0.5V to +7.0V
DC Output Current	120 mA
Power Dissipation	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, T_A = –40°C to +85°C, V_{CC} = 5V ±5%)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units
V _{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V _{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	–0.5		0.8	
I _{IH}	Input HIGH Current	V _{CC} = Max., V _{IN} = V _{CC}			±1	μA
I _{IL}	Input LOW Current	V _{CC} = Max., V _{IN} = GND			±1	
I _{OZH}	High Impedance Output Current	0 ≤ I, Y ≤ V _{CC}			±1	
V _{IK}	Clamp Diode Voltage	V _{CC} = Min., I _{IN} = –18 mA		–0.7	–1.2	V
I _{OS}	Short Circuit Current ⁽³⁾	I (Y) = 0V, Y (I) = V _{CC}	100			mA
V _H	Input Hysteresis at Control Pins			150		mV
R _{ON}	Switch On-Resistance ⁽⁴⁾	V _{CC} = Min., V _{IN} = 0.0V, I _{ON} = 48 mA		5	7	Ω
		V _{CC} = Min., V _{IN} = 2.4V, I _{ON} = 15 mA		10	15	

Capacitance (T_A = 25°C, f = 1 MHz)

Parameters ⁽⁵⁾	Description	Test Conditions	Typ.	Max.	Units
C _{IN}	Input Capacitance	V _{IN} = 0V		6	pF
C _{I(OFF)}	I ₀ - I ₇ Capacitance, Switch Off			6	
C _{Y(OFF)}	Y Capacitance, Switch Off			48	
C _{I(ON)}	I ₀ - I ₇ Capacitance, Switch On		35	54	

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at V_{CC} = 5.0V, T_A = 25°C ambient and maximum loading.
- Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- Measured by the voltage drop between I and Y pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (I,Y) pins.
- This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} = Max.	V _{IN} = GND or V _{CC}		0.1	3.0	μA
ΔI _{CC}	Supply Current per Input @ TTL HIGH	V _{CC} = Max.	V _{IN} = 3.4V ⁽³⁾			2.5	mA
I _{CCD}	Supply Current per Input per MHz ⁽⁴⁾	V _{CC} = Max., I and Y Pins Open E = GND Control Input Toggling 50% Duty Cycle				0.25	mA/ MHz

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at V_{CC} = 5.0V, +25°C ambient.
3. Per TTL driven input (V_{IN} = 3.4V, control inputs only); I and Y pins do not contribute to I_{CC}.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The I and Y inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

Switching Characteristics over Operating Range

Parameters	Description	Conditions ⁽¹⁾	Com.		Unit
			Min.	Max.	
t _{IY}	Propagation Delay ^(2,3) In to Y	C _L = 50 pF R _L = 500Ω		0.25	ns
t _{SY}	Bus Select Time S _n to Y		0.5	6.6	
t _{PZH} t _{PZL}	Bus Enable Time E to Y		0.5	6.0	
t _{PHZ} t _{PLZ}	Bus Disable Time E to Y		0.5	6.0	

Notes:

1. See test circuit and wave forms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

16

.150
.157

3.81
3.99

1

.189
.197

4.80
5.00

.008
0.203 REF

.053
.069

1.35
1.75

SEATING PLANE

.025
BSC
0.635

.008
.012

0.203
0.305

.004
.010

0.101
0.254

X.XX
X.XX

DENOTES DIMENSIONS IN MILLIMETERS

15' MAX.

.008
0.20 MIN.

.008
.013
0.20
0.33

Guage Plane

.010
0.254

15' MAX.

.016
.035
0.41
0.89

b'-6'

.041
1.04 REF

Detail A

.015 x 45°

0.38

Detail A

.041
1.27

.016
.050

.007
.010

0.178
0.254

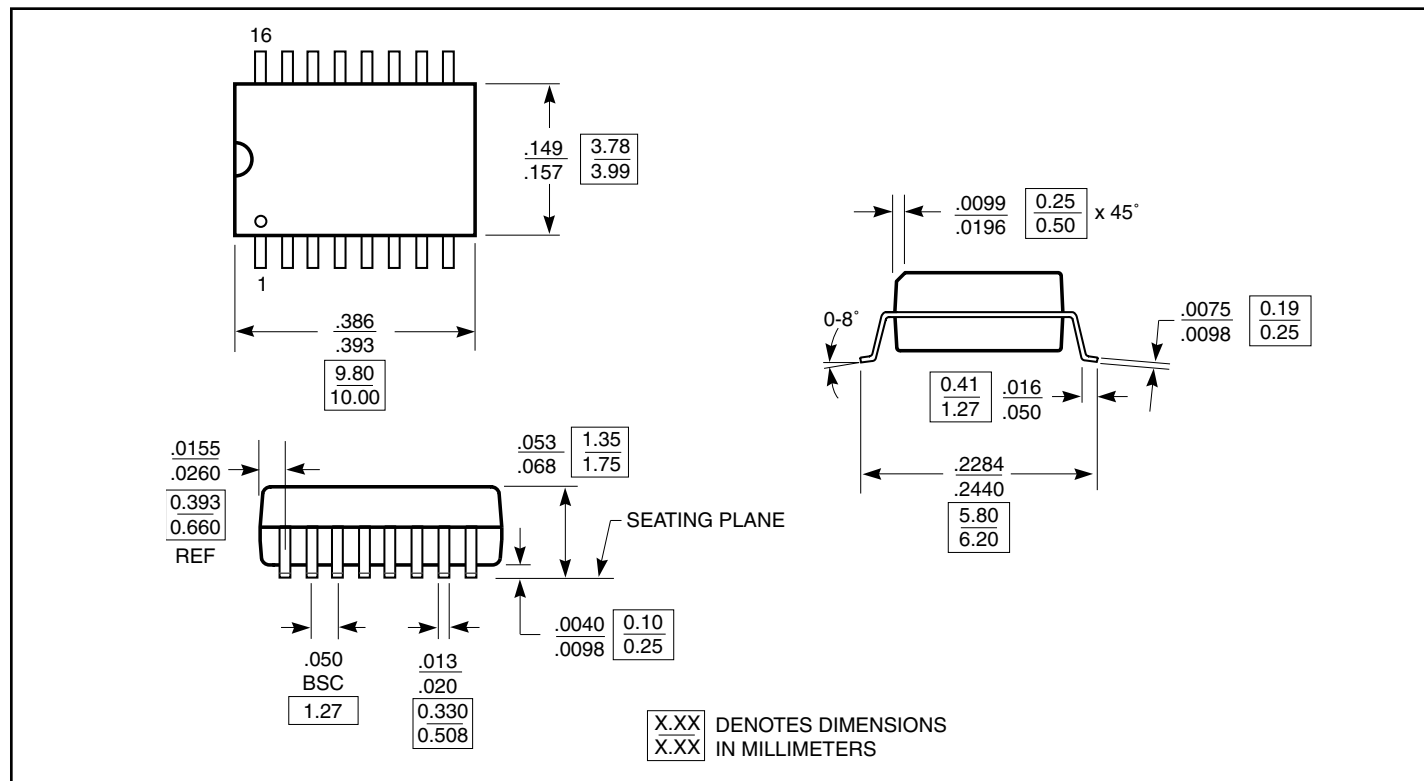
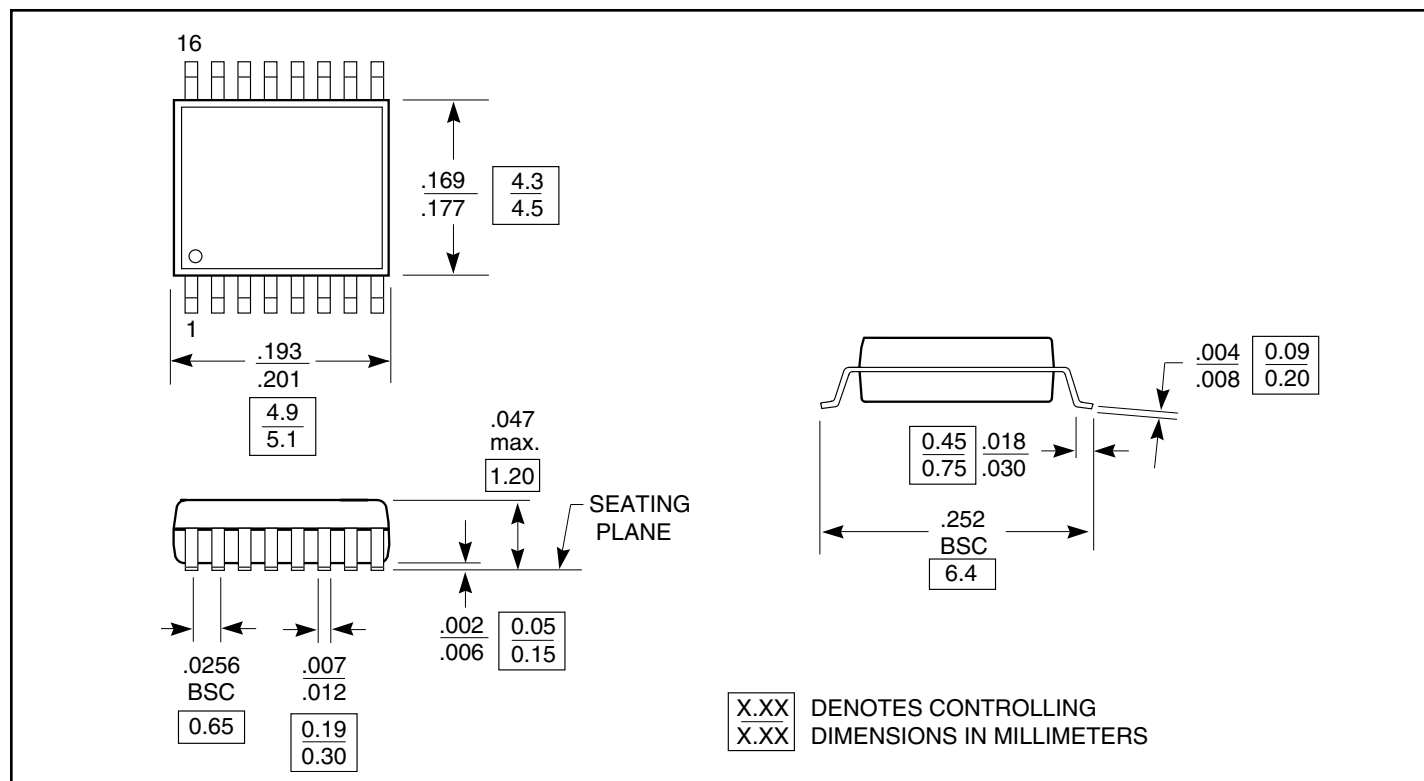
.228
.244

5.79
6.19

The image displays three mechanical drawings of a 16-pin DIP package:

- Top View:** Shows the package with 16 pins. Dimensions include a width of .2914 to .2992 inches (7.40 to 7.60 mm), a pin pitch of .020 inches (0.508 mm), and a body width of .398 to .413 inches (10.10 to 10.50 mm).
- Side View:** Shows the package height and pin dimensions. The height is .0926 to .1043 inches (2.35 to 2.64 mm). The pin height is .0040 to .0118 inches (0.10 to 0.30 mm). The pin width is .013 to .020 inches (0.33 to 0.51 mm). The body width is .050 inches (1.27 mm).
- Cross-sectional View:** Shows the internal structure of the package. Dimensions include a top width of .010 to .029 inches (0.254 to 0.737 mm) at a 45-degree angle, a body width of .394 to .419 inches (10.00 to 10.65 mm), and a bottom width of .016 to .050 inches (0.41 to 1.27 mm).

A legend indicates that dimensions in the format X.XX denote dimensions in millimeters.

Packaging Mechanical: 16-pin SOIC (W)

Packaging Mechanical: 16-pin TSSOP (L)


Ordering Information

Ordering Code	Package Code	Package Description
PI5C3251Q	Q	16-pin 150-mil wide QSOP
PI5C3251QE	Q	Pb-free & Green, 16-pin 150-mil wide QSOP
PI5C3251S	S	16-pin 300-mil wide SOIC
PI5C3251SE	S	Pb-free & Green, 16-pin 300-mil wide SOIC
PI5C3251W	W	16-pin 150-mil wide SOIC
PI5C3251WE	W	Pb-free & Green, 16-pin 150-mil wide SOIC
PI5C3251L	L	16-pin 173-mil wide TSSOP
PI5C3251LE	L	Pb-free & Green, 16-pin 173-mil wide TSSOP

Notes:

1. Thermal characteristics can be found on the company web site at www.pericom.com/packaging/