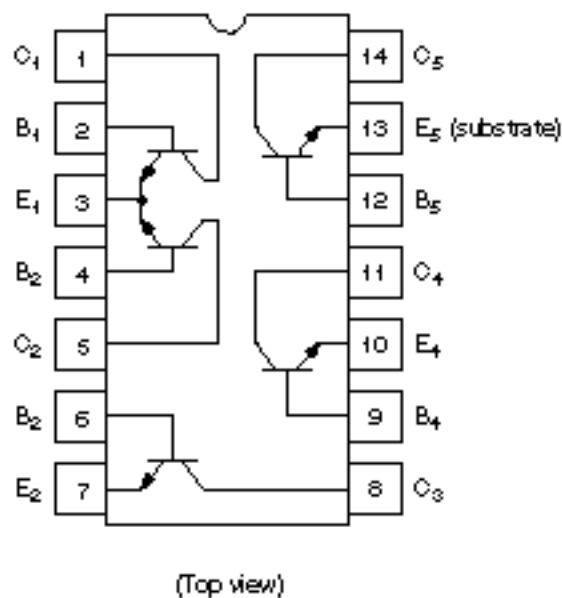

HA1127, HA1127P, HA1127FP

5 Transistor Arrays

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Pin Arrangement



Note: Use pin 13 as the lowest potential for this IC.

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Absolute Maximum Ratings (Ta = 25°C)

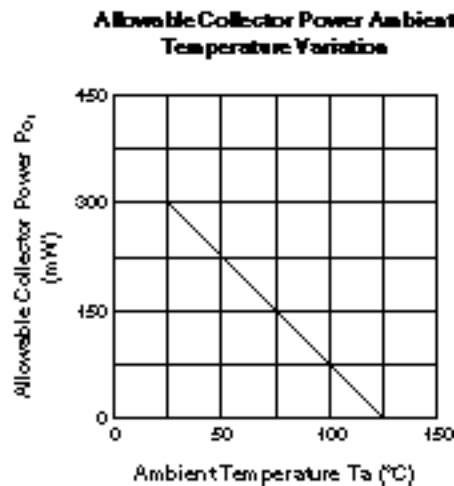
Item	Symbol	HA1127	Unit
Collector base voltage	V_{CBO}	20	V
Collector substrate voltage	V_{CIO}	20	V
Collector emitter voltage	V_{CEO}	15	V
Emitter-base voltage	V_{EBO}	5	V
Collector current	I_C	50	mA
Allowable collector power	P_C^{*1}	300	mW
Allowable collector power	P_C	750 ^{*2} 625 ^{*3}	mW
Operating temperature	Topr	-55 to +125	°C
Storage temperature	Tstg	-55 to +125	°C

Notes: 1. Allowable value per individual transistor. This is the allowable value up to Ta = 25°C. Derate at 3 mW/°C above that temperature.

2. Allowable value for the whole package.

This is the allowable value up to Ta = 35°C for the HA1127P. Derate at 8.3 mW/°C above that temperature.

3. See page 51.



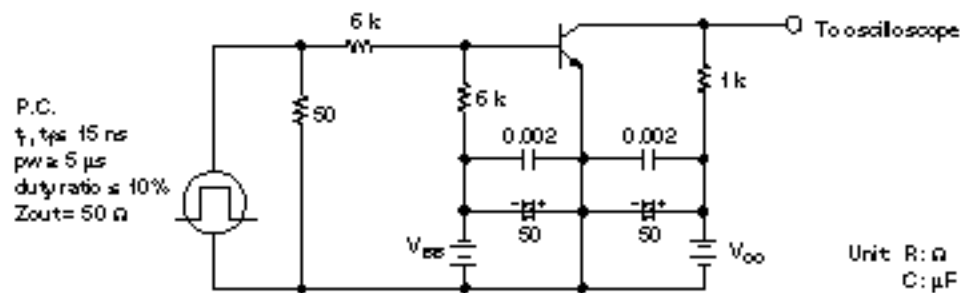
HA1127, HA1127P, HA1127FP

Electrical Characteristics (Ta = 25°C)

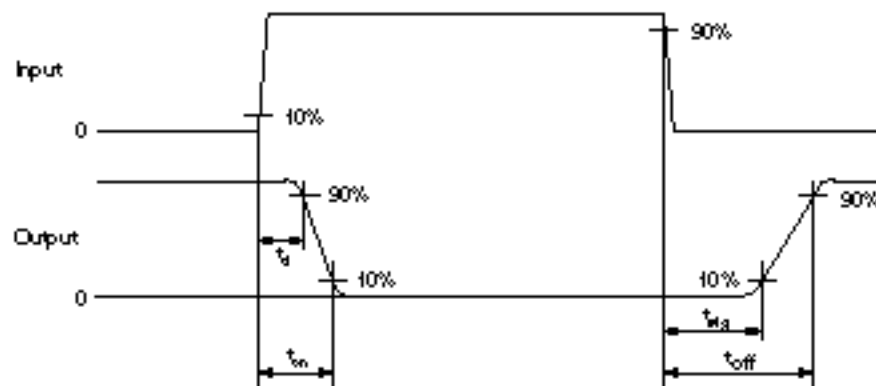
Item	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-base breakdown voltage	$V_{(BR)CBO}$	20	—	—	V	$I_C = 10 \mu A, I_E = 0$
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	15	—	—	V	$I_C = 1 mA, R_{BE} =$
Collector-substrate breakdown voltage	$V_{(BR)CIO}$	20	—	—	V	$I_C = 10 \mu A, I_E = 0, I_B = 0$
Emitter-base breakdown voltage	$V_{(BR)EBO}$	5	—	—	V	$I_E = 10 \mu A, I_C = 0$
Collector cutoff current	I_{CBO}	—	0.002	40	nA	$V_{CB} = 10 V, I_E = 0$
	I_{CEO}	—	—	0.5	μA	$V_{CE} = 10 V, R_{BE} =$
Collector-emitter saturation voltage	$V_{CE(sat)}$	—	0.17	—	V	$I_C = 10 mA, I_B = 1 mA$
Base-emitter voltage	V_{BE}	—	0.72	—	V	$V_{CE} = 3 V, I_C = 1 mA$
		—	0.80	—	V	$I_C = 10 mA$
DC current amplification ratio	h_{FE}	40	140	—		$V_{EE} = 3 V, I_C = 1 mA$
		—	120	—		$I_C = 10 mA$
Gain-bandwidth product	f_T	—	460	—	MHz	$V_{CE} = 3 V, I_C = 3 mA$
Collector output capacitance	C_{ob}	—	1.7	—	pF	$V_{CB} = 3 V, I_E = 0, f = 1 MHz$
Emitter input capacitance	C_{in}	—	2.0	—	pF	$V_{CB} = 3 V, I_E = 0, f = 1 MHz$
Switching time	t_{on}	—	35	—	ns	$V_{CC} = 10 V, I_C = 10 I_{B1} = -10 I_{B2} = 10 mA$
	t_{off}	—	130	—	ns	
	t_{stg}	—	75	—	ns	

HA1127, HA1127P, HA1127FP

Switching Time Test Circuit



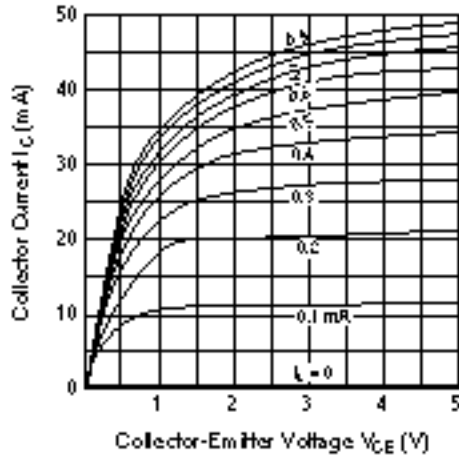
Response Waveform



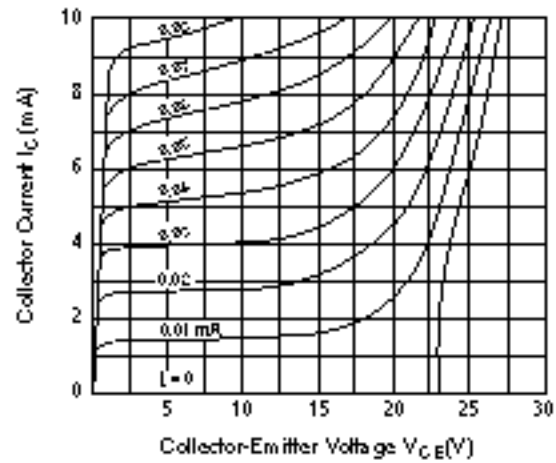
Operating Conditions

Symbol	I_b	I_{b1}	I_{b2}	V_{cc}	V_{ee}	V_{in}
Unit	mA	mA	mA	V	V	V
Bias	10	+1.0	-1.0	+13.0	-6.0	+13.0

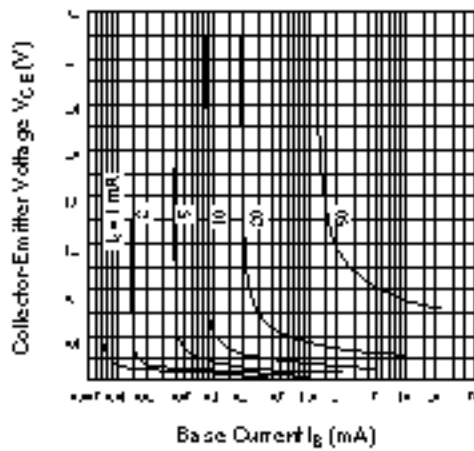
Emitter-Ground Output Static Characteristics (1)



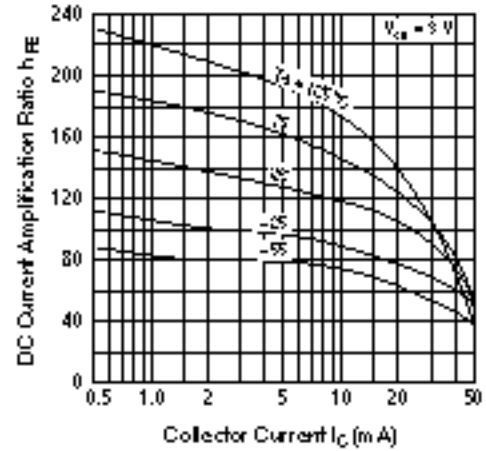
Emitter-Ground Output Static Characteristics (2)



Collector-Emitter Voltage vs. Base Current Characteristics

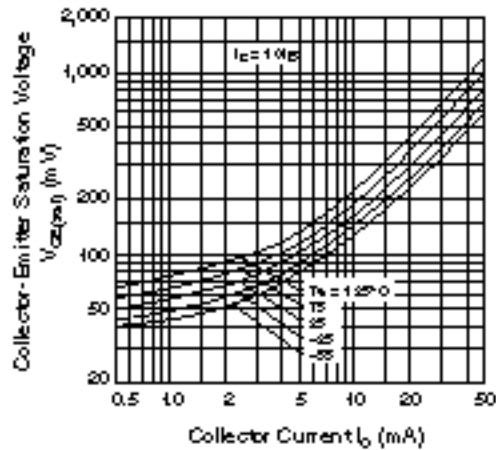


DC Current Amplification Ratio vs. Collector Current Characteristics

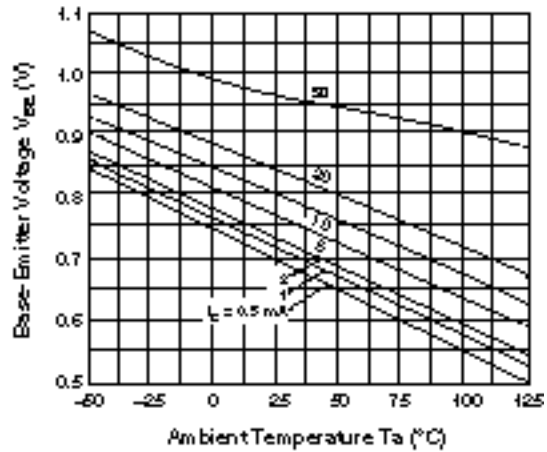


HA1127, HA1127P, HA1127FP

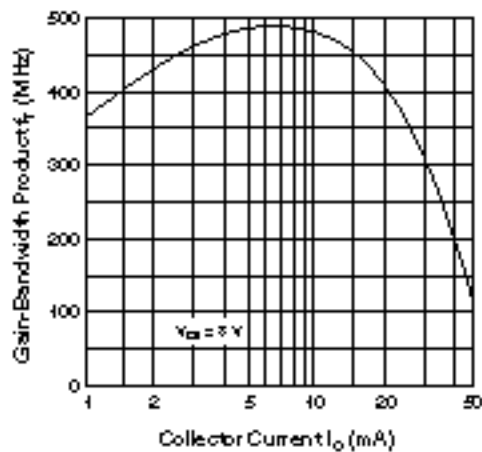
**Collector-Emitter Saturation Voltage vs.
Collector Current Characteristics**



**Base-Emitter Voltage vs.
Ambient Temperature Characteristics**



**Gain-Bandwidth Product vs.
Collector Current Characteristics**



**Input and Output Capacitances vs.
Voltage Characteristics**

