

The MPSA12 is NPN silicon darlington transistor designed for preamplifier input applications requiring input impedance of several megohms.

T0-92



EBC

ABSOLUTE MAXIMUM RATINGS

Collector-Emitter Voltage	V <sub>CES</sub>	20V
Emitter-Base Voltage	V <sub>EB0</sub>	10V
Collector Current	I <sub>C</sub>	500mA
Total Power Dissipation	P <sub>tot</sub>	625mW
Operating Junction & Storage Temperature	T <sub>j</sub> , T <sub>stg</sub>	-55 to +150°C

ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C)

PARAMETER	SYMBOL	MIN	MAX	UNIT	TEST CONDITIONS
Collector-Emitter Breakdown Voltage	BV <sub>CES</sub>	20		V	I <sub>C</sub> =100μA I <sub>B</sub> =0
Collector Cutoff Current	I <sub>CES</sub>		100	nA	V <sub>CE</sub> =15V V <sub>BE</sub> =0
Collector Cutoff Current	I <sub>CB0</sub>		100	nA	V <sub>CB</sub> =15V I <sub>E</sub> =0
Emitter Cutoff Current	I <sub>EB0</sub>		100	nA	V <sub>EB</sub> =10V I <sub>C</sub> =0
D.C. Current Gain	HFE	20K			I <sub>C</sub> =10mA V <sub>CE</sub> =5V
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>		1	V	I <sub>C</sub> =10mA I <sub>B</sub> =10μA
Base-Emitter Voltage	V <sub>BE</sub>		1.4	V	I <sub>C</sub> =10mA V <sub>CE</sub> =5V
Output Capacitance	C <sub>ob</sub>	8 typ		pF	V <sub>CB</sub> =10V f=100KHz



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