

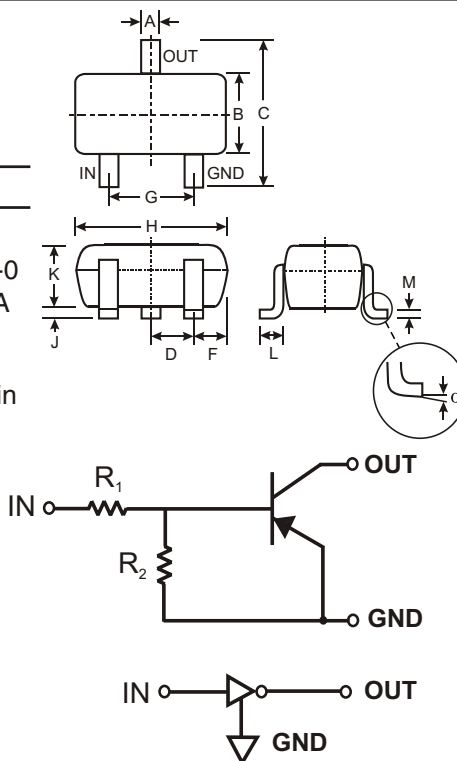
Features

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDTC)
- Built-In Biasing Resistors, R1 = R2
- Also Available in Lead Free Version

Mechanical Data

- Case: SOT-323, Molded Plastic
- Case material - UL Flammability Rating 94V-0
- Moisture sensitivity: Level 1 per J-STD-020A
- Terminals: Solderable per MIL-STD-202, Method 208
- Also Available in Lead Free Plating (Matte Tin Finish). Please see Ordering Information, Note 3, on Page 2
- Terminal Connections: See Diagram
- Marking: Date Code and Marking Code (See Diagrams & Page 2)
- Weight: 0.006 grams (approx.)
- Ordering Information (See Page 2)

P/N	R1, R2 (NOM)	MARKING
DDTA123EUA	2.2K Ω	P04
DDTA143EUA	4.7K Ω	P08
DDTA114EUA	10K Ω	P13
DDTA124EUA	22K Ω	P17
DDTA144EUA	47K Ω	P20
DDTA115EUA	100K Ω	P24



SOT-323		
Dim	Min	Max
A	0.25	0.40
B	1.15	1.35
C	2.00	2.20
D	0.65 Nominal	
E	0.30	0.40
G	1.20	1.40
H	1.80	2.20
J	0.0	0.10
K	0.90	1.00
L	0.25	0.40
M	0.10	0.18
α	0°	8°
All Dimensions in mm		

SCHEMATIC DIAGRAM

Maximum Ratings @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Supply Voltage, (3) to (1)	V _{CC}	-50	V
Input Voltage, (2) to (1)	V _{IN}	+10 to -12 +10 to -30 +10 to -40 +10 to -40 +10 to -40 +10 to -40 +10 to -40	V
Output Current	I _O	-100 -100 -50 -30 -30 -20	mA
Output Current All	I _C (Max)	-100	mA
Power Dissipation	P _d	200	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	R _{θJA}	625	°C/W
Operating and Storage and Temperature Range	T _j , T _{STG}	-55 to +150	°C

Note: 1. Mounted on FR4 PC Board with recommended pad layout at <http://www.diodes.com/datasheets/ap02001.pdf>.

Electrical Characteristics @ T_A = 25°C unless otherwise specified

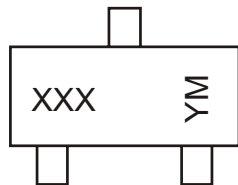
Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition
Input Voltage		V _{I(off)}	-0.5	-1.1	—	V	V _{CC} = 5V, I _O = 100μA
		V _{I(on)}	—	-1.9	-3		V _O = 0.3V, I _O = 20mA, DDTA123EUA V _O = 0.3V, I _O = 20mA, DDTA143EUA V _O = 0.3V, I _O = 10mA, DDTA114EUA V _O = 0.3V, I _O = 5mA, DDTA124EUA V _O = 0.3V, I _O = 2mA, DDTA144EUA V _O = 0.3V, I _O = 1mA, DDTA115EUA
Output Voltage		V _{O(on)}	—	-0.1	-0.3	V	I _O /I _I = 10mA/0.5mA, DDTA123EUA I _O /I _I = 10mA/0.5mA, DDTA143EUA I _O /I _I = 10mA/0.5mA, DDTA114EUA I _O /I _I = 10mA/0.5mA, DDTA124EUA I _O /I _I = 10mA/0.5mA, DDTA144EUA I _O /I _I = 5mA/0.25mA, DDTA115EUA
Input Current		I _I	—	—	-3.8 -1.8 -0.88 -0.36 -0.18 -0.15	mA	V _I = -5V
Output Current		I _{O(off)}	—	—	0.5	μA	V _{CC} = -50V, V _I = 0V
DC Current Gain		G _I	-20 -20 -30 -56 -68 -82	—	—	—	V _O = -5V, I _O = -20mA V _O = -5V, I _O = -10mA V _O = -5V, I _O = -5mA V _O = -5V, I _O = -5mA V _O = -5V, I _O = -5mA V _O = -5V, I _O = -5mA
Input Resistor (R ₁) Tolerance		ΔR ₁	-30	—	+30	%	—
Resistance Ratio		R ₂ /R ₁	0.8	1	1.2	—	—
Gain-Bandwidth Product*		f _T	—	250	—	MHz	V _{CE} = -10V, I _E = 5mA, f = 100MHz

* Transistor - For Reference Only

Ordering Information (Note 2)

Device	Packaging	Shipping
DDTA1xxEUA-7	SOT-323	3000/Tape & Reel
DDTA1xxEUA-13	SOT-323	10,000/Tape & Reel

- Notes: 2. For Packaging Details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.
 3. For Lead Free version (with Lead Free terminal finish) part number, please add "-F" suffix to part number above.
 Example: DDTA115EUA-7-F.

Marking Information


XXX = Product Type Marking Code
 See Sheet 1 Diagrams
 YM = Date Code Marking
 Y = Year ex: N = 2002
 M = Month ex: 9 = September

Date Code Key

Year	2002	2003	2004	2005	2006	2007	2008	2009
Code	N	P	R	S	T	U	V	W

Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

TYPICAL CURVES - DDTA143EUA

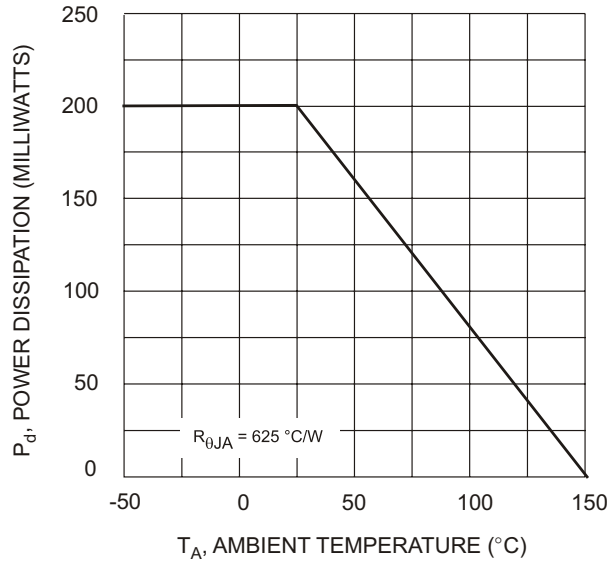


Fig. 1 Derating Curve

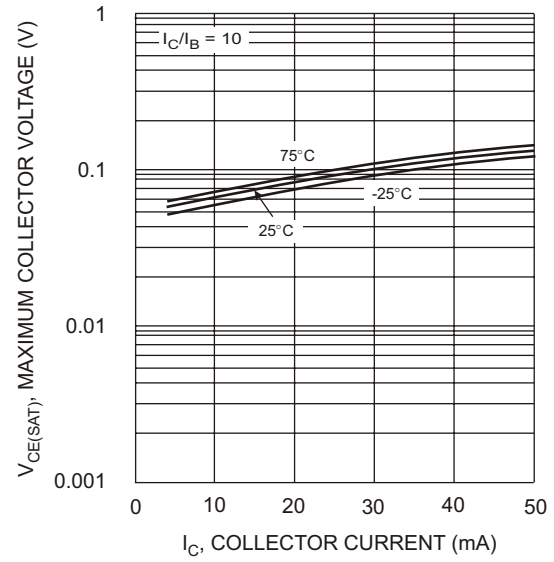


Fig. 2 $V_{CE(SAT)}$ vs. I_C

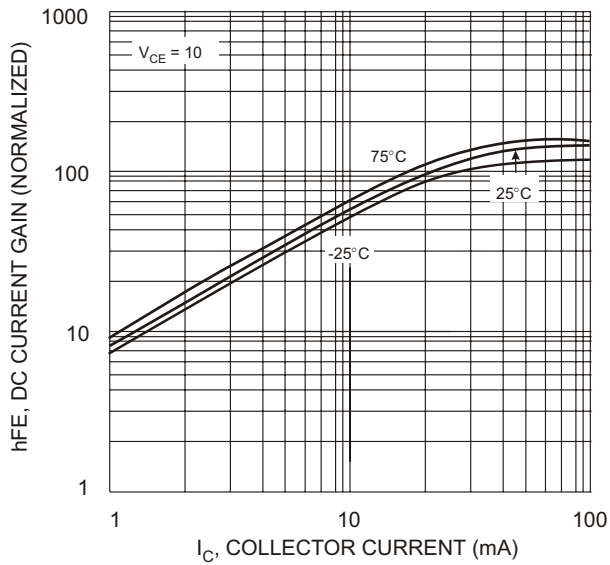


Fig. 3 DC CURRENT GAIN

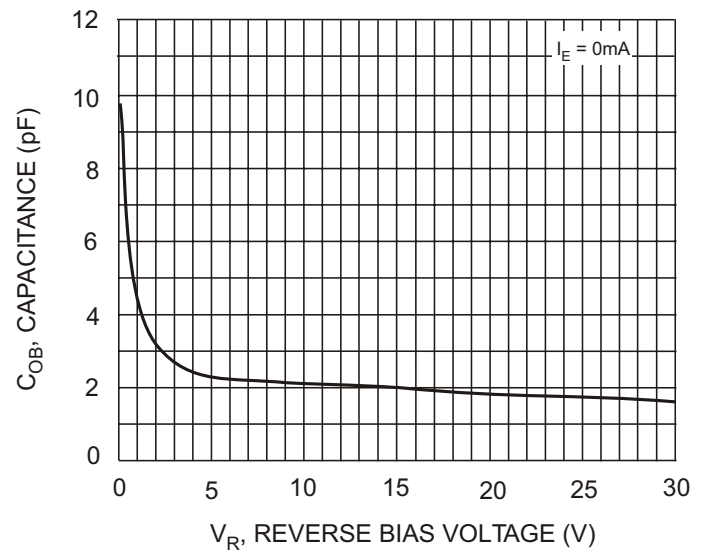


Fig. 4 Output Capacitance

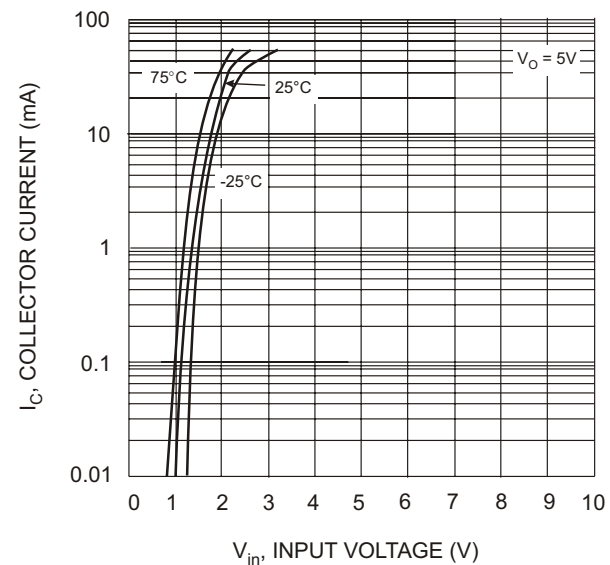


Fig. 5 Collector Current Vs. Input Voltage

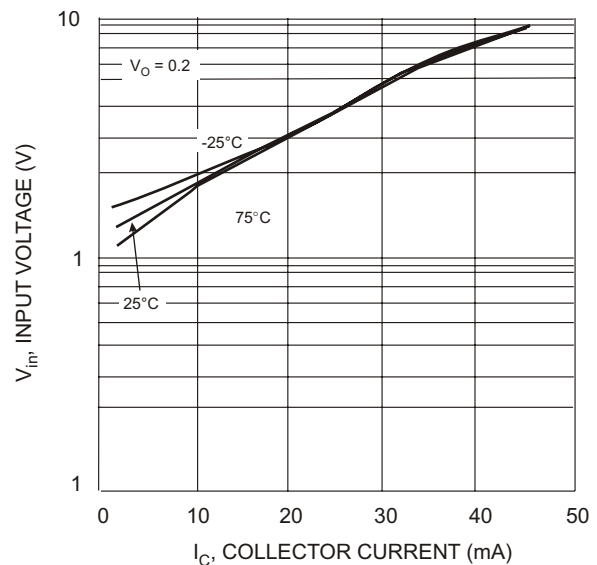


Fig. 6 Input Voltage vs. Collector Current