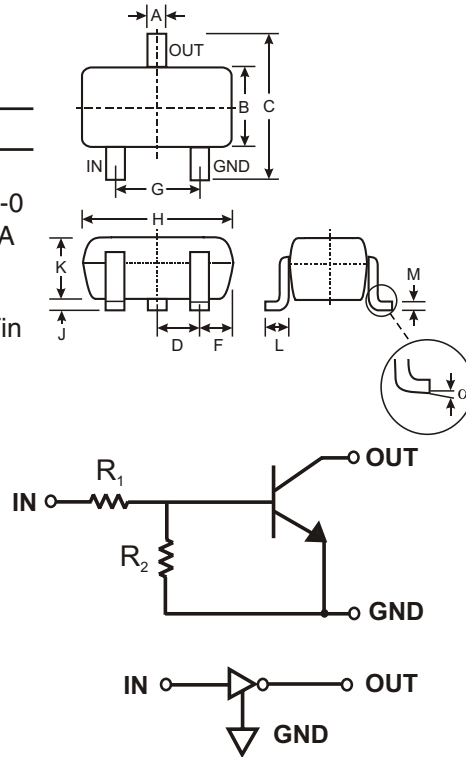


### Features

- Epitaxial Planar Die Construction
- Complementary PNP Types Available (DDTA)
- Built-In Biasing Resistors, R1≠R2
- Also Available in Lead Free Version

### Mechanical Data

- Case: SOT-323, Molded Plastic
- Case material - UL Flammability Rating 94V-0
- Moisture sensitivity: Level 1 per J-STD-020A
- Terminals: Solderable per MIL-STD-202, Method 208
- Also Available in Lead Free Plating (Matte Tin Finish). Please see Ordering Information, Note 3, on Page 2
- Terminal Connections: See Diagram
- Marking: Date Code and Marking Code (See Diagrams & Page 2)
- Weight: 0.006 grams (approx.)
- Ordering Information (See Page 2)



SOT-323		
Dim	Min	Max
A	0.25	0.40
B	1.15	1.35
C	2.00	2.20
D	0.65 Nominal	
E	0.30	0.40
G	1.20	1.40
H	1.80	2.20
J	0.0	0.10
K	0.90	1.00
L	0.25	0.40
M	0.10	0.18
α	0°	8°
All Dimensions in mm		

P/N	R1 (NOM)	R2 (NOM)	MARKING
DDTC113ZUA	1KΩ	10KΩ	N02
DDTC123YUA	2.2KΩ	10KΩ	N05
DDTC123JUA	2.2KΩ	47KΩ	N06
DDTC143XUA	4.7KΩ	10KΩ	N09
DDTC143FUA	4.7KΩ	22KΩ	N10
DDTC143ZUA	4.7KΩ	47KΩ	N11
DDTC114YUA	10KΩ	47KΩ	N14
DDTC114WUA	10KΩ	4.7KΩ	N15
DDTC124XUA	22KΩ	47KΩ	N18
DDTC144VUA	47KΩ	10KΩ	N21
DDTC144WUA	47KΩ	22KΩ	N22

SCHEMATIC DIAGRAM

### Maximum Ratings @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Supply Voltage, (3) to (1)	V <sub>CC</sub>	50	V
Input Voltage, (2) to (1)	V <sub>IN</sub>	-5 to +10 -5 to +12 -5 to +12 -7 to +20 -6 to +30 -5 to +30 -6 to +40 -10 to +30 -10 to +40 -15 to +40 -10 to +40	V
Output Current	I <sub>O</sub>	100 100 100 100 100 100 70 100 50 30 30	mA
Output Current	I <sub>C</sub> (Max)	100	mA
Power Dissipation	P <sub>d</sub>	200	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	R <sub>θJA</sub>	625	°C/W
Operating and Storage and Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C

Note: 1. Mounted on FR4 PC Board with recommended pad layout at <http://www.diodes.com/datasheets/ap02001.pdf>.

**Electrical Characteristics** @ T<sub>A</sub> = 25°C unless otherwise specified

Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition
Input Voltage	DDTC113ZUA DDTC123YUA DDTC123JUA DDTC143XUA DDTC143FUA DDTC143ZUA DDTC114YUA DDTC114WUA DDTC124XUA DDTC144VUA DDTC144WUA	V <sub>I(off)</sub>	0.3 0.3 0.5 0.3 0.3 0.5 0.3 0.8 0.4 1.0 0.8	—	—	V	V <sub>CC</sub> = 5V, I <sub>O</sub> = 100μA
	DDTC113ZUA DDTC123YUA DDTC123JUA DDTC143XUA DDTC143FUA DDTC143ZUA DDTC114YUA DDTC114WUA DDTC124XUA DDTC144VUA DDTC144WUA		V <sub>I(on)</sub>	—	—		
Output Voltage		V <sub>O(on)</sub>	—	0.1	0.3	V	I <sub>O</sub> /I <sub>I</sub> = 5mA/0.25mA DDCT123JUA I <sub>O</sub> /I <sub>I</sub> = 5mA/0.25mA DDCT143ZUA I <sub>O</sub> /I <sub>I</sub> = 5mA/0.25mA DDCT114YUA I <sub>O</sub> /I <sub>I</sub> = 10mA/0.5mA All Others
Input Current	DDTC113ZUA DDTC123YUA DDTC123JUA DDTC143XUA DDTC143FUA DDTC143ZUA DDTC114YUA DDTC114WUA DDTC124XUA DDTC144VUA DDTC144WUA	I <sub>I</sub>	—	—	7.2 3.8 3.6 1.8 1.8 1.8 0.88 0.88 0.36 0.16 0.16	mA	V <sub>I</sub> = 5V
Output Current		I <sub>O(off)</sub>	—	—	0.5	μA	V <sub>CC</sub> = 50V, V <sub>I</sub> = 0V
DC Current Gain	DDTC113ZUA DDTC123YUA DDTC123JUA DDTC143XUA DDTC143FUA DDTC143ZUA DDTC114YUA DDTC114WUA DDTC124XUA DDTC144VUA DDTC144WUA	G <sub>I</sub>	33 33 80 30 68 80 68 24 68 33 56	—	—	—	V <sub>O</sub> = 5V, I <sub>O</sub> = 5mA V <sub>O</sub> = 5V, I <sub>O</sub> = 10mA V <sub>O</sub> = 5V, I <sub>O</sub> = 10mA V <sub>O</sub> = 5V, I <sub>O</sub> = 10mA V <sub>O</sub> = 5V, I <sub>O</sub> = 10mA V <sub>O</sub> = 5V, I <sub>O</sub> = 10mA V <sub>O</sub> = 5V, I <sub>O</sub> = 5mA V <sub>O</sub> = 5V, I <sub>O</sub> = 10mA V <sub>O</sub> = 5V, I <sub>O</sub> = 5mA V <sub>O</sub> = 5V, I <sub>O</sub> = 5mA V <sub>O</sub> = 5V, I <sub>O</sub> = 5mA
Input Resistor Tolerance		ΔR <sub>1</sub>	-30	—	+30	%	—
Resistance Ratio Tolerance		ΔR <sub>2</sub> /R <sub>1</sub>	-20	—	+20	%	—
Gain-Bandwidth Product*		f <sub>T</sub>	—	250	—	MHz	V <sub>CE</sub> = 10V, I <sub>E</sub> = 5mA, f = 100MHz

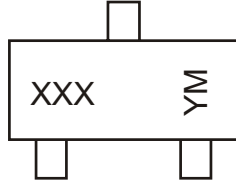
\* Transistor - For Reference Only

**Ordering Information** (Note 2)

Device	Packaging	Shipping
DDTC113ZUA-7	SOT-323	3000/Tape & Reel
DDTC123YUA-7	SOT-323	3000/Tape & Reel
DDTC123JUA-7	SOT-323	3000/Tape & Reel
DDTC143XUA-7	SOT-323	3000/Tape & Reel
DDTC143FUA-7	SOT-323	3000/Tape & Reel
DDTC143ZUA-7	SOT-323	3000/Tape & Reel
DDTC114YUA-7	SOT-323	3000/Tape & Reel
DDTC114WUA-7	SOT-323	3000/Tape & Reel
DDTC124XUA-7	SOT-323	3000/Tape & Reel
DDTC144VUA-7	SOT-323	3000/Tape & Reel
DDTC144WUA-7	SOT-323	3000/Tape & Reel

Notes: 2. For Packaging Details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.  
3. For Lead Free version (with Lead Free terminal finish) part number, please add "-F" suffix to part number above.  
Example: DDTC144WUA-7-F.

**Marking Information**



XXX = Product Type Marking Code  
 See Sheet 1 Diagrams  
 YM = Date Code Marking  
 Y = Year ex: N = 2002  
 M = Month ex: 9 = September

Date Code Key

<b>Year</b>	<b>2002</b>	<b>2003</b>	<b>2004</b>	<b>2005</b>	<b>2006</b>	<b>2007</b>	<b>2008</b>	<b>2009</b>
<b>Code</b>	N	P	R	S	T	U	V	W

<b>Month</b>	<b>Jan</b>	<b>Feb</b>	<b>March</b>	<b>Apr</b>	<b>May</b>	<b>Jun</b>	<b>Jul</b>	<b>Aug</b>	<b>Sep</b>	<b>Oct</b>	<b>Nov</b>	<b>Dec</b>
<b>Code</b>	1	2	3	4	5	6	7	8	9	O	N	D

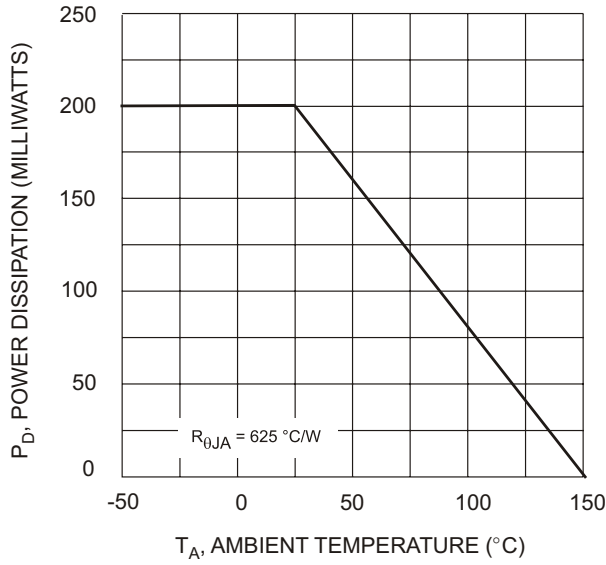


Fig. 1 Derating Curve

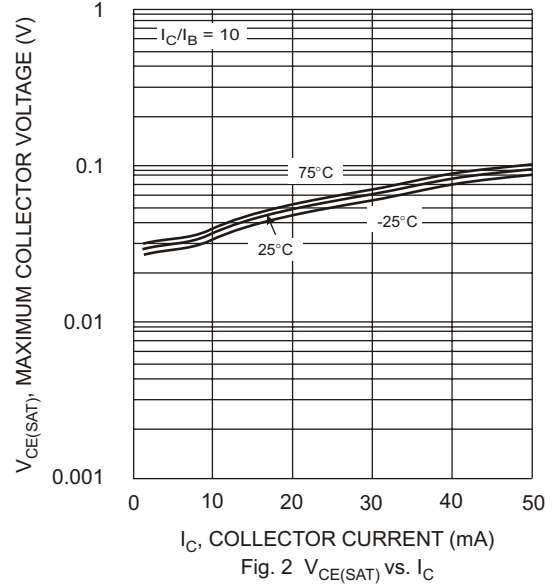


Fig. 2 V<sub>CE(SAT)</sub> vs. I<sub>C</sub>

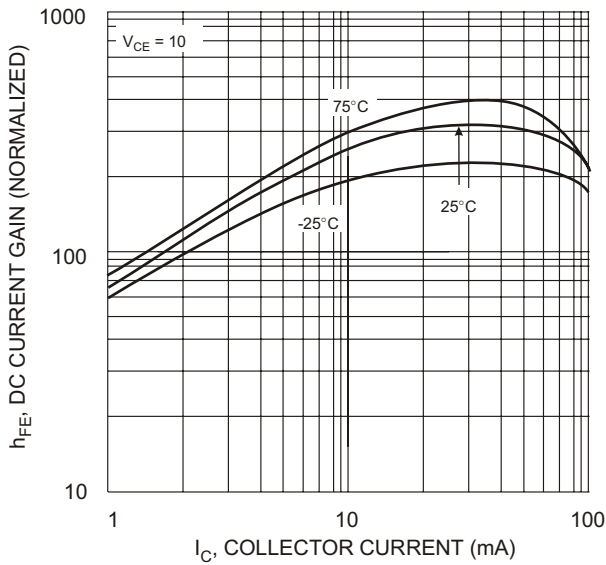


Fig. 3 DC Current Gain

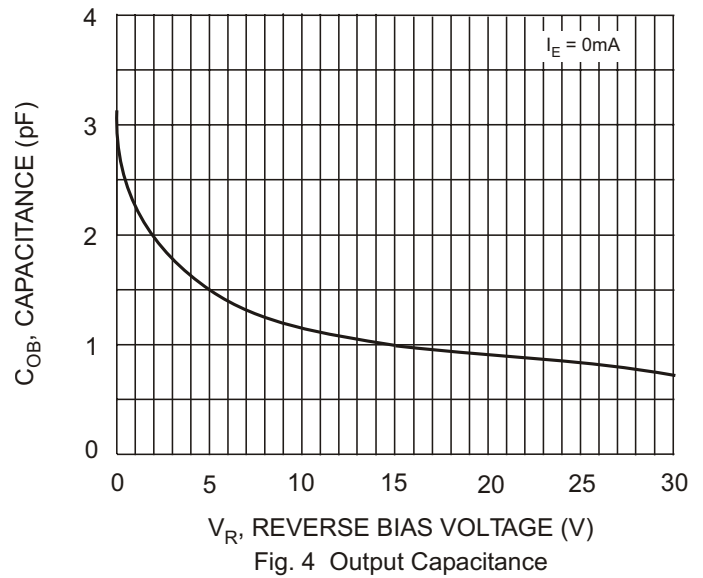


Fig. 4 Output Capacitance

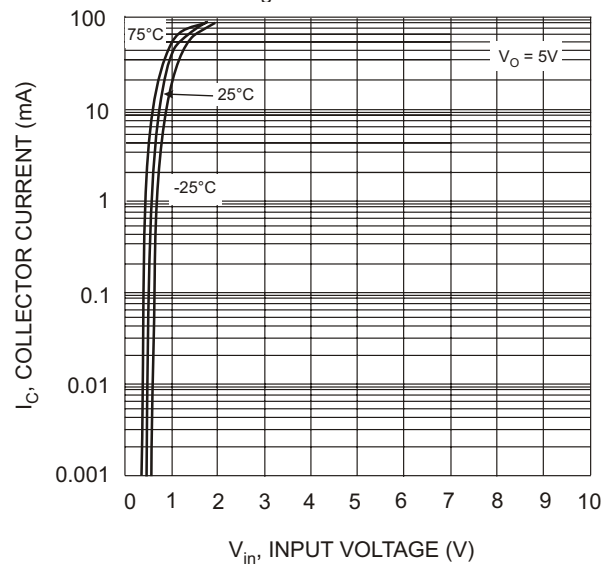


Fig. 5 Collector Current Vs. Input Voltage

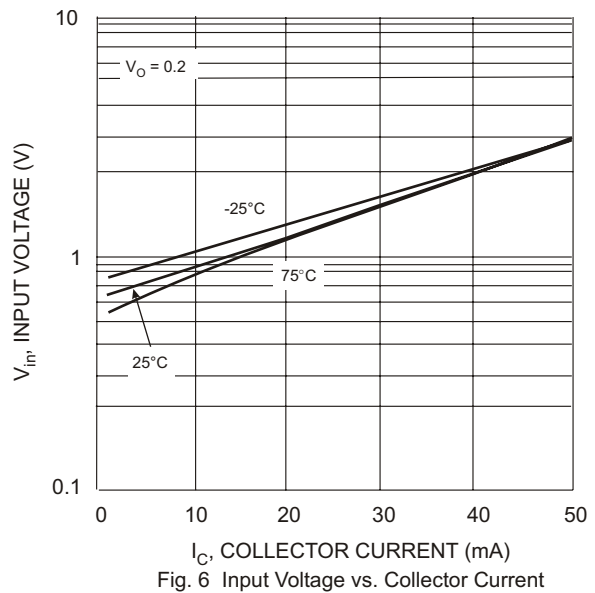


Fig. 6 Input Voltage vs. Collector Current