

UF600G THRU UF608G

GLASS PASSIVATED JUNCTION ULTRAFAST SWITCHING RECTIFIER

VOLTAGE - 50 to 800 Volts CURRENT - 6.0 Amperes

FEATURES

- Plastic package has Underwriters Laboratory Flammability Classification 94V-O Utilizing Flame Retardant Epoxy Molding Compound
- Glass passivated junction in P600 package
- 6.0 ampere operation at $T_A=55^{\circ}\text{C}$ with no thermal runaway
- Exceeds environmental standards of MIL-S-19500/228
- Ultra Fast switching for high efficiency

MECHANICAL DATA

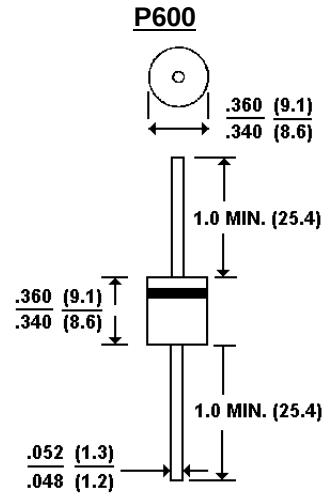
Case: Molded plastic, P600

Terminals: axial leads, solderable per MIL-STD-202, Method 208

Polarity: Band denotes cathode

Mounting Position: Any

Weight: 0.07 ounce, 2.1 gram



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

	UF600G	UF601G	UF602G	UF604G	UF606G	UF608G	UNITS
Peak Reverse Voltage, Repetitive; V _{RM} :	50	100	200	400	600	800	V
Maximum RMS Voltage	35	70	140	280	420	560	V
DC Reverse Voltage; V _R	50	100	200	400	600	800	V
Average Forward Current, I _o @ T _A =55 ºC 3/8" lead length, 60 Hz, resistive or inductive load	6.0						A
Peak Forward Surge Current, I _{FM} (surge) 8.3msec. single half sine wave superimposed on rated load(JEDEC method)	250						A
Maximum Forward Voltage VF @ 6.0A, 25 ºC	1.00			1.30	1.70		V
Maximum Reverse Current, @ Rated T _J =25 ºC	10.0						µg A
Reverse Voltage T _J =100 ºC	500						µg A
Typical Junction capacitance (Note 1) C _J	300						pF
Typical Junction Resistance (Note 2) R θKJA	10.0						ºC/W
Reverse Recovery Time I _F =.5A, I _R =1A, I _{rr} =.25A	50	50	50	50	100	100	ns
Operating and Storage Temperature Range	-55 to +150						ºC

NOTES:

1. Measured at 1 MHz and applied reverse voltage of 4.0 VDC
2. Thermal resistance from junction to ambient and from junction to lead length 0.375"(9.5mm) P.C.B. mounted

RATING AND CHARACTERISTIC CURVES

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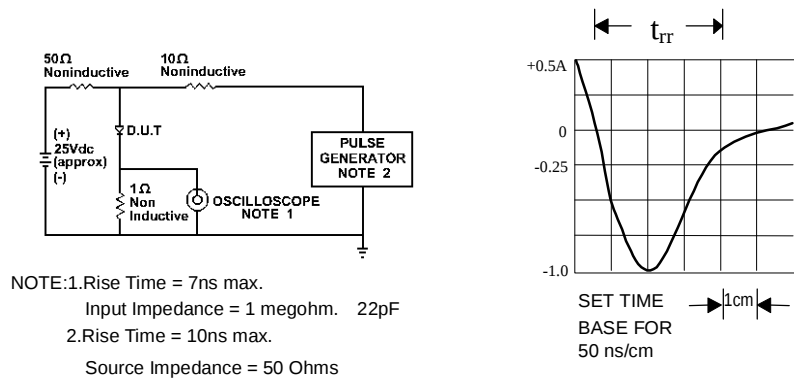


Fig. 1-REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

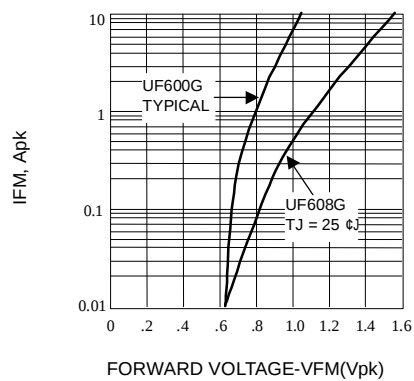


Fig. 2-FORWARD CHARACTERISTICS

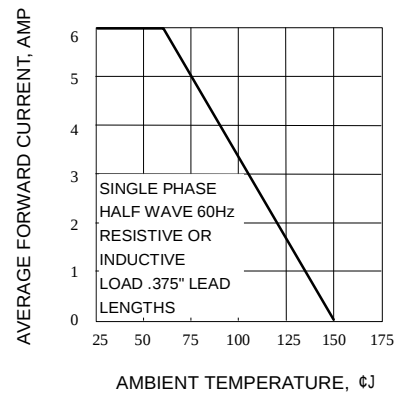


Fig. 3-FORWARD CURRENT DERATING CURVE

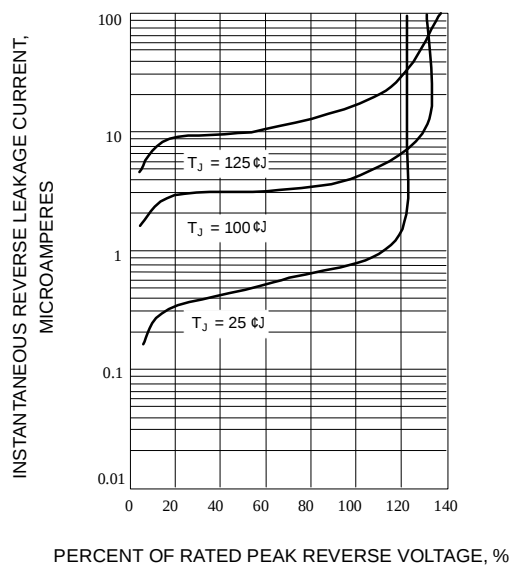


Fig. 4-TYPICAL REVERSE LEAKAGE CHARACTERISTICS

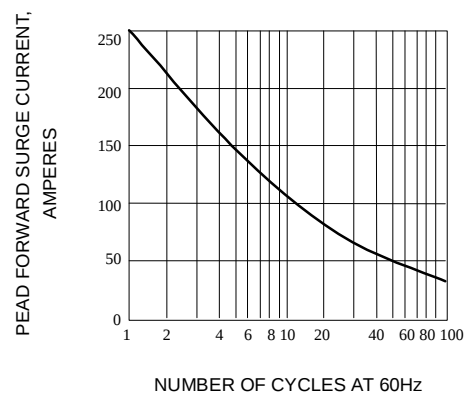


Fig. 5-PEAK FORWARD SURGE CURRENT