



STG3157

LOW VOLTAGE LOW ON RESISTANCE SPDT SWITCH WITH BREAK BEFORE MAKE FEATURE

- HIGH SPEED:
 $t_{PD} = 0.3ns$ (MAX.) at $V_{CC} = 4.5V$
 $t_{PD} = 0.8ns$ (MAX.) at $V_{CC} = 3.0V$
 $t_{PD} = 1.2ns$ (MAX.) at $V_{CC} = 2.3V$
- VERY LOW POWER DISSIPATION:
 $I_{CC} = 1\mu A$ (MAX.) at $T_A = 85^\circ C$
- LOW "ON" RESISTANCE $V_{IN} = 0V$:
 $R_{ON} = 7\Omega$ (MAX. $T_A = 85^\circ C$) at $V_{CC} = 4.5V$
 $R_{ON} = 9\Omega$ (MAX. $T_A = 85^\circ C$) at $V_{CC} = 3.0V$
- WIDE OPERATING VOLTAGE RANGE:
 V_{CC} (OPR) = 1.65V to 5.5V SINGLE SUPPLY
- TTL THRESHOLD ON CONTROL INPUT at $V_{CC} = 2.7$ to $3.6V$
- PIN AND FUNCTION COMPATIBLE WITH 74 SERIES 3157
- LATCH-UP PERFORMANCE EXCEEDS 300mA (JESD 17)

DESCRIPTION

The STG3157 is an high-speed spdt CMOS ANALOG S.P.D.T. (Single Pole Dual Throw) SWITCH or 2:1 Multiplexer/Demultiplexer Bus Switch fabricated in silicon gate C²MOS technology. It designed to operate from 1.65V to 5.5V, making this device ideal for portable applications.

It offers very low ON-Resistance ($<9\Omega$) at $V_{CC} = 3.0V$. The IN input is provided to control the S.P.D.T. switch, it's compatible with standard



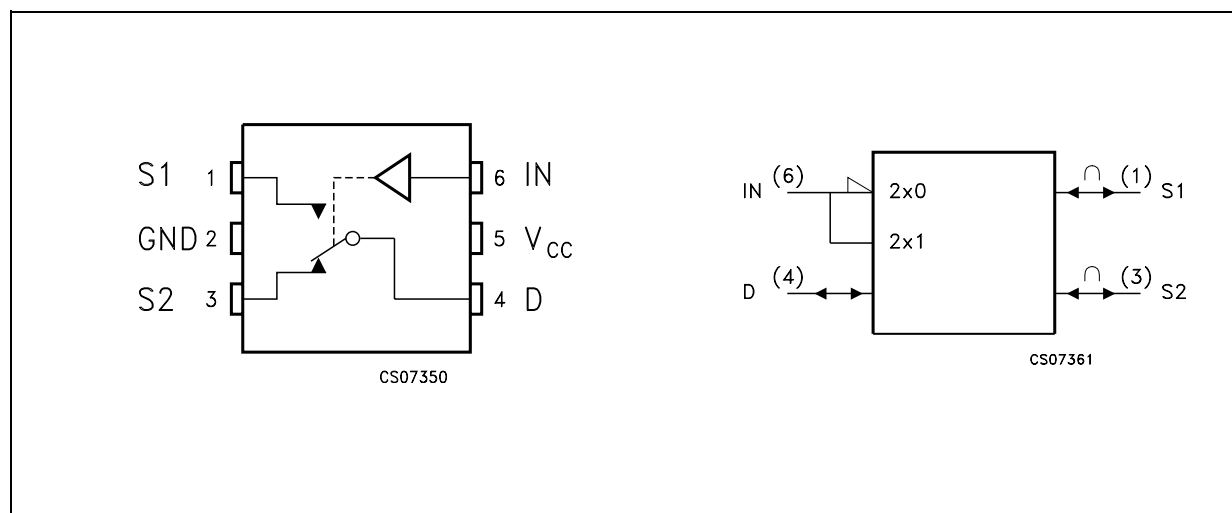
ORDER CODES

PACKAGE	T & R
SOT323-6L	STG3157CTR

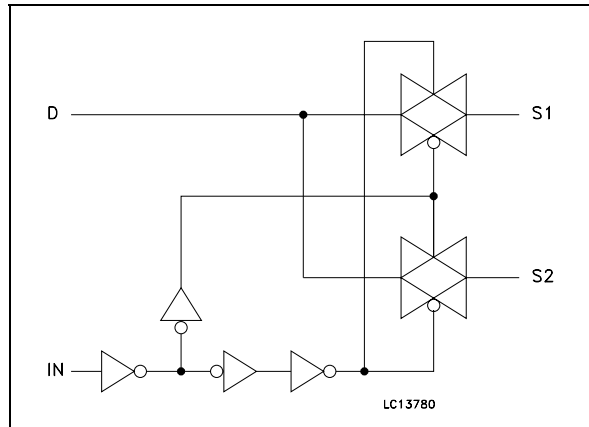
CMOS output. The switch S1 is ON (it is connected to common Port D) when the IN input is held high and OFF (high impedance state exists between the two ports) when IN is held low; the switch S2 is ON (it is connected to common Port D) when the IN input is held low and OFF (high impedance state exists between the two ports) when IN is held high. Additional key features are fast switching speed, Break Before Make Delay Time and Very Low Power Consumption. All inputs and output are equipped with protection circuits against static discharge, giving them ESD immunity and transient excess voltage.

It's available in the commercial temperature range in smallest six lead smd package on the market.

PIN CONNECTION AND IEC LOGIC SYMBOLS



INPUT EQUIVALENT CIRCUIT



PIN DESCRIPTION

PIN No	SYMBOL	NAME AND FUNCTION
1, 3	S1, S2	Independent Channel
4	D	Common Channel
6	IN	Control
5	V _{CC}	Positive Supply Voltage
2	GND	Ground (0V)

TRUTH TABLE

IN	SWITCH S1	SWITCH S2
H	ON	OFF(*)
L	OFF(*)	ON

*) High Impedance

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CC}	Supply Voltage	-0.5 to +7.0	V
V _I	DC Input Voltage	-0.5 to V _{CC} + 0.5	V
V _{IC}	DC Control Input Voltage	-0.5 to V _{CC} + 0.5	V
V _O	DC Output Voltage	-0.5 to V _{CC} + 0.5	V
I _{IKC}	DC Input Diode Current on control pin (V _{IN} < 0V)	± 50	mA
I _{IK}	DC Input Diode Current (V _{IN} < 0V)	± 50	mA
I _{OK}	DC Output Diode Current	± 20	mA
I _O	DC Output Current	+ 128	mA
I _{CC} or I _{GND}	DC V _{CC} or Ground Current	± 100	mA
T _{stg}	Storage Temperature	-65 to +150	°C
T _L	Lead Temperature (10 sec)	300	°C

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
V _{CC}	Supply Voltage (note 1)	1.65 to 5.5	V
V _I	Input Voltage	0 to V _{CC}	V
V _{IC}	Control Input Voltage	0 to 5.5	V
V _O	Output Voltage	0 to V _{CC}	V
T _{op}	Operating Temperature	-55 to 125	°C
dt/dv	Input Rise and Fall Time Control Input for V _{CC} = 2.3V - 3.6V	0 to 10	ns/V
	Input Rise and Fall Time Control Input for V _{CC} = 4.5V - 5.5V	0 to 5	

1) Truth Table guaranteed: 1.2V to 6.0V

DC SPECIFICATIONS

Symbol	Parameter	Test Condition		Value							Unit
		V _{CC} (V)		T _A = 25°C			-40 to 85°C		-55 to 125°C		
				Min.	Typ.	Max.	Min.	Max.	Min.	Max.	
V _{IH}	High Level Input Voltage	1.65-1.95		0.75V _{CC}			0.75V _{CC}		0.75V _{CC}		V
		2.3-5.5		0.7V _{CC}			0.7V _{CC}		0.7V _{CC}		
		2.7-3.6		2			2		2		
V _{IL}	Low Level Input Voltage	1.65-1.95				0.25V _{CC}		0.25V _{CC}		0.25V _{CC}	V
		2.3-5.5				0.3V _{CC}		0.3V _{CC}		0.3V _{CC}	
		2.7-3.6				0.8		0.8		0.8	
R _{ON}	Switch ON Resistance	4.5	V _S =0V I _S =30mA		4.4	7		7		9	Ω
			V _S =2.4V I _S =30mA		4.9	12		12		14.5	
			V _S = 4.5V I _S =30mA		6.1	15		15		18	
		3.0	V _S =0V I _S =24mA		5.2	9		9		11	
			V _S =3V I _S =24mA		7.8	20		20		24	
		2.3	V _S =0V I _S =8mA		6.5	12		12		14.5	
			V _S =2.3V I _S =8mA		9.6	30		30		36	
		1.65	V _S =0V I _S =4mA		9.0	20		20		24	
			V _S =1.65V I _S =4mA		14	50		50		60	
ΔR _{ON}	ON Resistance Match between channels	4.5	V _S =3.15V I _S =30mA		0.10					Ω	
		3.0	V _S =2.1V I _S =24mA		0.10						
		2.3	V _S =1.6V I _S =8mA		0.20						
		1.65	V _S =1.15V I _S =4mA		0.35						
R _{FLAT}	ON Resistance FLATNESS	5.0	V _S =0 to V _{CC} I _S =30mA		3					Ω	
		3.3	V _S =0 to V _{CC} I _S =24mA		6						
		2.5	V _S =0 to V _{CC} I _S =8mA		14						
		1.8	V _S =0toV _{CC} I _S =4mA		80						
I _{OFF}	OFF State Leakage Current	1.65-1.95	V _S =0 to V _{CC}		±0.05	±0.1		± 1		±10	μA

Symbol	Parameter	Test Condition		Value						Unit	
		V _{CC} (V)		T _A = 25°C			-40 to 85°C		-55 to 125°C		
				Min.	Typ.	Max.	Min.	Max.	Min.		Max.
I _{IN}	Input Leakage Current	0 - 5.5	V _{IN} = 0 to 5.5V		±0.05	±0.1		± 1		±10	μA
I _{CC}	Quiescent Supply Current (All Channel ON or OFF)	5.5	V _{IN} =V _{CC} or GND			1		1		10	μA

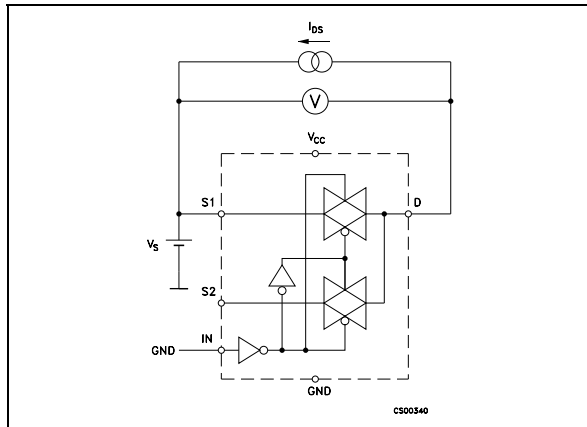
AC ELECTRICAL CHARACTERISTICS (C_L = 50pF, R_L = 500Ω)

Symbol	Parameter	Test Condition		Value						Unit	
		V _{CC} (V)		T _A = 25°C			-40 to 85°C		-55 to 125°C		
				Min.	Typ.	Max.	Min.	Max.	Min.		Max.
t _{LH} , t _{HL}	Propagation Delay	1.65-1.95	V _I =OPEN								ns
		2.3-2.7				1.2		1.2		1.8	
		3.0-3.6				0.8		0.8		1.2	
		4.5-5.5				0.3		0.3		0.5	
t _{PZH} , t _{PZL}	Output Enable Time (D to S _n)	1.65-1.95		7		15	7	20	7	27	ns
		2.3-2.7		3.5		11	3.5	14	3.5	17	
		3.0-3.6		2.5		7	2.5	7.6	2.5	9	
		4.5-5.5		1.7		5.2	1.7	5.7	1.7	7	
t _{PLZ} , t _{PHZ}	Output Disable Time (D to S _n)	1.65-1.95		3		10	3	13	3	16	ns
		2.3-2.7		2		7	2	7.5	2	9	
		3.0-3.6		1.5		5	1.5	5.3	1.5	6.5	
		4.5-5.5		1.7		3.5	1.7	3.8	1.7	5	
t _D	Break Before Make Time Delay	1.65-5.5		0.5			0.5		0.5		ns
Q	Charge injection	5			23						pC
		3.3			19						

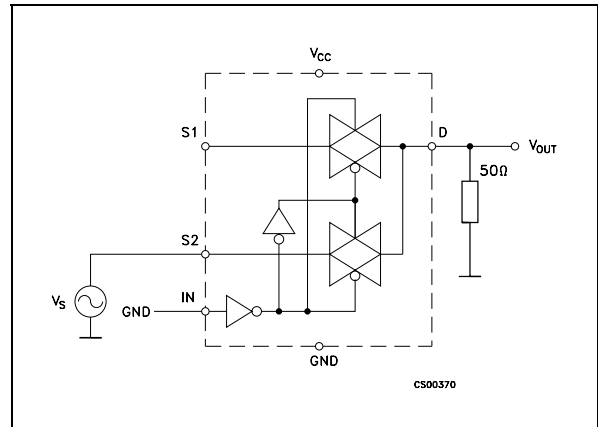
ANALOG SWITCH CHARACTERISTICS (C_L= 5pF, R_L=50Ω, T_A=25°C)

Symbol	Parameter	Test Condition		Value						Unit	
		V _{CC} (V)		T _A = 25°C			-40 to 85°C		-55 to 125°C		
				Min.	Typ.	Max.	Min.	Max.	Min.		Max.
OIRR	Off Isolation	1.65-5.5	R _L = 50Ω f= 10MHz		-57						dB
Xtalk	Crosstalk	1.65-5.5	R _L = 50Ω f= 10MHz		-54						dB
BW	-3dB Bandwidth	1.65-5.5	R _L = 50Ω		250						MHz
C _{IN}	Control Pin Input Capacitance				5						pF
C _{Sn}	Sn Port Capacitance	5.0	f= 1MHz		13						
C _D	D Port Capacitance when Switch is Enabled	5.0	f= 1MHz		21						

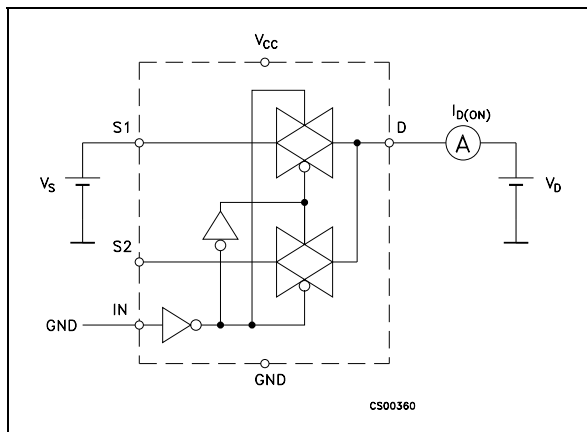
ON RESISTANCE



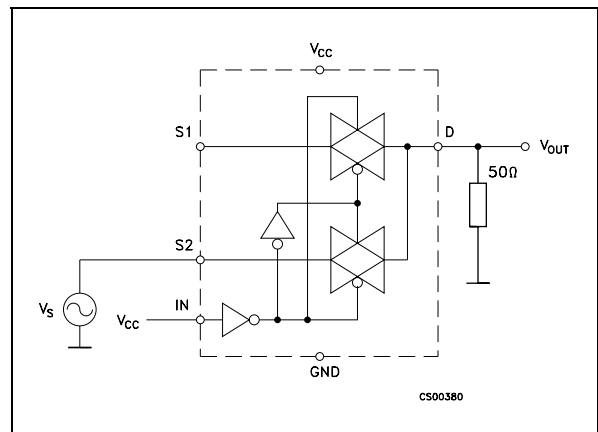
OFF ISOLATION



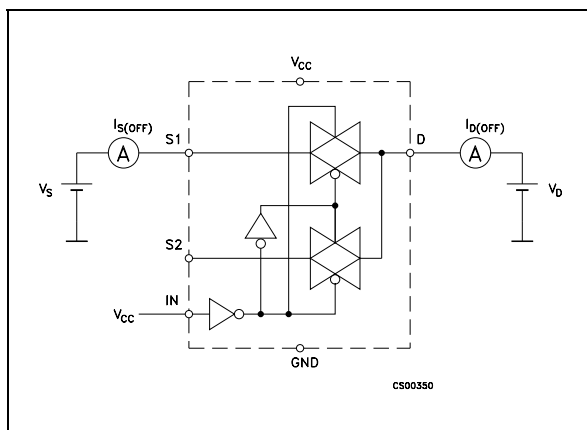
ON LEAKAGE



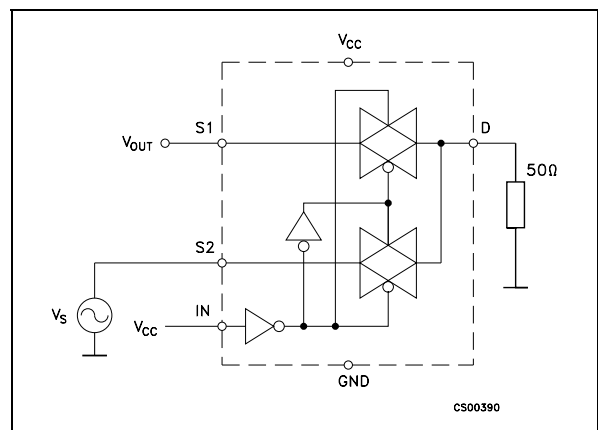
BANDWIDTH



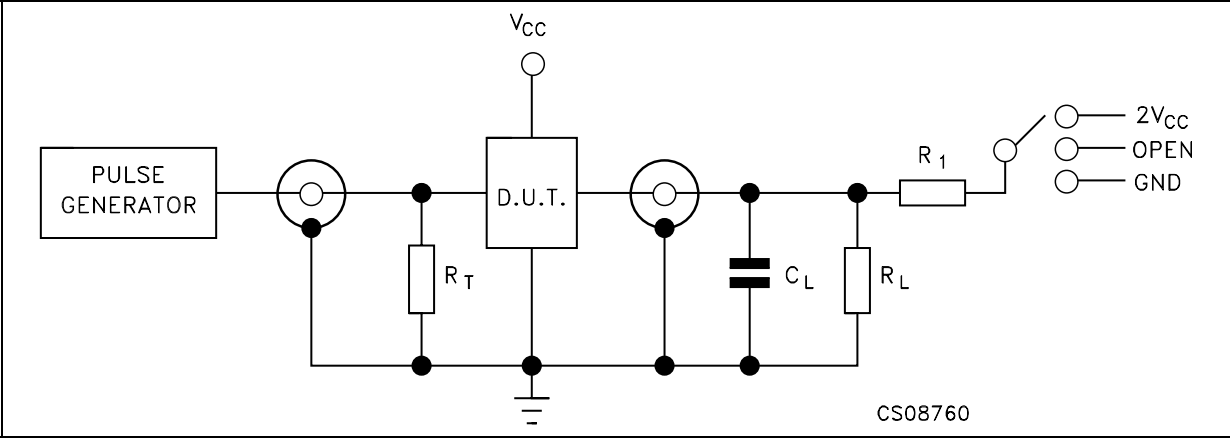
OFF LEAKAGE



CHANNEL TO CHANNEL CROSSTALK



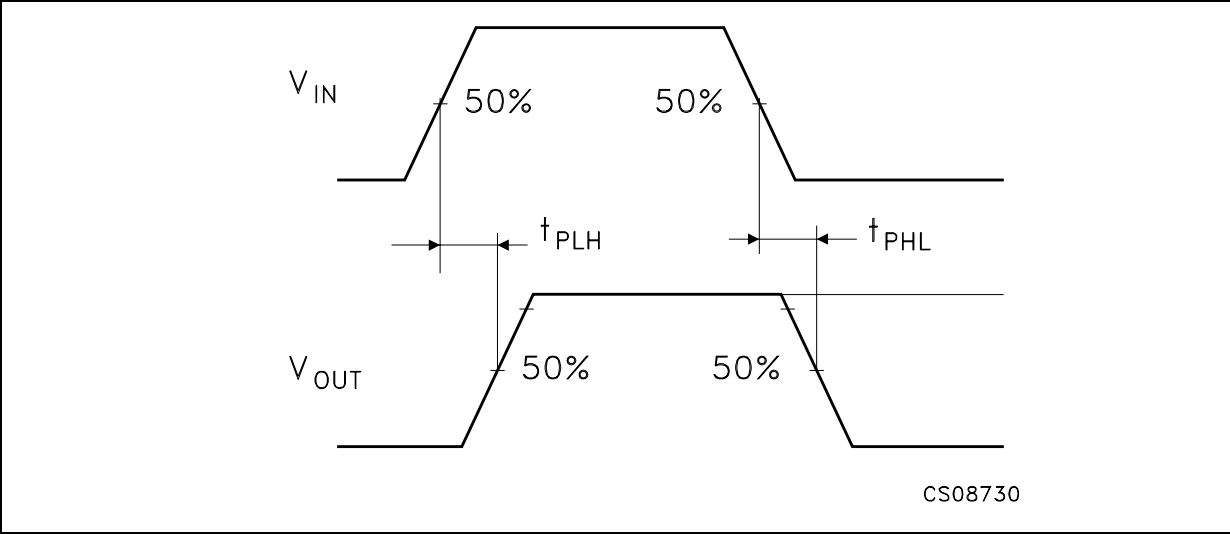
TEST CIRCUIT



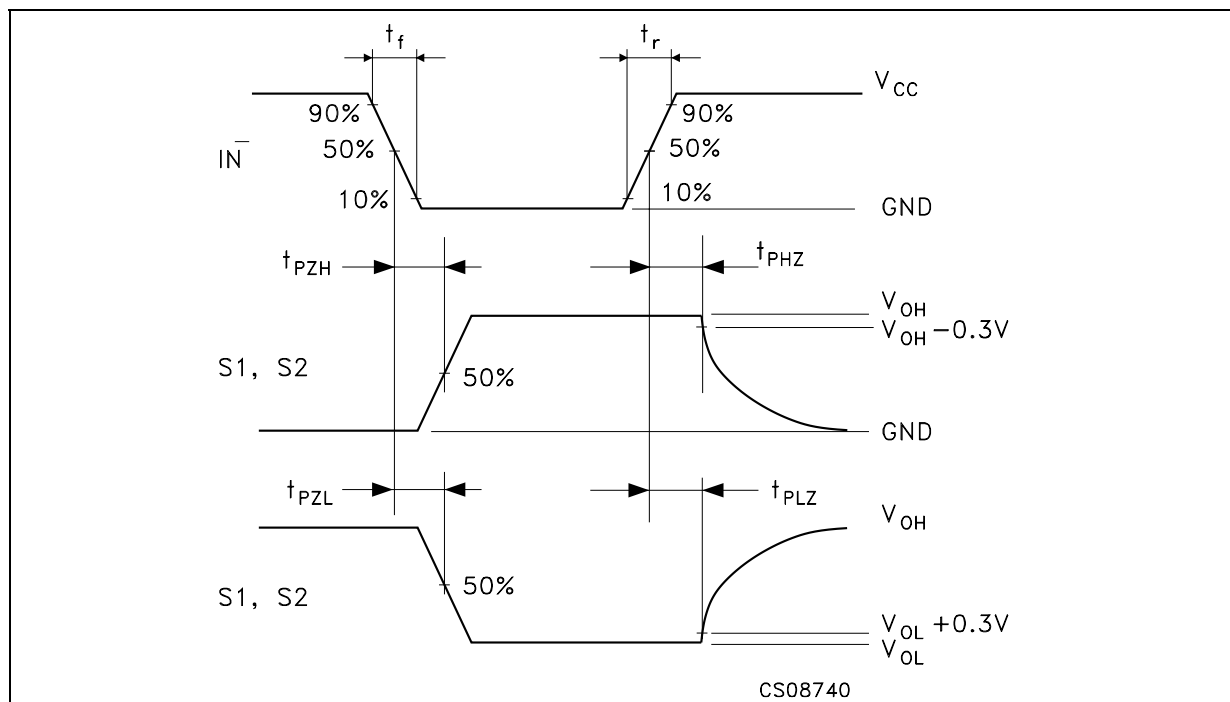
TEST	SWITCH
t_{PLH} , t_{PHL}	Open
t_{PZL} , t_{PLZ}	V_{CC}
t_{PZH} , t_{PHZ}	GND

C_L = 50pF or equivalent (includes jig and probe capacitance)
 $R_L = R_1$ = 500Ω or equivalent
 R_T = Z_{OUT} of pulse generator (typically 50Ω)

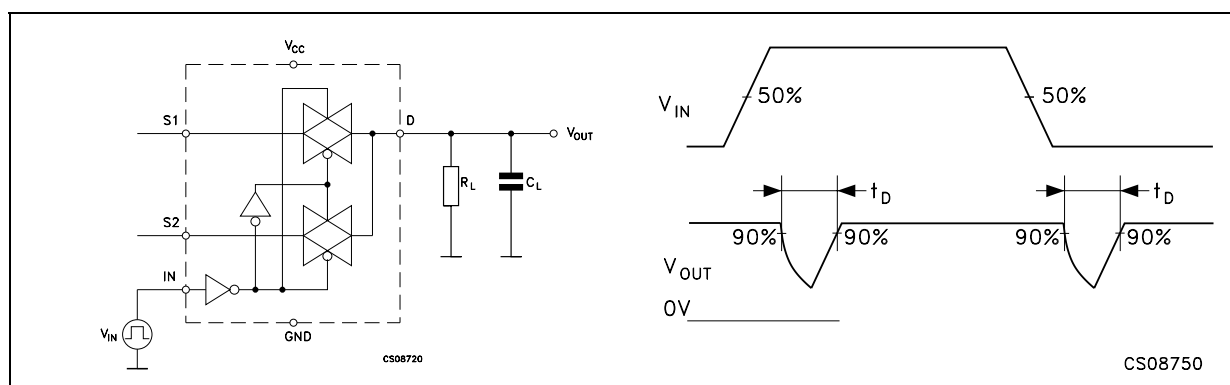
SWITCHING TIMES WAVEFORM



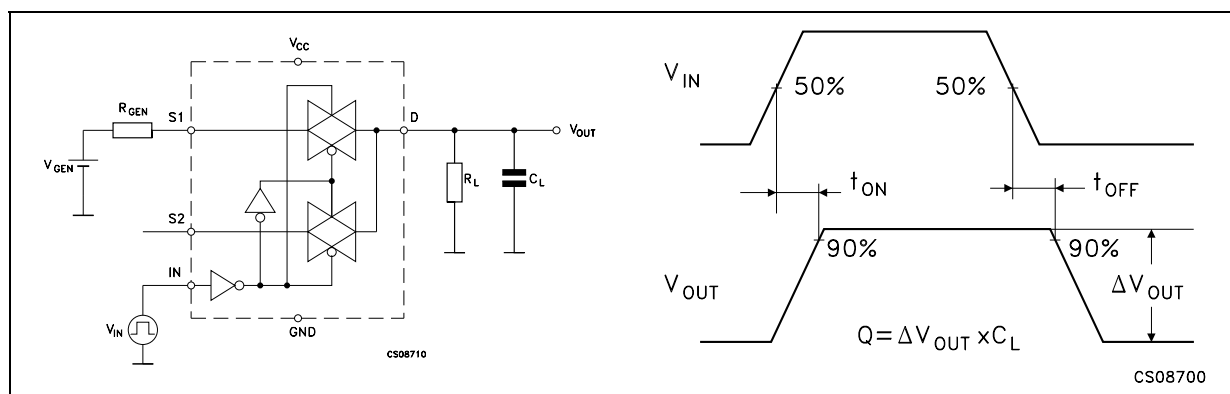
SWITCHING TIMES WAVEFORM



BREAK BEFORE MAKE TIME DELAY

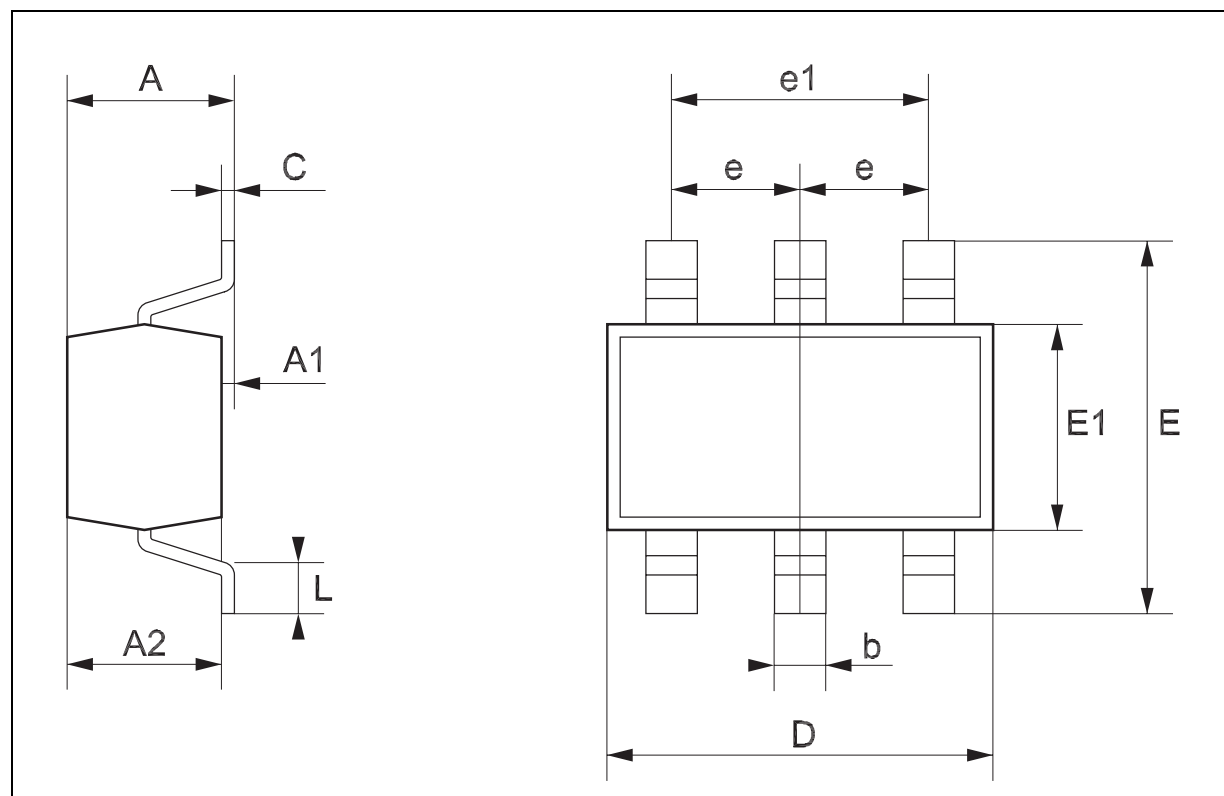


CHARGE INJECTION ($V_{GEN}=0V$, $R_{GEN}=0\Omega$, $R_L=1M\Omega$, $C_L=100pF$)



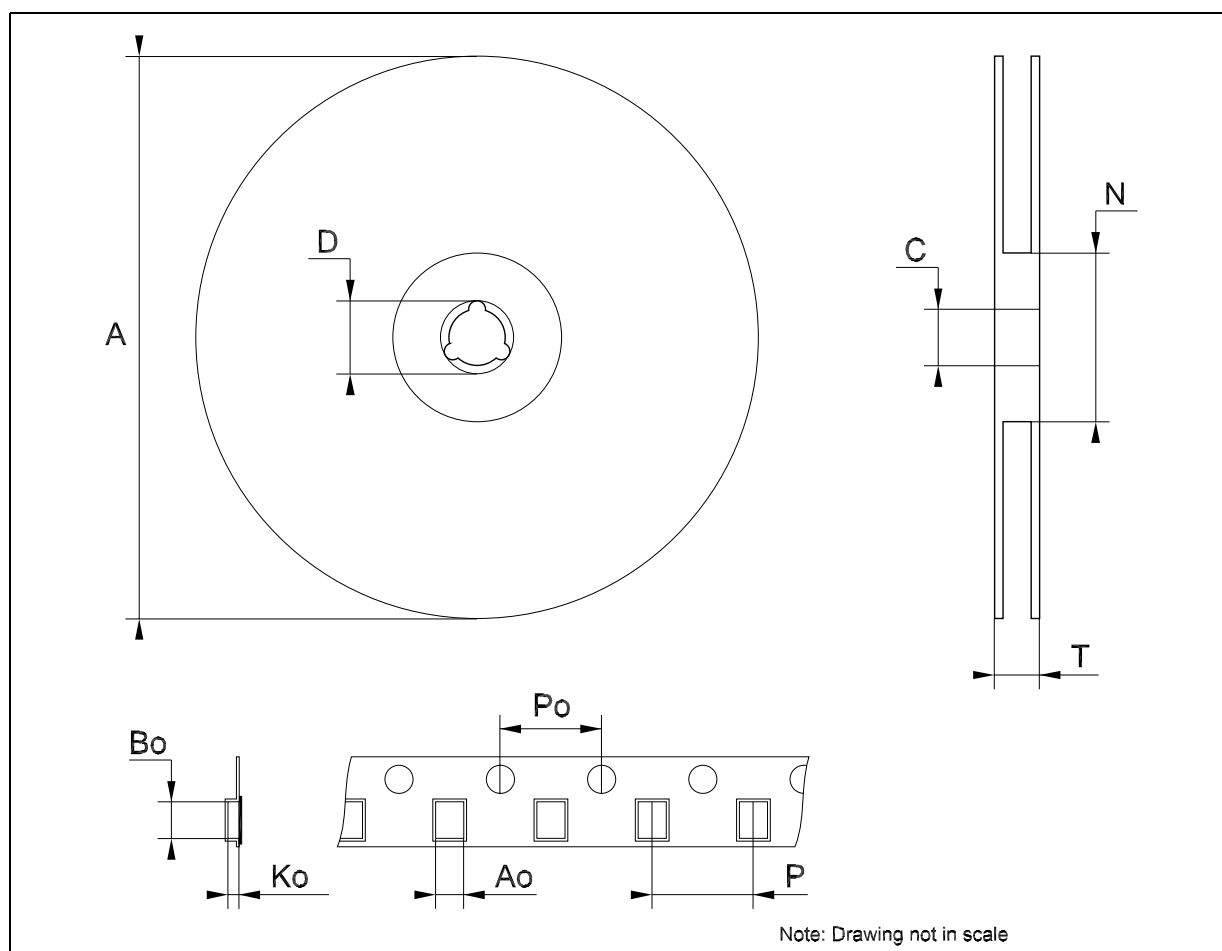
SOT323-6L MECHANICAL DATA

DIM.	mm.			mils		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A	0.80		1.10	31.5		43.3
A1	0.00		0.10	0.0		3.9
A2	0.80		1.00	31.5		39.4
b	0.15		0.30	5.9		11.8
C	0.10		0.18	3.9		7.1
D	1.80		2.20	70.9		86.6
E	1.80		2.40	70.9		94.5
E1	1.15		1.35	45.3		53.1
e	0	.65			25.6	
e1		1.3			51.2	
L	0.10		0.30	3.9		11.8



Tape & Reel SOT323-xL MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A	175	180	185	6.889	7.086	7.283
C	12.8	13	13.2	0.504	0.512	0.519
D	20.2			0.795		
N	59.5	60	60.5		2.362	
T			14.4			0.567
Ao		2.25			0.088	
Bo		2.7			0.106	
Ko		1.2			0.047	
Po	3.98	4	4.2	0.156	0.157	0.165
P	3.98	4	4.2	0.156	0.157	0.165



Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics.

© The ST logo is a registered trademark of STMicroelectronics

© 2002 STMicroelectronics - Printed in Italy - All Rights Reserved
STMicroelectronics GROUP OF COMPANIES

Australia - Brazil - Canada - China - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco
Singapore - Spain - Sweden - Switzerland - United Kingdom - United States.

© <http://www.st.com>