



HIGH SIDE SMART POWER SOLID STATE RELAY

TYPE	V _{DSS}	R _{DS(on)}	I _n (*)	V _{CC}
VN31SP	60 V	0.03 Ω	11.5 A	26 V

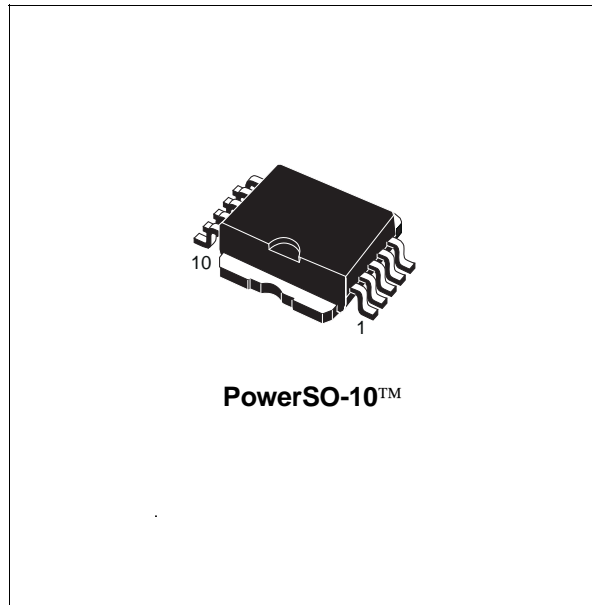
- MAXIMUM CONTINUOUS OUTPUT CURRENT (#):31 A @ T_c=85°C
- 5 V LOGIC LEVEL COMPATIBLE INPUT
- THERMAL SHUT-DOWN
- UNDER VOLTAGE PROTECTION
- OPEN DRAIN DIAGNOSTIC OUTPUT
- INDUCTIVE LOAD FAST DEMAGNETIZATION
- VERY LOW STAND-BY POWER DISSIPATION

DESCRIPTION

The VN31SP is a monolithic device made using STMicroelectronics VIPower Technology, intended for driving resistive or inductive loads with one side grounded.

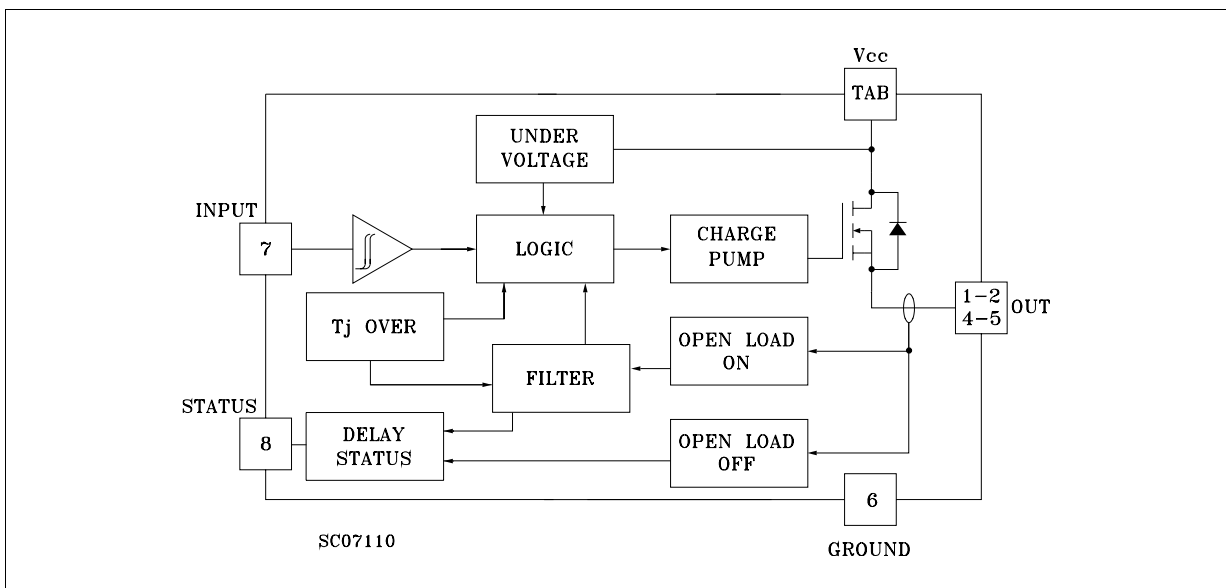
Built-in thermal shut-down protects the chip from over temperature and short circuit.

The open drain diagnostic output indicates: open load in off state, and in on state, output shorted to



V_{CC} and overtemperature. Fast demagnetization of inductive loads is achieved by negative (-18V) load voltage at turn-off.

BLOCK DIAGRAM



(*) I_n = Nominal current according to ISO definition for high side automotive switch (see note 1)

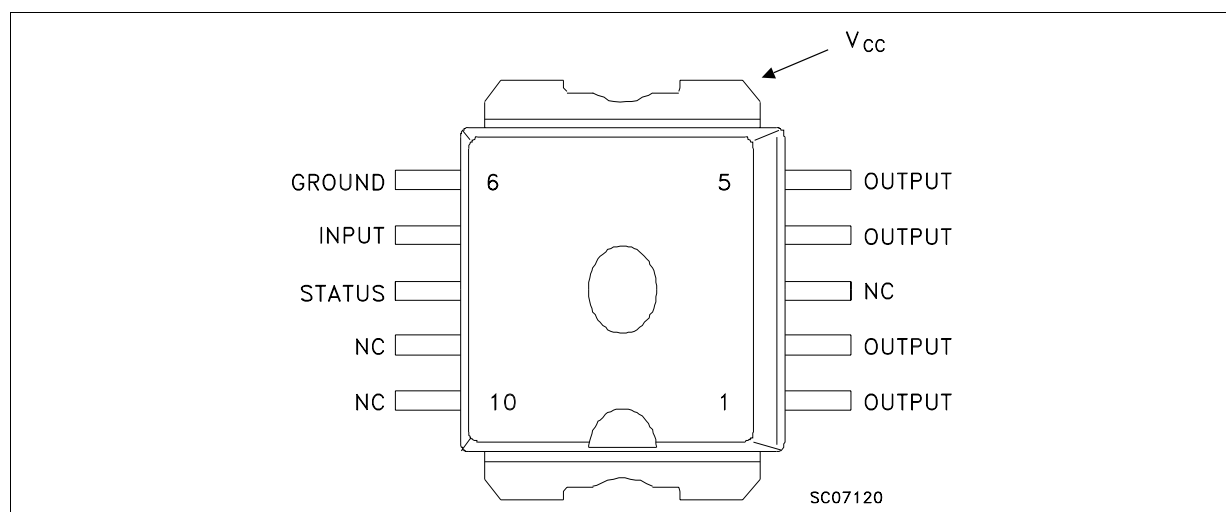
(#) The maximum continuous output current is the the current at T_c = 85 °C for a battery voltage of 13V which does not activate self protection.

VN31SP

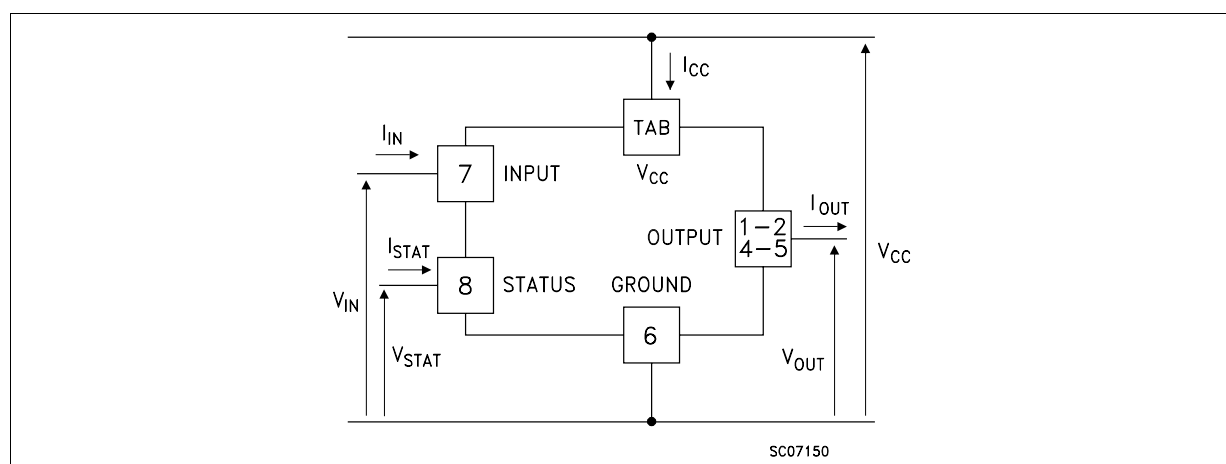
ABSOLUTE MAXIMUM RATING

Symbol	Parameter	Value	Unit
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	60	V
I_{OUT}	Output Current (cont.) at $T_c = 85\text{ }^\circ\text{C}$	31	A
I_R	Reverse Output Current at $T_c = 85\text{ }^\circ\text{C}$	-31	A
I_{IN}	Input Current	± 10	mA
$-V_{CC}$	Reverse Supply Voltage	-4	V
I_{STAT}	Status Current	± 10	mA
V_{ESD}	Electrostatic Discharge (1.5 k Ω , 100 pF)	2000	V
P_{tot}	Power Dissipation at $T_c = 85\text{ }^\circ\text{C}$	54	W
T_j	Junction Operating Temperature	-40 to 150	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55 to 150	$^\circ\text{C}$

CONNECTION DIAGRAMS



CURRENT AND VOLTAGE CONVENTIONS



THERMAL DATA

R _{thj-case}	Thermal Resistance Junction-case	Max	1.2	°C/W
R _{thj-amb}	Thermal Resistance Junction-ambient (\$)	Max	50	°C/W

(\$) When mounted using minimum recommended pad size on FR-4 board

ELECTRICAL CHARACTERISTICS (V_{CC} = 13 V; -40 ≤ T_j ≤ 125 °C unless otherwise specified)

POWER

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{CC}	Supply Voltage		5.5	13	26	V
I _n (*)	Nominal Current	T _c = 85 °C V _{DS(on)} ≤ 0.5 (note 1)	11.5			A
R _{on}	On State Resistance	I _{OUT} = 11.5 A I _{OUT} = 11.5 A T _j = 25 °C			0.06 0.03	Ω Ω
I _s	Supply Current	Off State T _j ≥ 25 °C On State			50 15	μA mA
V _{DS(MAX)}	Maximum Voltage Drop	I _{OUT} = 25 A T _c = 85 °C			1.5	V

SWITCHING

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
t _{d(on)} (^)	Turn-on Delay Time Of Output Current	I _{OUT} = 11.5 A Resistive Load Input Rise Time < 0.1 μs		90		μs
t _r (^)	Rise Time Of Output Current	I _{OUT} = 11.5A Resistive Load Input Rise Time < 0.1 μs		100		μs
t _{d(off)} (^)	Turn-off Delay Time Of Output Current	I _{OUT} = 11.5 A Resistive Load Input Rise Time < 0.1 μs		140		μs
t _f (^)	Fall Time Of Output Current	I _{OUT} = 11.5 A Resistive Load Input Rise Time < 0.1 μs		50		μs
(di/dt) _{on}	Turn-on Current Slope	I _{OUT} = 11.5 A I _{OUT} = I _{OV}		0.08	0.5 1	A/μs A/μs
(di/dt) _{off}	Turn-off Current Slope	I _{OUT} = 11.5 A I _{OUT} = I _{OV}		0.2	3 3	A/μs A/μs
V _{demag}	Inductive Load Clamp Voltage	I _{OUT} = 11.5 A L = 1 mH	-24	-18	-14	V

LOGIC INPUT

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{IL}	Input Low Level Voltage				0.8	V
V _{IH}	Input High Level Voltage		2		(•)	V
V _{I(hyst.)}	Input Hysteresis Voltage			0.5		V
I _{IN}	Input Current	V _{IN} = 5 V V _{IN} = 2 V V _{IN} = 0.8 V	25	250	500 250	μA μA μA
V _{ICL}	Input Clamp Voltage	I _{IN} = 10 mA I _{IN} = -10 mA	5.5	6 -0.7	-0.3	V V

ELECTRICAL CHARACTERISTICS (continued)
PROTECTION AND DIAGNOSTICS

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V _{STAT}	Status Voltage Output Low	I _{STAT} = 1.6 mA			0.4	V
V _{USD}	Under Voltage Shut Down			5		V
V _{SCL}	Status Clamp Voltage	I _{STAT} = 10 mA I _{STAT} = -10 mA		6 -0.7		V V
I _{OV}	Over Current	R _{LOAD} < 10 mΩ -40 T _c 125 °C		140		A
I _{AV}	Average Current in Short Circuit	R _{LOAD} < 10 mΩ T _c = 85 °C		2.5		A
I _{OL}	Open Load Current Level		5	600	1250	mA
T _{TSD}	Thermal Shut-down Temperature		140			°C
T _R	Reset Temperature		125			°C
V _{OL}	Open Load Voltage Level	Off-State (note 2)	2.5	3.75	5	V
t _{1(on)}	Open Load Filtering Time	(note 3)	1	5	10	ms
t _{1(off)}	Open Load Filtering Time	(note 3)	1	5	10	ms
t _{2(off)}	Open Load Filtering Time	(note 3)	1	5	10	ms
t _{povl}	Status Delay	(note 3)		5	10	μs
t _{pol}	Status Delay	(note 3)	50	700		μs

(^) See Switching Time Waveforms

() The V_{IH} is internally clamped at 6V about. It is possible to connect this pin to an higher voltage via an external resistor calculated to not exceed 10 mA at the input pin.

note 1: The Nominal Current is the current at T_c = 85 °C for battery voltage of 13V which produces a voltage drop of 0.5 V

note 2: I_{OL(off)} = (V_{CC} - V_{OL})/R_{OL} (see figure)

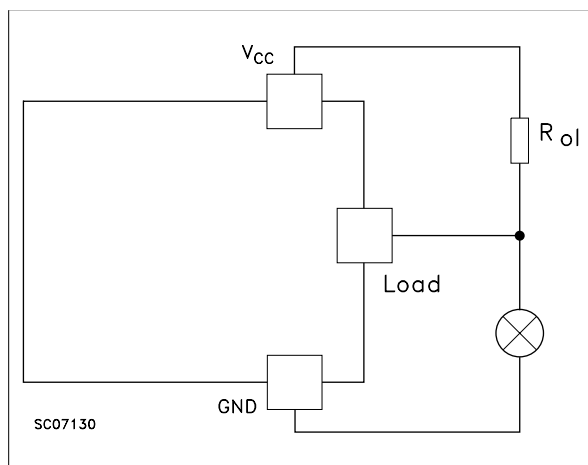
note 3: t_{1(on)}: minimum open load duration which activates the status output

t_{1(off)}: minimum load recovery time which desactivates the status output

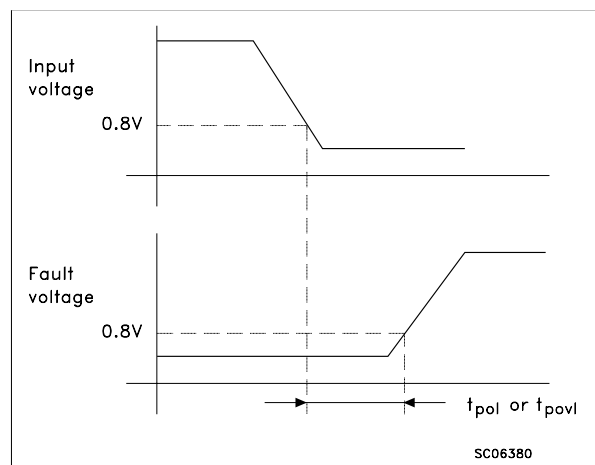
t_{2(off)}: minimum on time after thermal shut down which desactivates status output

t_{povl} t_{pol}: ISO definition (see figure)

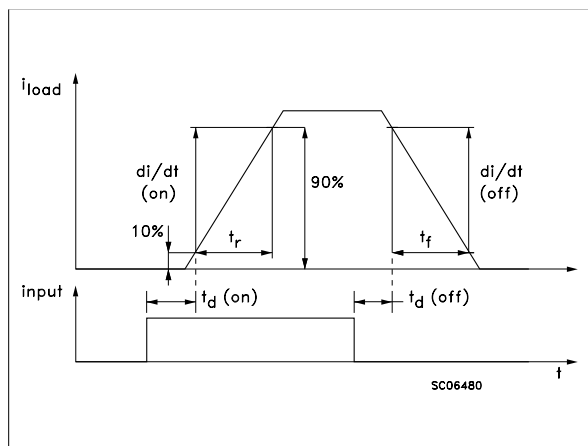
Note 2 Relevant Figure



Note 3 Relevant Figure



Switching Time Waveforms



FUNCTIONAL DESCRIPTION

The device has a diagnostic output which indicates open load conditions in off state as well as in on state, output shorted to V_{CC} and overtemperature. The truth table shows input, diagnostic and output voltage level in normal operation and in fault conditions. The output signals are processed by internal logic. The open load diagnostic output has a 5 ms filtering. The filter gives a continuous signal for the fault condition after an initial delay of about 5 ms. This means that a disconnection during normal operation, with a duration of less than 5 ms does not affect the status output. Equally, any re-connection of less than 5 ms during a disconnection duration does not affect the status output. No delay occur for the status to go low in case of overtemperature conditions. From the falling edge of the input signal the status output initially low in fault condition (over temperature or open load) will go back with a delay (t_{povl}) in case of overtemperature condition and a delay (t_{pol}) in case of open load. These feature fully comply with International Standard Office (I.S.O.) requirement for automotive High Side Driver.

To protect the device against short circuit and over current conditions, the thermal protection turns the integrated Power MOS off at a minimum junction temperature of 140 °C. When the temperature returns to 125 °C the switch is automatically turned on again. In short circuit the protection reacts with virtually no delay, the sensor being located in the region of the die where the heat is generated. Driving inductive loads, an internal function of the device ensures the fast demagnetization with a

typical voltage (V_{demag}) of -18V.

This function allows to greatly reduce the power dissipation according to the formula:

$$P_{dem} = 0.5 \cdot L_{load} \cdot (I_{load})^2 \cdot [(V_{CC} + V_{demag}) / V_{demag}] \cdot f$$

where f = switching frequency and V_{demag} = demagnetization voltage

Based on this formula it is possible to know the value of inductance and/or current to avoid a thermal shut-down. The maximum inductance which causes the chip temperature to reach the shut down temperature in a specific thermal environment, is infact a function of the load current for a fixed V_{CC} , V_{demag} and f .

PROTECTING THE DEVICE AGAINST LOAD DUMP - TEST PULSE 5

The device is able to withstand the test pulse No. 5 at level II ($V_s = 46.5V$) according to the ISO T/R 7637/1 without any external component. This means that all functions of the device are performed as designed after exposure to disturbance at level II. The VN06SP is able to withstand the test pulse No.5 at level III adding an external resistor of 150 ohm between GND pin and ground plus a filter capacitor of 1000 μF between V_{CC} pin and ground (if $R_{LOAD} \leq 20 \Omega$).

PROTECTING THE DEVICE AGAINST REVERSE BATTERY

The simplest way to protect the device against a continuous reverse battery voltage (-26V) is to insert a Schottky diode between GND pin and ground, as shown in the typical application circuit (fig.3).

The consequences of the voltage drop across this diode are as follows:

If the input is pulled to power GND, a negative voltage of $-V_f$ is seen by the device. (V_{il} , V_{ih} thresholds and V_{stat} are increased by V_f with respect to power GND).

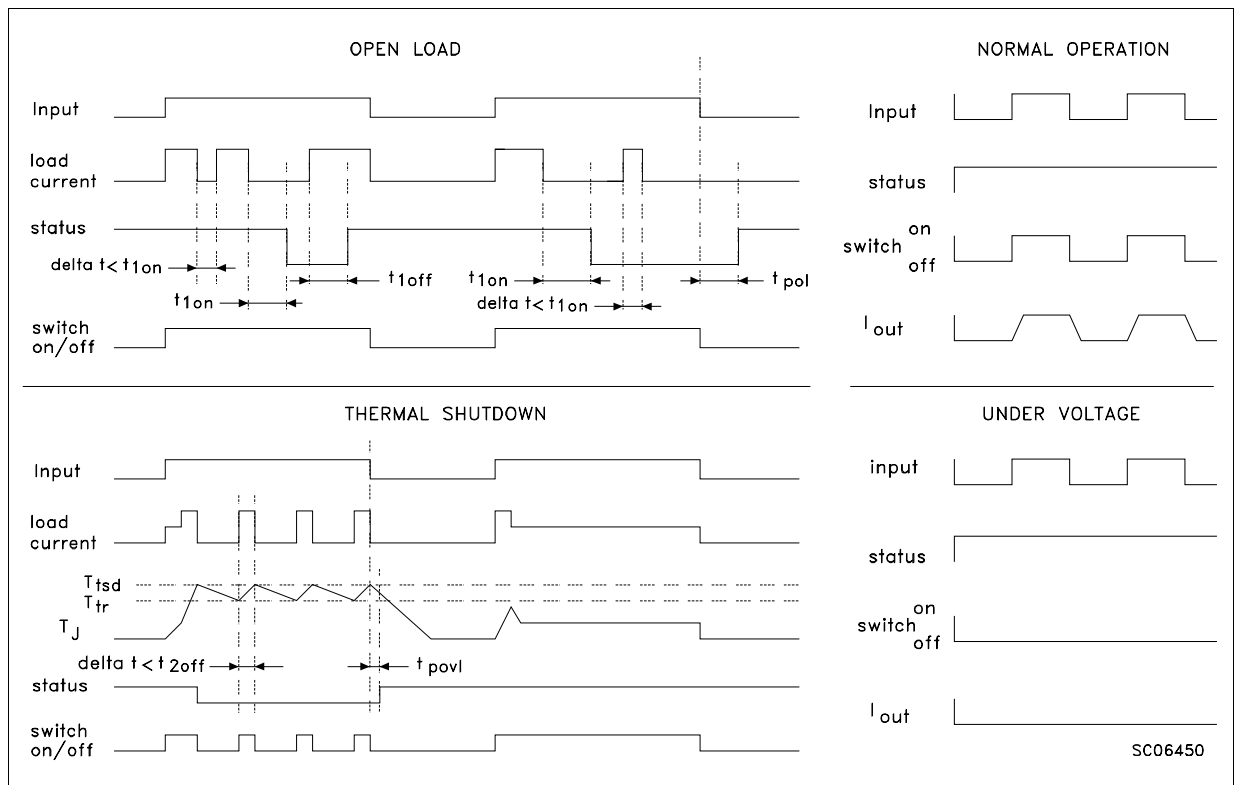
The undervoltage shutdown level is increased by V_f .

If there is no need for the control unit to handle external analog signals referred to the power GND, the best approach is to connect the reference potential of the control unit to node [6] (see application circuit in fig. 4), which becomes the common signal GND for the whole control board avoiding shift of V_{ih} , V_{il} and V_{stat} . This solution allows the use of a standard diode.

TRUTH TABLE

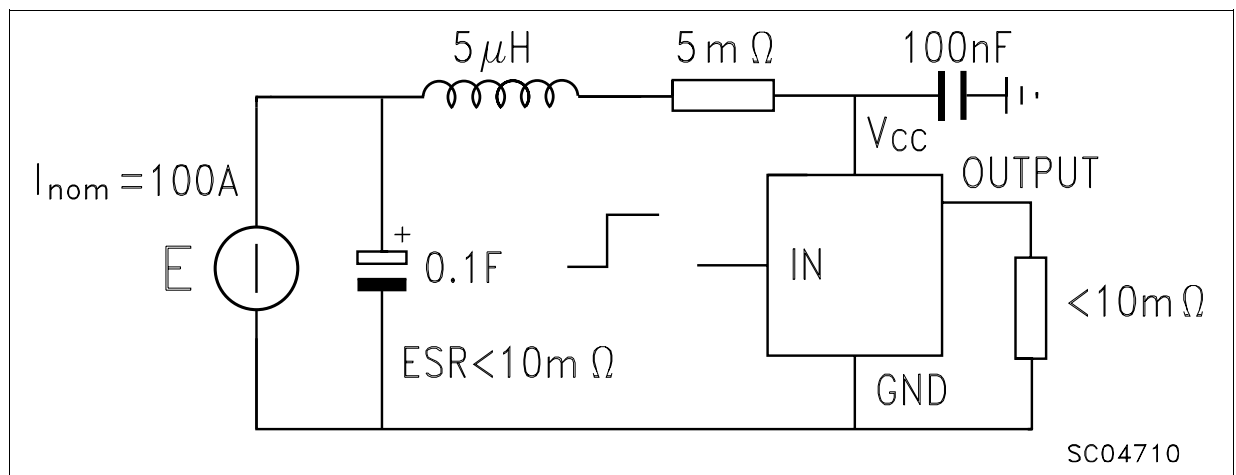
	INPUT	OUTPUT	DIAGNOSTIC
Normal Operation	L	L	H
	H	H	H
Open Circuit (No Load)	H	H	L
Over-temperature	H	L	L
Under-voltage	X	L	H
Short load to V _{CC}	L	H	L

Figure 1: Waveforms



SC06450

Figure 2: Over Current Test Circuit



SC04710

Figure 3: Typical Application Circuit With A Schottky Diode For Reverse Supply Protection

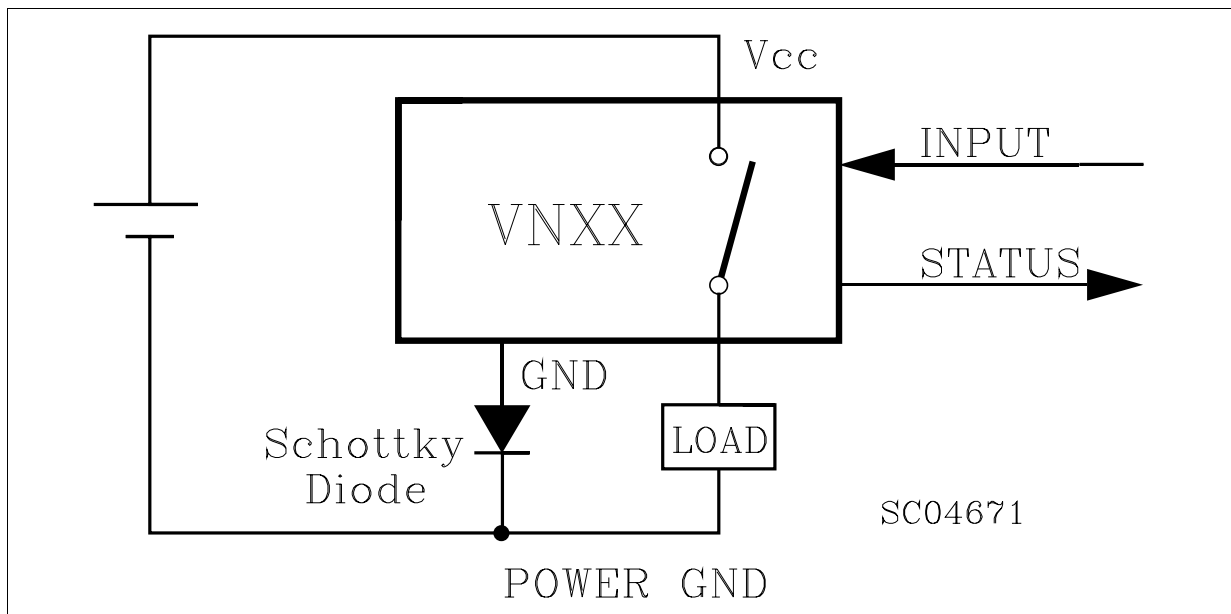
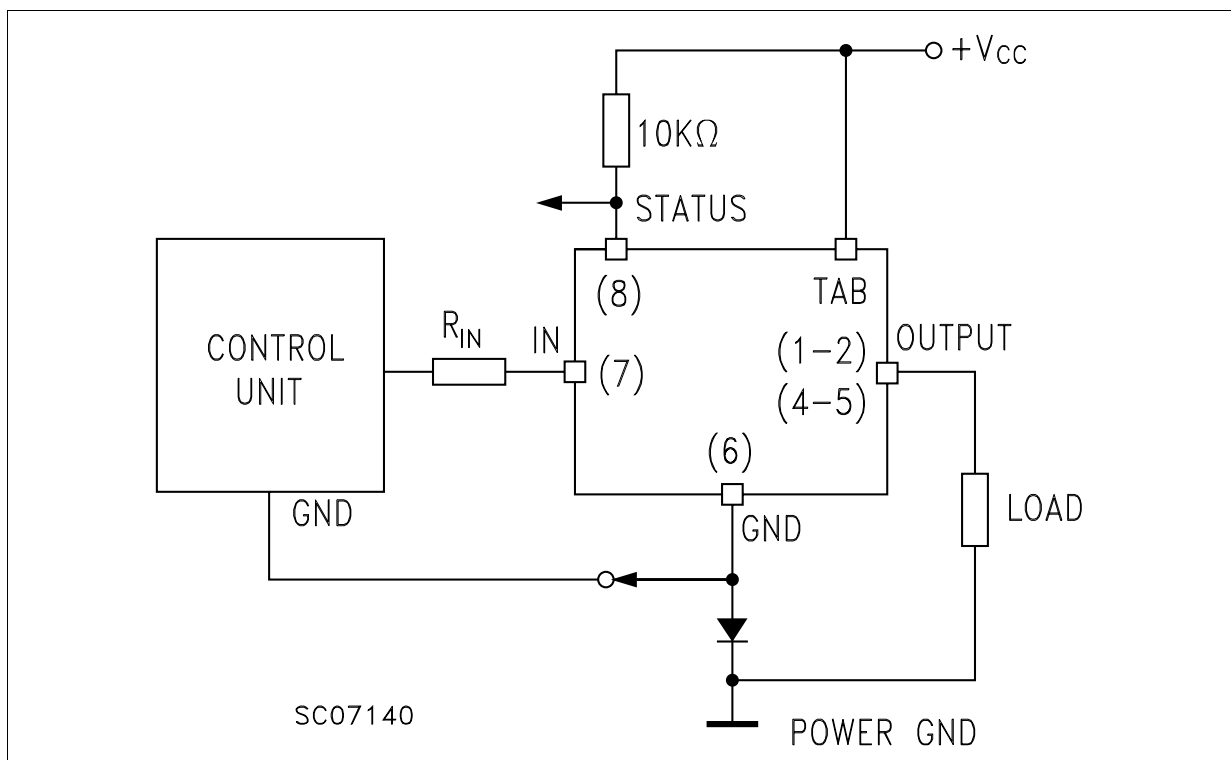
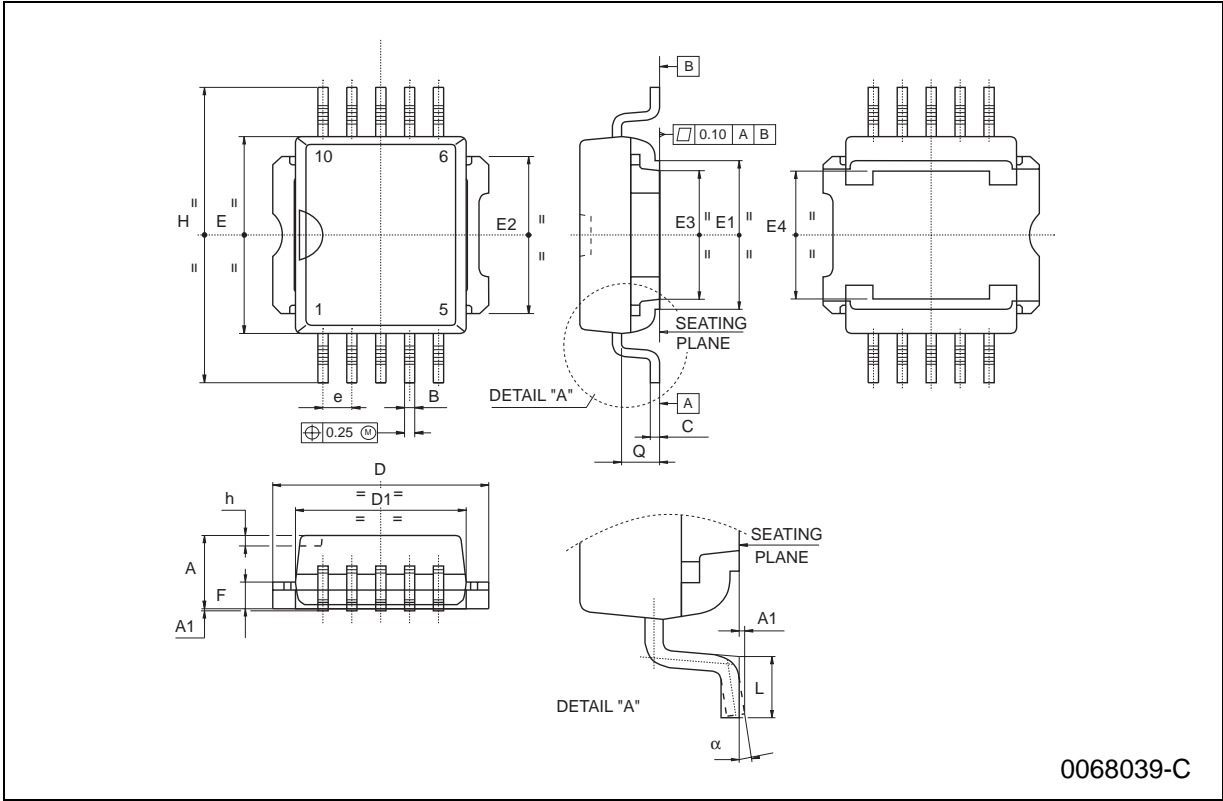


Figure 4: Typical Application Circuit With Separate Signal Ground



PowerSO-10 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	3.35		3.65	0.132		0.144
A1	0.00		0.10	0.000		0.004
B	0.40		0.60	0.016		0.024
c	0.35		0.55	0.013		0.022
D	9.40		9.60	0.370		0.378
D1	7.40		7.60	0.291		0.300
E	9.30		9.50	0.366		0.374
E1	7.20		7.40	0.283		0.291
E2	7.20		7.60	0.283		0.300
E3	6.10		6.35	0.240		0.250
E4	5.90		6.10	0.232		0.240
e		1.27			0.050	
F	1.25		1.35	0.049		0.053
H	13.80		14.40	0.543		0.567
h		0.50			0.002	
L	1.20		1.80	0.047		0.071
q		1.70			0.067	
α	0°		8°			



0068039-C

Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specification mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics.

The ST logo is a trademark of STMicroelectronics

© 1998 STMicroelectronics – Printed in Italy – All Rights Reserved

STMicroelectronics GROUP OF COMPANIES

Australia - Brazil - Canada - China - France - Germany - Italy - Japan - Korea - Malaysia - Malta - Mexico - Morocco - The Netherlands - Singapore - Spain - Sweden - Switzerland - Taiwan - Thailand - United Kingdom - U.S.A.