



High-Current, 10 Ω , SPST, CMOS Analog Switches

General Description

Maxim's MAX4655–MAX4658 are medium-voltage CMOS analog switches with low on-resistance of 10 Ω max, specifically designed to handle large switch currents. With a switch capability of up to 400mA peak current and 300mA continuous current (MAX4655/MAX4656), and up to 300mA peak current and 150mA continuous current (MAX4657/MAX4658), these parts can switch loads as low as 25 Ω . They can replace reed relays with a million times the speed and virtually unlimited number of lifetime cycles. Normal power consumption is only 3mW, whether the switch is on or off. These parts are TTL/CMOS compatible and will switch any voltage within its power-supply range.

These are SPST (single-pole/single-throw) switches. The MAX4655/MAX4657 are normally closed (NC), while the MAX4656/MAX4658 are normally open (NO). The difference between the MAX4655/MAX4656 and the MAX4657/MAX4658 is in the power dissipation of their packages. Refer to the *Absolute Maximum Ratings* and the *Electrical Characteristics*.

The MAX4655–MAX4658 power-supply range is from $\pm 4.5\text{V}$ to $\pm 20\text{V}$ for dual-supply operation and $+9\text{V}$ to $+40\text{V}$ for single-supply operation. These switches can operate from any combination of supplies, within a 40V V_+ to V_- range. They conduct equally well in either direction and can handle Rail-to-Rail[®] analog signals. The off-leakage current is only 1nA max at $T_A = +25^\circ\text{C}$. They are available in 8-pin μMAX , TDFN, and SO packages, with exposed pad options for high-power applications.

Applications

Relay Replacement
Test Equipment
Communication Systems
xDSL Modems
PBX, PABX Systems
Audio Signal Routing
Audio Systems
PC Multimedia Boards
Redundant/Backup Systems

Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.

Features

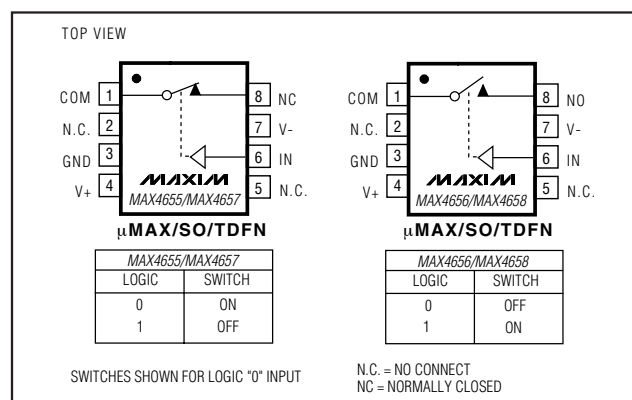
- ◆ High Continuous Current Handling
300mA (MAX4655/MAX4656)
150mA (MAX4657/MAX4658)
- ◆ High Peak Current Handling
400mA (MAX4655/MAX4656)
300mA (MAX4657/MAX4658)
- ◆ 10 Ω max On-Resistance ($\pm 15\text{V}$ supplies)
- ◆ V_L not Required
- ◆ 1 Ω max R_{ON} Flatness over Specified Signal Range
- ◆ Rail-to-Rail Signal Handling
- ◆ $+12\text{V}$ Single Supply or $\pm 15\text{V}$ Dual Supply Operation
- ◆ Pin Compatible with DG417, DG418

Ordering Information

PART	TEMP RANGE	PIN-PACKAGE	TOP MARK
MAX4655ETA	-40°C to +85°C	8 TDFN	AKT
MAX4655EUA	-40°C to +85°C	8 μMAX -EP*	AAAL
MAX4655ESA	-40°C to +85°C	8 SO-EP*	—
MAX4656ETA	-40°C to +85°C	8 TDFN	AKU
MAX4656EUA	-40°C to +85°C	8 μMAX -EP*	AAAM
MAX4656ESA	-40°C to +85°C	8 SO-EP*	—
MAX4657EUA	-40°C to +85°C	8 μMAX	—
MAX4657ESA	-40°C to +85°C	8 SO	—
MAX4658EUA	-40°C to +85°C	8 μMAX	—
MAX4658ESA	-40°C to +85°C	8 SO	—

*EP = Exposed paddle.

Pin Configurations/ Functional Diagrams/Truth Tables



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ABSOLUTE MAXIMUM RATINGS

V+ to GND	-0.3V to +44V
V- to GND	-44V to +0.3V
V+ to V-	-0.3V to +44V
All Other Pins to GND (Note 1)	V- - 0.3V to V+ + 0.3V
Continuous Current, COM, NO, NC (MAX4655/MAX4656)	$\pm 300\text{mA}$
Continuous Current, COM, NO, NC (MAX4657/MAX4658)	$\pm 150\text{mA}$
Continuous Current, IN	$\pm 30\text{mA}$
Peak Current, COM, NO, NC (pulsed at 1ms, 10% duty cycle)	
MAX4655/MAX4656	$\pm 400\text{mA}$
MAX4657/MAX4658	$\pm 300\text{mA}$

Continuous Power Dissipation ($T_A = +70^\circ\text{C}$)	
8-Pin TDFN (derate 24.4mW/ $^\circ\text{C}$ above $+70^\circ\text{C}$)	1951mW
8-Pin $\mu\text{MAX-EP}$ (derate 10.3mW/ $^\circ\text{C}$ above $+70^\circ\text{C}$)	
MAX4655/MAX4656	825mW
8-Pin μMAX (derate 4.50mW/ $^\circ\text{C}$ above $+70^\circ\text{C}$)	
MAX4657/MAX4658	362mW
8-Pin SO-EP (derate 18.9mW/ $^\circ\text{C}$ above $+70^\circ\text{C}$)	
MAX4655/MAX4656	1509mW
8-Pin SO (derate 5.88mW/ $^\circ\text{C}$ above $+70^\circ\text{C}$)	
MAX4657/MAX4658	471mW
Operating Temperature Ranges	
MAX4655-MAX4658	-40°C to $+85^\circ\text{C}$
Junction Temperature	$+150^\circ\text{C}$
Storage Temperature Range	-65°C to $+150^\circ\text{C}$
Lead Temperature (soldering, 10s)	$+300^\circ\text{C}$

Note 1: Signals on NC, NO, COM, or IN exceeding V+ or V- will be clamped by internal diodes. Limit forward diode current to maximum current rating.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—Dual Supplies

(V+ = +15V, V- = -15V, $V_{IH} = 2.4\text{V}$, $V_{IL} = 0.8\text{V}$, $T_A = T_{MIN}$ to T_{MAX} , unless otherwise noted. Typical values are at $T_A = +25^\circ\text{C}$.)
(Notes 2, 7, 8)

PARAMETER	SYMBOL	CONDITIONS	T_A	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V_{NO} , V_{NC} , V_{COM}			V-		V+	V
On-Resistance	R_{ON}	$I_{COM} = 100\text{mA}$; V_{NO} or $V_{NC} = \pm 10\text{V}$	$+25^\circ\text{C}$ T_{MIN} to T_{MAX}		7 10	15	Ω
On-Resistance Flatness (Note 3)	$R_{FLAT(ON)}$	$I_{COM} = 100\text{mA}$; V_{NO} or $V_{NC} = -5\text{V}, 0, +5\text{V}$	$+25^\circ\text{C}$ T_{MIN} to T_{MAX}		0.3 1	1.5	Ω
NO or NC Off-Leakage Current (Note 4)	$I_{NO(OFF)}$ or $I_{NC(OFF)}$	$V_{COM} = +14.5\text{V}, -14.5\text{V}$; V_{NO} or $V_{NC} = -14.5\text{V}, +14.5\text{V}$	$+25^\circ\text{C}$ T_{MIN} to T_{MAX}	-1 -10	0.01 10	1	nA
COM Off-Leakage Current (Note 4)	$I_{COM(OFF)}$	$V_{COM} = +14.5\text{V}, -14.5\text{V}$; V_{NO} or $V_{NC} = -14.5\text{V}, +14.5\text{V}$	$+25^\circ\text{C}$ T_{MIN} to T_{MAX}	-1 -10	0.01 10	1	nA
COM On-Leakage Current (Note 4)	$I_{COM(ON)}$	$V_{COM} = +14.5\text{V}, -14.5\text{V}$; V_{NO} or $V_{NC} = +14.5\text{V}$, -14.5V, or floating	$+25^\circ\text{C}$ T_{MIN} to T_{MAX}	-2 -20		2 20	nA
DYNAMIC CHARACTERISTICS							
Turn-On Time	t_{ON}	V_{NO} or $V_{NC} = 10\text{V}$; $R_L = 50\Omega$; MAX4655/4656, $R_L = 100\Omega$; MAX4657/4658, $C_L = 35\text{pF}$; Figure 3	$+25^\circ\text{C}$ T_{MIN} to T_{MAX}		110 300	200	ns
Turn-Off Time	t_{OFF}	V_{NO} or $V_{NC} = 10\text{V}$; $R_L = 50\Omega$; MAX4655/4656, $R_L = 100\Omega$; MAX4657/4658, $C_L = 35\text{pF}$; Figure 3	$+25^\circ\text{C}$ T_{MIN} to T_{MAX}		75 150	100	ns

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MAX4655-MAX4658

ELECTRICAL CHARACTERISTICS—Dual Supplies (continued)

(V+ = +15V, V- = -15V, V_{IH} = 2.4V, V_{IL} = 0.8V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.)
(Notes 2, 7, 8)

PARAMETER	SYMBOL	CONDITIONS	T _A	MIN	TYP	MAX	UNITS
Charge Injection	Q	V _{GEN} = 0; R _{GEN} = 0; C _L = 1nF; Figure 4	+25°C		23		pC
-3dB Bandwidth	BW		+25°C		210		MHz
Off-Isolation (Note 5)	V _{ISO}	f = 1MHz; R _L = 50Ω; Figure 5	+25°C		-77		dB
Total Harmonic Distortion	THD	f = 20Hz to 20kHz, V _{NL} = 5V _{p-p} ; R _L = 600Ω	+25°C		0.007		%
NO or NC Off-Capacitance	C _{NO(OFF)} , C _{NC(OFF)}	f = 1MHz; Figure 6	+25°C		25		pF
COM Off-Capacitance	C _{COM(OFF)}	f = 1MHz; Figure 6	+25°C		25		pF
COM On-Capacitance	C _{COM(ON)}	f = 1MHz; Figure 7	+25°C		67		pF
DIGITAL I/O							
Input Logic High	V _{IH}		T _{MIN} to T _{MAX}	2.4			V
Input Logic Low	V _{IL}		T _{MIN} to T _{MAX}			0.8	V
Input Leakage Current	I _{IN}	V _{IN} = 0.8V or 2.4V	T _{MIN} to T _{MAX}	-1		1	μA
POWER SUPPLY							
Power-Supply Range			T _{MIN} to T _{MAX}	±4.5		±20	V
Positive Supply Current	I+	V _{IN} = 0 or 5V, V _{NL} = 3V; I _{SWITCH} = 200mA, MAX4655/4656; I _{SWITCH} = 100mA, MAX4657/4658	+25°C		90	150	μA
			T _{MIN} to T _{MAX}			300	
Negative Supply Current	I-	V _{IN} = 0 or 5V, V _{NL} = 3V; I _{SWITCH} = 200mA, MAX4655/4656; I _{SWITCH} = 100mA, MAX4657/4658	+25°C		10	50	μA
			T _{MIN} to T _{MAX}			100	
Ground Current	I _{GND}	V _{IN} = 0 or 5V, V _{NL} = 3V; I _{SWITCH} = 200mA, MAX4655/4656; I _{SWITCH} = 100mA, MAX4657/4658	+25°C		80	130	μA
			T _{MIN} to T _{MAX}			260	

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ELECTRICAL CHARACTERISTICS—Single Supply

(V₊ = +12V, V₋ = 0, V_{IH} = 2.4V, V_{IL} = 0.8V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	T _A	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V _{IN}		T _{MIN} to T _{MAX}	0		V+	V
On-Resistance	R _{ON}	I _{COM} = 50mA, V _{NO} or V _{NC} = 10V	+25°C		15	22	Ω
			T _{MIN} to T _{MAX}			33	
On-Resistance Flatness (Note 3)	R _{FLAT} (ON)	I _{COM} = 50mA, V _{NO} or V _{NC} = 2V, 6V, 10V	+25°C		2.2	4	Ω
			T _{MIN} to T _{MAX}			5	
DYNAMIC CHARACTERISTICS							
Turn-On Time	t _{ON}	V _{NO} or V _{NC} = 10V; R _L = 100Ω MAX4655/4656, R _L = 200Ω MAX4657/4658, C _L = 35pF; Figure 3	+25°C		140	200	ns
			T _{MIN} to T _{MAX}			300	
Turn-Off Time	t _{OFF}	V _{NO} or V _{NC} = 10V; R _L = 100Ω MAX4655/4656, R _L = 200Ω MAX4657/4658, C _L = 35pF; Figure 3	+25°C		65	125	ns
			T _{MIN} to T _{MAX}			200	
Charge Injection	Q	V _{GEN} = 0; R _{GEN} = 0; C _L = 1nF; Figure 4	+25°C		1		pC
POWER SUPPLY							
Power-Supply Range	V+			9		40	V
Positive Supply Current (Note 6)	I+	V _{IN} = 0 or 12V, I _{SWITCH} = 100mA, MAX4655/4656; I _{SWITCH} = 50mA, MAX4657/4658	+25°C		25	100	μA
			T _{MIN} to T _{MAX}			200	
		V _{IN} = 0 or 5V, I _{SWITCH} = 100mA, MAX4655/4656; I _{SWITCH} = 50mA, MAX4657/4658	+25°C		46	125	
			T _{MIN} to T _{MAX}			200	

Note 2: The algebraic convention is used in this data sheet; the most negative value is shown in the minimum column.

Note 3: Flatness is defined as the difference between the maximum and minimum value of on-resistance as measured over the specified analog signal range.

Note 4: Leakage parameters are 100% tested at maximum rated hot temperature and guaranteed by correlation at T_A = +25°C.

Note 5: Off-isolation = 20log₁₀ [V_{COM} / (V_{NC} or V_{NO})], V_{COM} = output, V_{NC} or V_{NO} = input to off switch.

Note 6: Guaranteed by testing with dual supplies.

Note 7: -40°C specifications are guaranteed by design.

Note 8: TDFN parts are tested at +25°C and guaranteed by design over the entire temperature range.

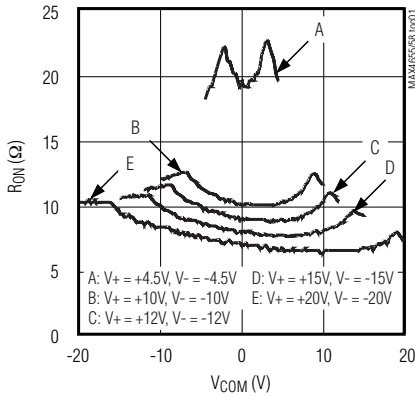
High-Current, 10 Ω , SPST, CMOS Analog Switches

Typical Operating Characteristics

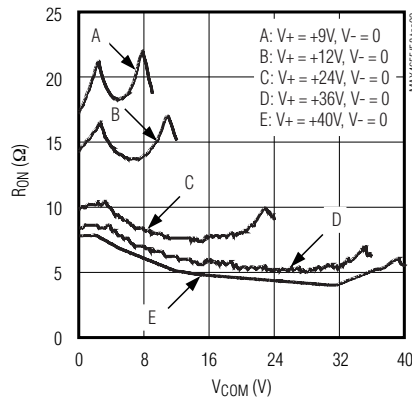
($T_A = +25^\circ\text{C}$, unless otherwise noted.)

MAX4655-MAX4658

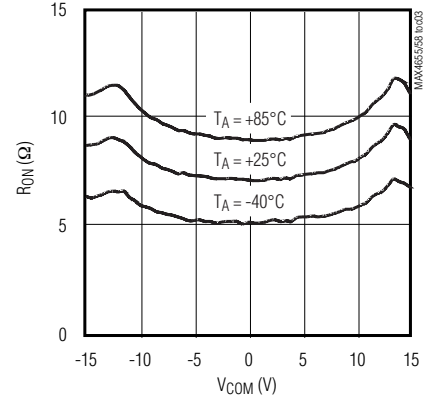
**ON-RESISTANCE vs. V_{COM}
(DUAL SUPPLIES)**



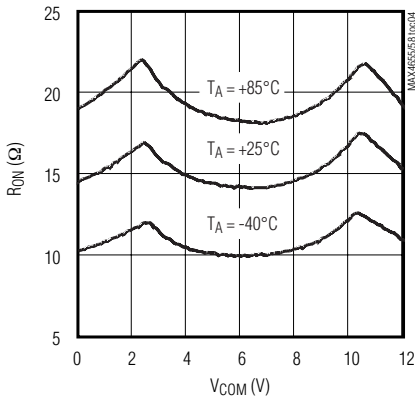
**ON-RESISTANCE vs. V_{COM}
(SINGLE SUPPLY)**



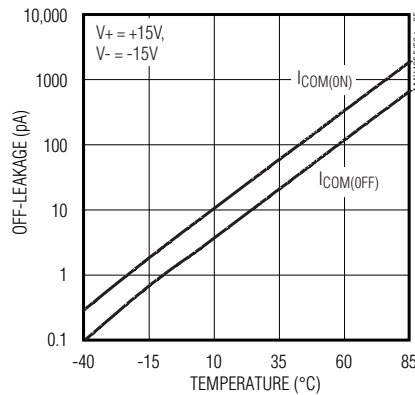
**ON-RESISTANCE vs. V_{COM}
AND TEMPERATURE (DUAL SUPPLIES)**



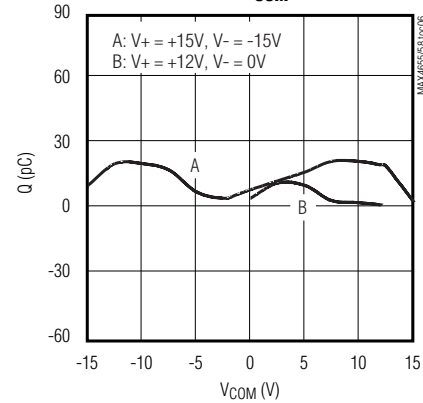
**ON-RESISTANCE vs. V_{COM}
AND TEMPERATURE (SINGLE SUPPLY)**



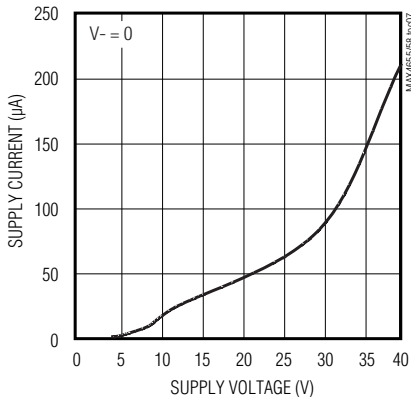
**LEAKAGE CURRENT
vs. TEMPERATURE**



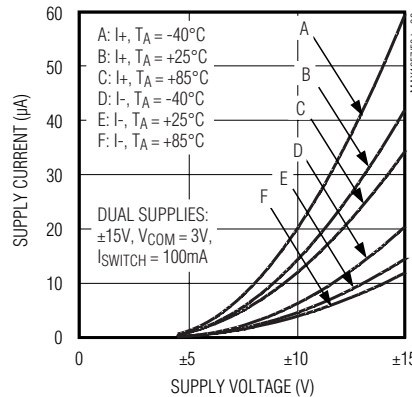
**CHARGE INJECTION
vs. V_{COM}**



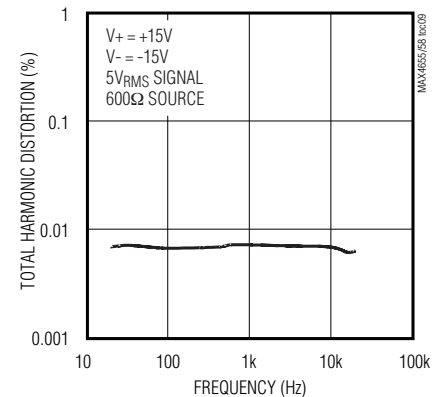
**SUPPLY CURRENT vs. SUPPLY VOLTAGE
(SINGLE SUPPLY)**



**SUPPLY CURRENT
vs. SUPPLY VOLTAGE AND TEMPERATURE**



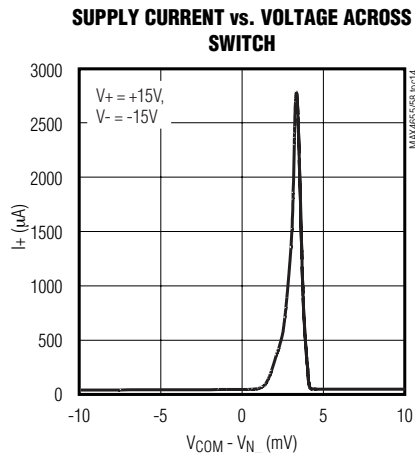
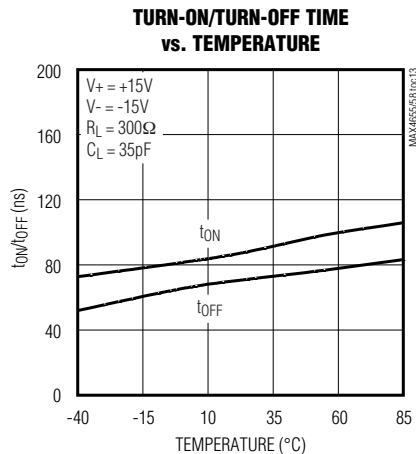
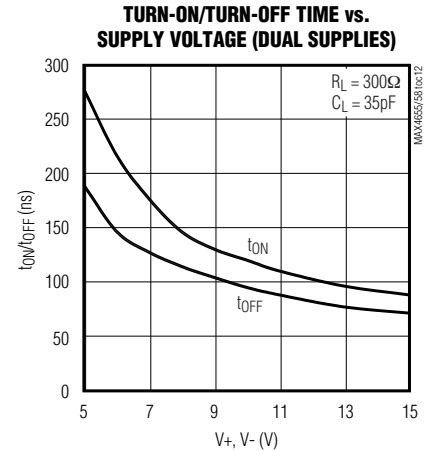
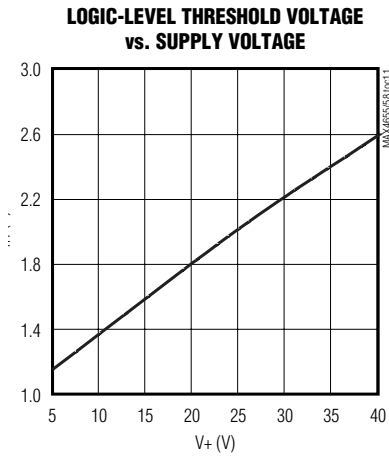
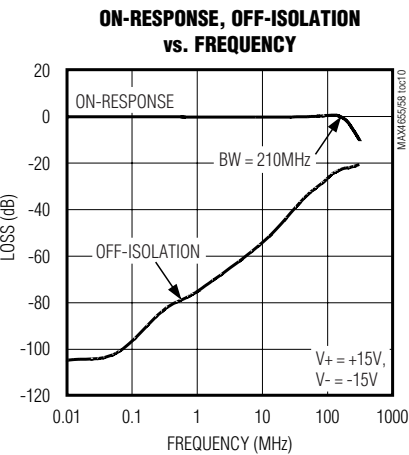
**TOTAL HARMONIC DISTORTION
vs. FREQUENCY**



High-Current, 10Ω, SPST, CMOS Analog Switches

Typical Operating Characteristics (continued)

(T_A = +25°C, unless otherwise noted.)



Pin Description

PIN		NAME	FUNCTION
MAX4655/ MAX4657	MAX4656/ MAX4658		
1	1	COM	Analog Switch Common
2, 5	2, 5	N.C.	No Internal Connection
3	3	GND	Ground
4	4	V+	Positive Supply Voltage Input
6	6	IN	Digital Control Input
7	7	V-	Negative Supply Voltage Input
—	8	NO	Analog Switch Normally Open
8	—	NC	Analog Switch Normally Closed
EP	—	PAD	Exposed Pad. Connect to GND.

High-Current, 10 Ω , SPST, CMOS Analog Switches

Detailed Description

The MAX4655-MAX4658 are single SPST CMOS analog switches. The CMOS switch construction provides rail-to-rail signal handling while consuming very little power. The switch is controlled by a TTL/CMOS level compatible digital input. The MAX4655/MAX4657 are normally closed switches, and the MAX4656/MAX4658 are normally open switches.

These devices can be operated with either single power supplies or dual power supplies. Operation at up to $\pm 20\text{V}$ supplies allows users a wide switching dynamic range. Additionally, asymmetrical operation is possible to tailor performance to a particular application.

These switches have been specifically designed to handle high switch currents, up to 400mA peak current and 300mA continuous currents. In order to do this, a new technique is used to drive the body of the output N-channel device. (Note: the basic switch between the input NC/NO terminal, and the output common terminal consists of an N-channel MOSFET and a P-channel MOSFET in parallel.) The standard method limits operation to approximately a 600mV drop across the switch. More than 600mV causes an increase in I_{DQ} leakage current (due to the turn-on of on-chip parasitic diodes) and an increase in V_+ supply current. With the new sensing method, there is no limitation to the voltage drop across the switch. Current and voltage are limited only by the power dissipation rating of the package and the absolute maximum ratings of the switch.

When the analog input to output voltage drop is approximately 7mV there is an increase in power supply current from typically 90 μA to 2mA within a 1mV to 7mV range, caused by the new sensing/driving circuitry.

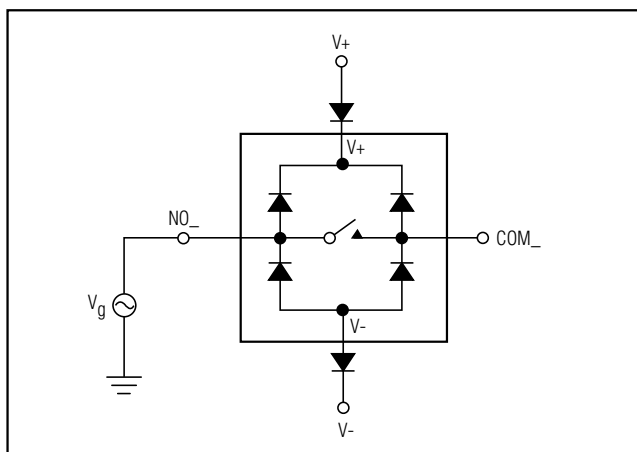


Figure 1. Overvoltage Protection Using Blocking Diodes

Applications Information

Overvoltage Protection

Proper power-supply sequencing is recommended for all CMOS devices. Do not exceed the absolute maximum ratings, because stresses beyond the listed ratings can cause permanent damage to the devices. First, connect GND, followed by V_+ , V_- , and the remaining pins. If power-supply sequencing is not possible, add two small signal diodes (D1, D2) in series with supply pins (Figure 1). Adding diodes reduces the analog signal range to one diode drop below V_+ and one diode drop above V_- , but does not affect the devices' low switch resistance and low leakage characteristics. Device operation is unchanged, and the difference between V_+ and V_- should not exceed 44V. The protection diode for the negative supply is not required when V_- is connected to GND.

Off-Isolation at High Frequencies

In 50 Ω systems, the high-frequency on-response of these parts extends from DC to above 100MHz, with a typical loss of -2dB. When the switch is turned off, however, it behaves like a capacitor, and off-isolation decreases with increasing frequency. This effect is more pronounced with higher source and load impedances. Above 5MHz, circuit board layout becomes critical. The graphs shown in the *Typical Operating Characteristics* were taken using a 50 Ω source and load connected with BNC connectors.

High-Current, 10Ω , SPST, CMOS Analog Switches

Test Circuits/Timing Diagrams

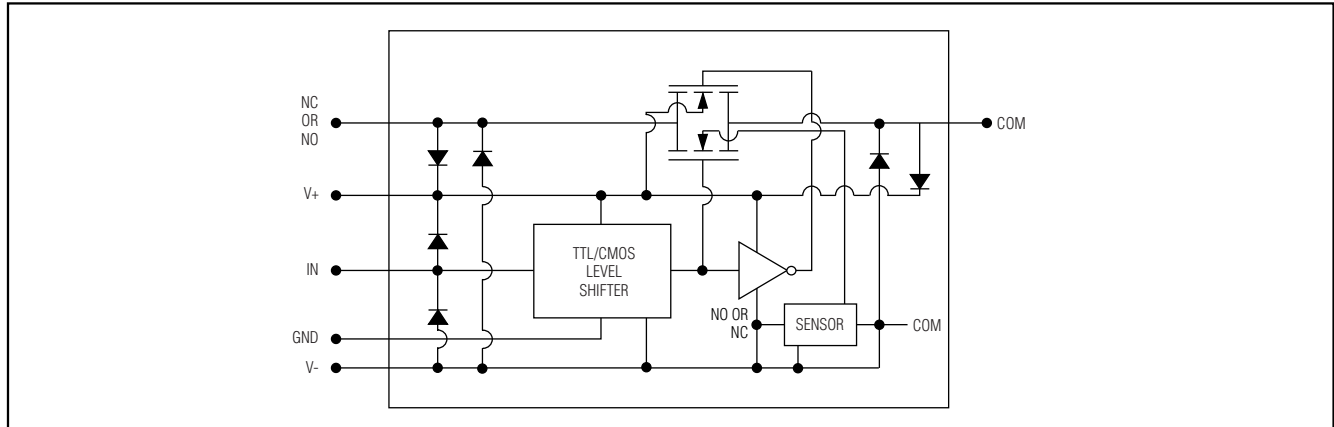


Figure 2. Block Diagram

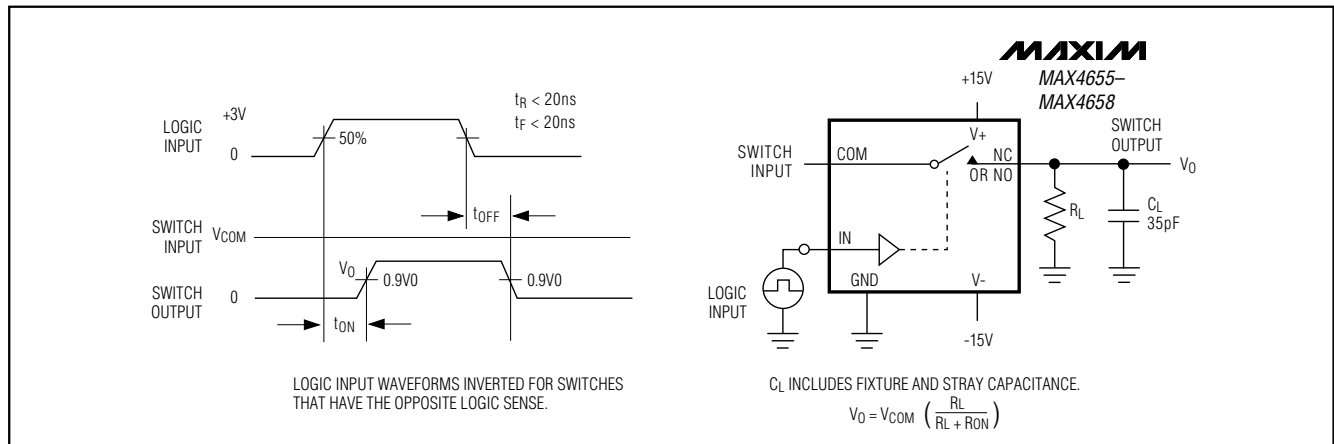


Figure 3. Switching Time

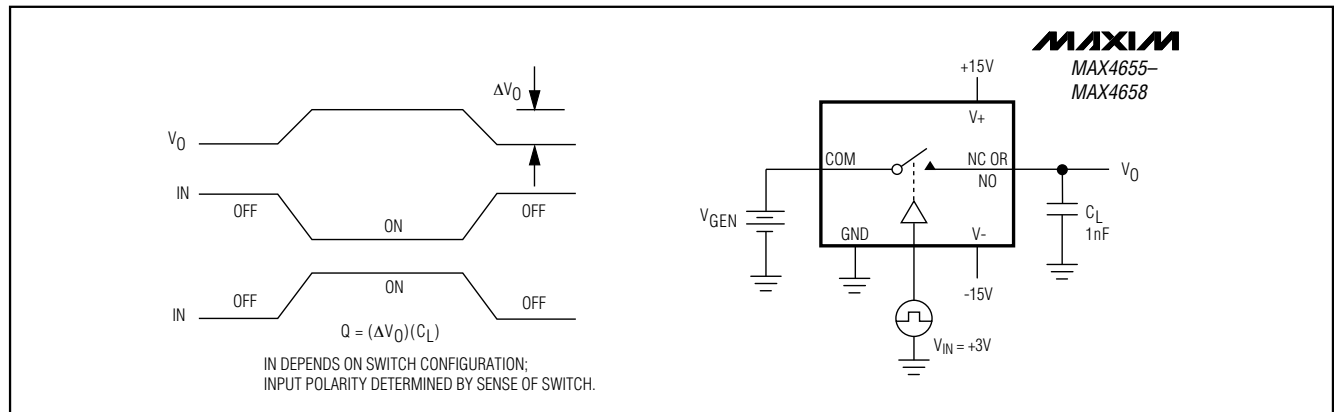


Figure 4. Charge Injection

High-Current, 10 Ω , SPST, CMOS Analog Switches

Test Circuits/Timing Diagrams (continued)

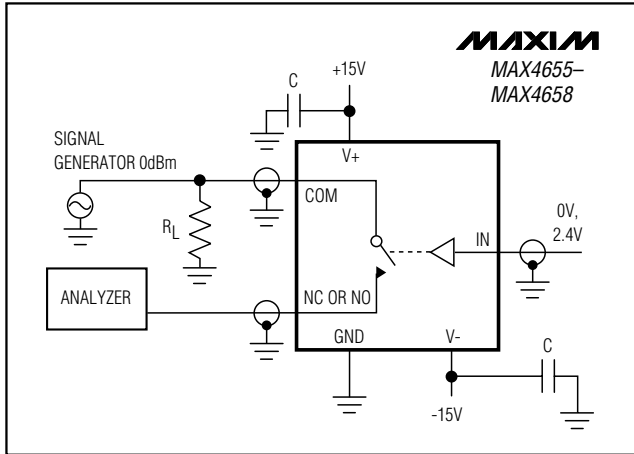


Figure 5. Off-Isolation

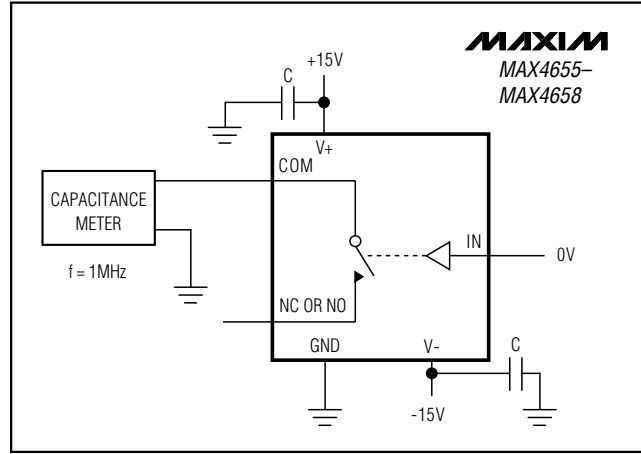


Figure 7. Channel On-Capacitance

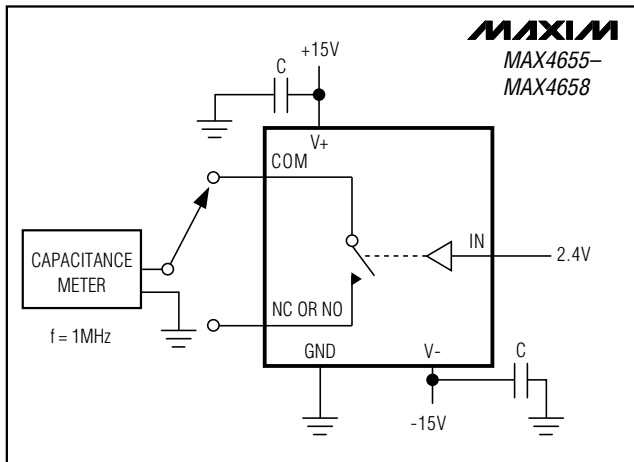


Figure 6. Channel Off-Capacitance

Chip Information

TRANSISTOR COUNT: 45

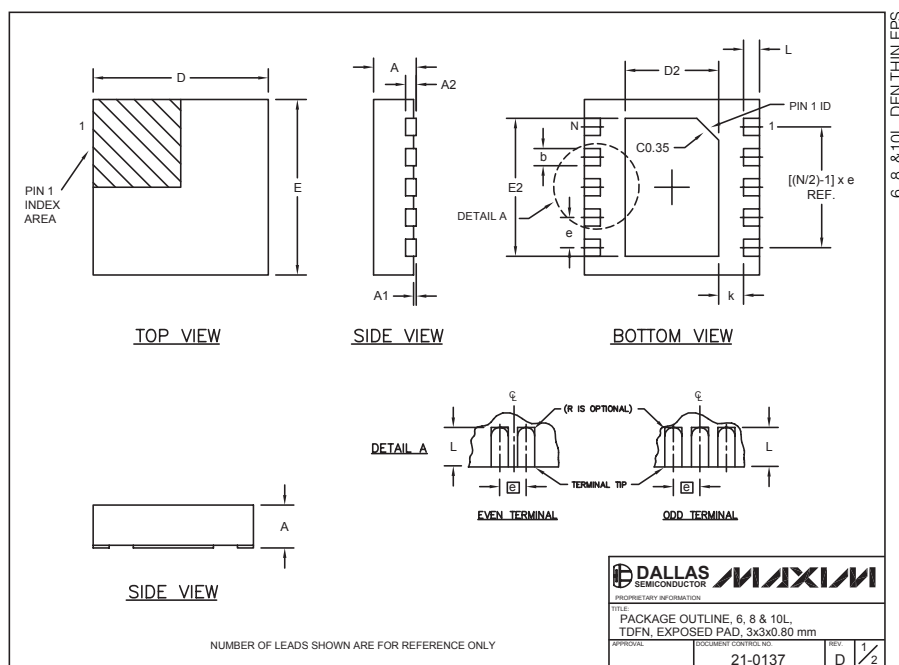
PROCESS: CMOS

MAX4655-MAX4658

High Current, 10Ω , SPST, CMOS Analog Switches

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to www.maxim-ic.com/packages.)



COMMON DIMENSIONS		
SYMBOL	MIN.	MAX.
A	0.70	0.80
D	2.90	3.10
E	2.90	3.10
A1	0.00	0.05
L	0.20	0.40
k	0.25 MIN.	
A2	0.20 REF.	

PACKAGE VARIATIONS							
PKG. CODE	N	D2	E2	e	JEDEC SPEC	b	[(N/2)-1] x e
T633-1	6	1.50±0.10	2.30±0.10	0.95 BSC	MO229 / WEEA	0.40±0.05	1.90 REF
T833-1	8	1.50±0.10	2.30±0.10	0.65 BSC	MO229 / WEEC	0.30±0.05	1.95 REF
T1033-1	10	1.50±0.10	2.30±0.10	0.50 BSC	MO229 / WEED-3	0.25±0.05	2.00 REF

NOTES:

- ALL DIMENSIONS ARE IN mm. ANGLES IN DEGREES.
- COPLANARITY SHALL NOT EXCEED 0.08 mm.
- WARPAGE SHALL NOT EXCEED 0.10 mm.
- PACKAGE LENGTH/PACKAGE WIDTH ARE CONSIDERED AS SPECIAL CHARACTERISTIC(S).
- DRAWING CONFORMS TO JEDEC MO229, EXCEPT DIMENSIONS "D2" AND "E2".
- "N" IS THE TOTAL NUMBER OF LEADS.

DALLAS MAXIM SEMICONDUCTOR		
PROPRIETARY INFORMATION		
TITLE: PACKAGE OUTLINE, 6, 8 & 10L, TDFN, EXPOSED PAD, 3x3x0.80 mm		
APPROVAL: 21-0137	REV: D	2/2

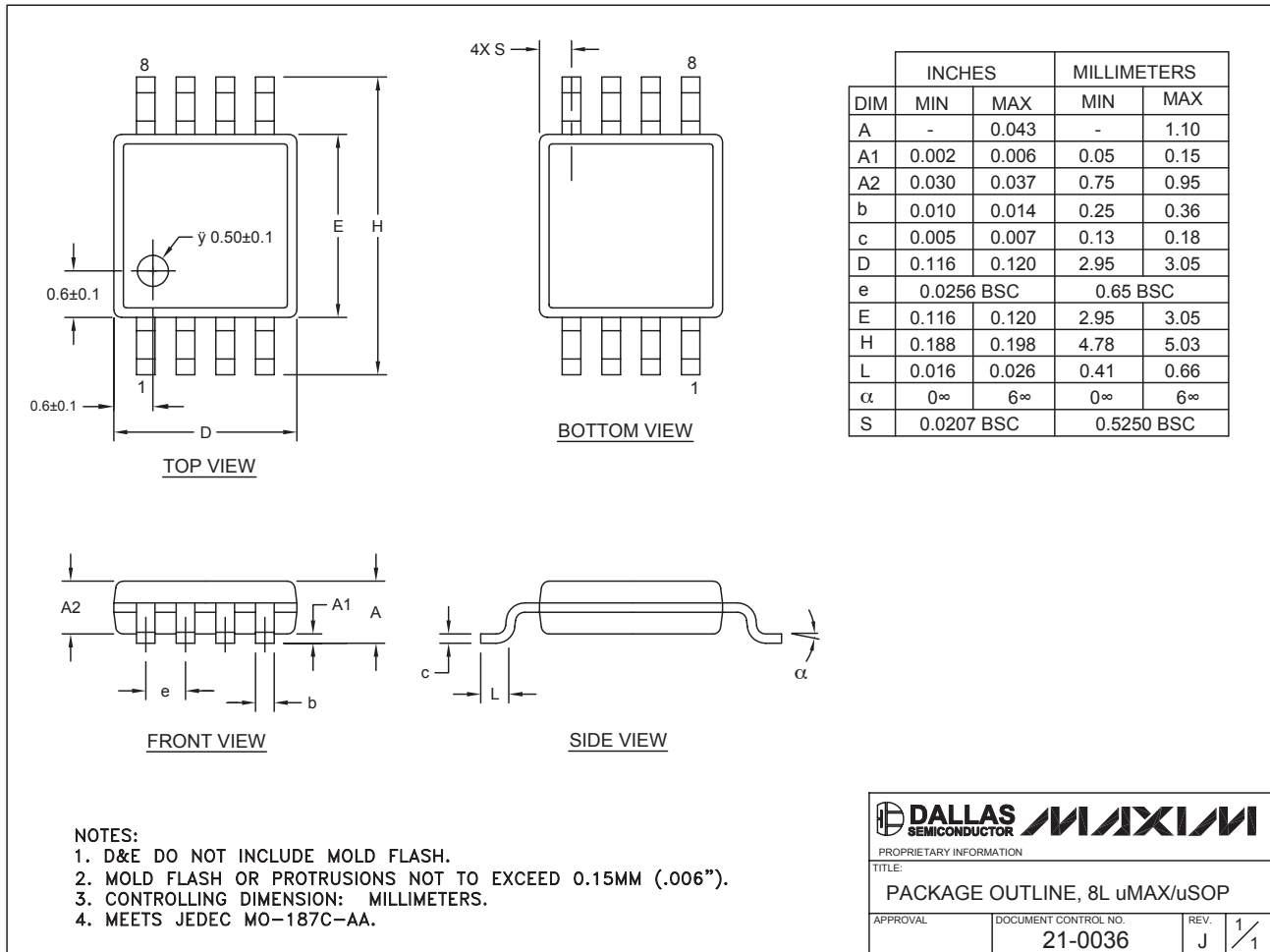
High Current, 10Ω, SPST, CMOS Analog Switches

Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to www.maxim-ic.com/packages.)

MAX4655-MAX4658

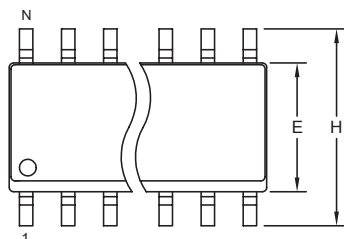
8LUMAXD.EPS



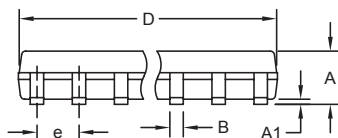
High Current, 10Ω , SPST, CMOS Analog Switches

Package Information (continued)

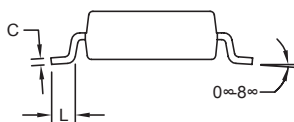
(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to www.maxim-ic.com/packages.)



TOP VIEW



FRONT VIEW



SIDE VIEW

NOTES:

1. D&E DO NOT INCLUDE MOLD FLASH.
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED 0.15mm (.006").
3. LEADS TO BE COPLANAR WITHIN 0.10mm (.004").
4. CONTROLLING DIMENSION: MILLIMETERS.
5. MEETS JEDEC MS012.
6. N = NUMBER OF PINS.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.053	0.069	1.35	1.75
A1	0.004	0.010	0.10	0.25
B	0.014	0.019	0.35	0.49
C	0.007	0.010	0.19	0.25
e	0.050 BSC		1.27 BSC	
E	0.150	0.157	3.80	4.00
H	0.228	0.244	5.80	6.20
L	0.016	0.050	0.40	1.27

VARIATIONS:

DIM	INCHES		MILLIMETERS		N	MS012
	MIN	MAX	MIN	MAX		
D	0.189	0.197	4.80	5.00	8	AA
D	0.337	0.344	8.55	8.75	14	AB
D	0.386	0.394	9.80	10.00	16	AC

SOICN EPS

 DALLAS SEMICONDUCTOR			
PROPRIETARY INFORMATION			
TITLE:			
PACKAGE OUTLINE, .150" SOIC			
APPROVAL	DOCUMENT CONTROL NO.	REV.	1/
	21-0041	B	

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