

8961726 TEXAS INSTR (OPTO)

62C 36635 D

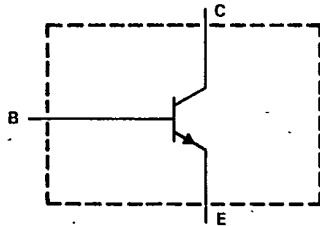
BU406, BU407
N-P-N SILICON POWER TRANSISTORS

T-33-11

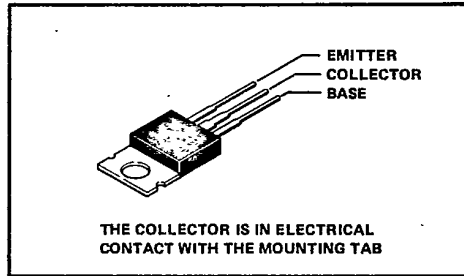
OCTOBER 1982 -- REVISED OCTOBER 1984

- 60 W at 25°C Case Temperature
- 7 A Continuous Collector Current
- 15 A Peak Collector Current
- Specifically Designed for Portable TV Linescan Applications for MTV Receiver with 110-Degree Tubes

device schematic



TO-220AB PACKAGE



absolute maximum ratings at 25°C case temperature (unless otherwise noted)

	BU406	BU407
Collector-base voltage	400 V	330 V
Collector-emitter voltage ($I_B = 0$)	200 V	150 V
Collector-emitter voltage ($V_{BE} = -2V$)	400 V	330 V
Emitter-base voltage	6 V	
Continuous base current	4 A	
Continuous collector current	7 A	
Peak collector current (see Note 1)	15 A	
Continuous device dissipation at (or below) 25°C case temperature	60 W	
Operating junction temperature range	- 55°C to 150°C	

NOTE 1: This value applies for $t_w = 10$ ms, duty cycle = 25%.



BD, BDW, BDX, BU, BUX, BUY Devices

8961726 TEXAS INSTR (OPTO)

62C 36636 D

T-33-11

**BU406, BU407
N-P-N SILICON POWER TRANSISTORS**

electrical characteristics at 25°C case temperature (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
V _{(BR)CEO}	I _C = 30 mA, I _B = 0	140			V
I _{CES}	V _{CE} = 400 V, V _{BE} = 0		5		mA
	V _{CE} = 250 V, V _{BE} = 0		0.1		
	V _{CE} = 250 V, V _{BE} = 0, T _C = 150°C		1		
	V _{CE} = 330 V, V _{BE} = 0		5		
	V _{CE} = 200 V, V _{BE} = 0		0.1		
	V _{CE} = 200 V, V _{BE} = 0, T _C = 150°C		1		
I _{EBO}	V _{EB} = 6 V, I _C = 0			1	mA
h _{FE}	V _{CE} = 10 V, I _C = 4 A, See Note 2		12		
	V _{CE} = 10 V, I _C = 0.5 A, See Note 2		20		
V _{CE(sat)}	I _B = 0.5 A, I _C = 5 A, See Note 2			1	V
V _{BE(sat)}	I _B = 0.5 A, I _C = 5 A, See Note 2			1.2	V
f _T	V _{CE} = 5 V, I _C = 0.5 A, See Note 3		6		MHz
C _{obo}	V _{CB} = 20 V, I _E = 0, f = 1 MHz		60		pF

NOTES: 2. These parameters must be measured using pulse techniques, t_W = 300 μs, duty cycle = 2%.
3. To obtain f_T, the |h_{FE}| response is extrapolated at the rate of -6 dB per octave from f = 1 MHz to the frequency at which |h_{FE}| = 1.

thermal characteristics

PARAMETER	MIN	TYP	MAX	UNIT
R _{θJC}		2.08		°C/W
R _{θJA}		70		°C/W

inductive-load switching characteristics at 25°C case temperature (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t _s	I _C = 5 A, I _{B(end)} = 0.5 A,		2.7		μs
t _{off}	See Figure 1		0.75		μs

BD, BDW, BDX, BU, BUX, BUY Devices

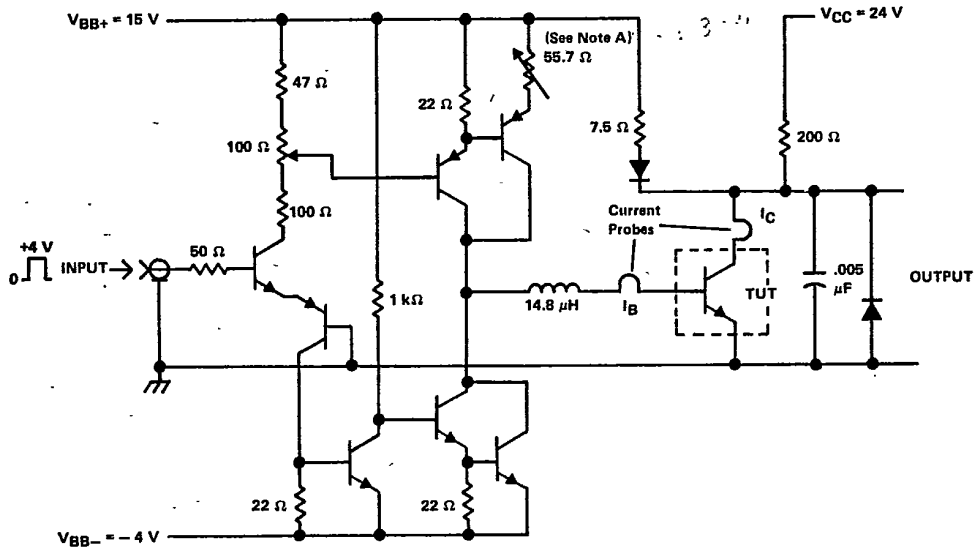
8961726 TEXAS INSTR (OPTO)

62C 36637 D

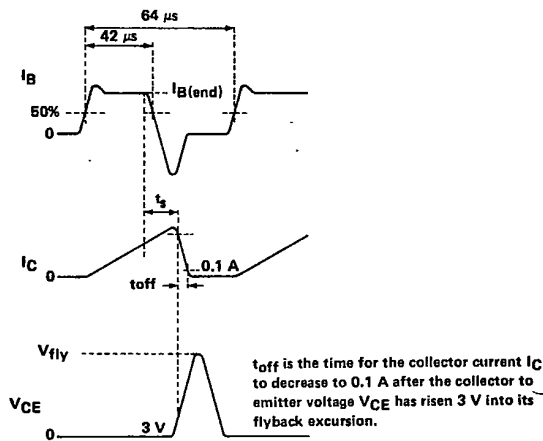
BU406, BU407
N-P-N SILICON POWER TRANSISTORS

T-33-11

PARAMETER MEASUREMENT INFORMATION



TEST CIRCUIT



VOLTAGE AND CURRENT WAVEFORMS

NOTE A: Adjust resistance to set $I_{B(end)}$.

FIGURE 1. RESISTIVE-LOAD SWITCHING



BD, BDW, BDX, BU, BUX, BUY Devices

8961726 TEXAS INSTR (OPTO)

62C 36638 D

BU406, BU407
N-P-N SILICON POWER TRANSISTORS

T-33-11

TYPICAL CHARACTERISTICS

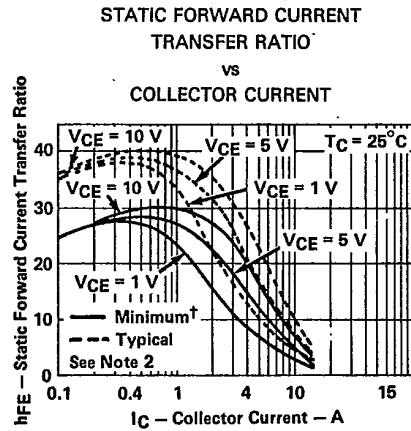


FIGURE 2

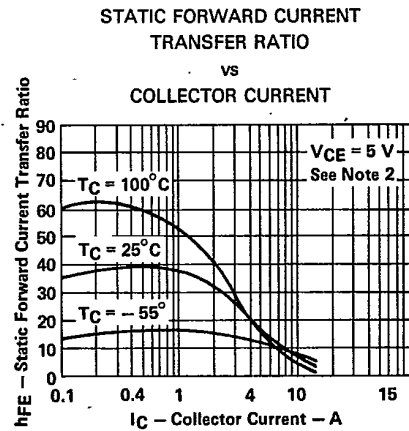


FIGURE 3

COLLECTOR-EMITTER SATURATION VOLTAGE
vs
CASE TEMPERATURE

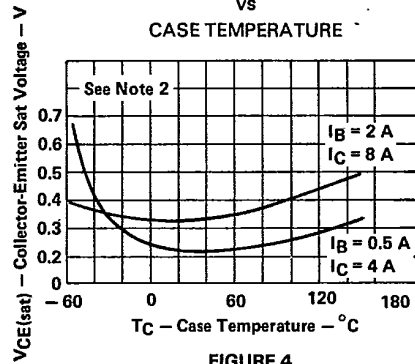


FIGURE 4

† This value is given with 95% confidence.
NOTE 2: These parameters must be measured using pulse techniques, $t_w \leq 300 \mu s$, duty cycle $\leq 2\%$.



BD, BDW, BDX, BU, BUX, BUY Devices

8961726 TEXAS INSTR (OPTO)

62C 36639 D

BU406, BU407
N-P-N SILICON POWER TRANSISTORS

T-33-11

MAXIMUM SAFE OPERATING AREA
FORWARD-BIAS SAFE OPERATING AREA

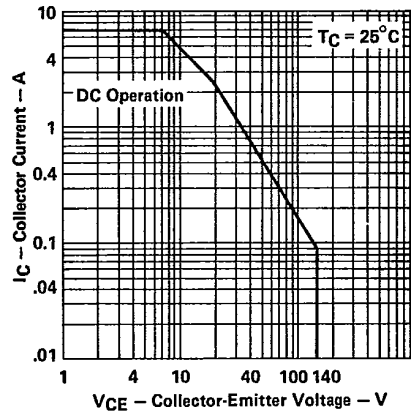


FIGURE 5



BD, BDW, BDX, BU, BUX, BUY Devices