

LM317M

3-TERMINAL ADJUSTABLE REGULATOR

SLVS297H – APRIL 2000 – REVISED FEBRUARY 2003

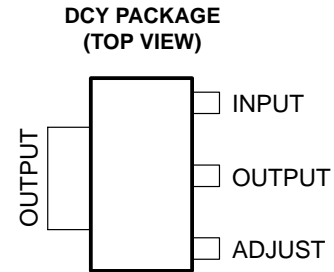
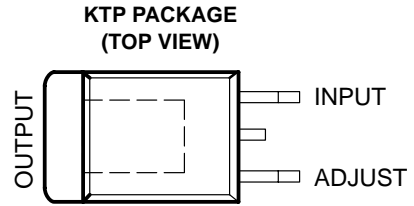
- Output Voltage Range Adjustable From 1.25 V to 37 V
- Output Current Greater Than 500 mA
- Internal Short-Circuit Current Limiting
- Thermal Overload Protection
- Output Safe-Area Compensation

description/ordering information

The LM317M is an adjustable 3-terminal positive-voltage regulator capable of supplying more than 500 mA over an output-voltage range of 1.25 V to 37 V. The LM317M is exceptionally easy to use and requires only two external resistors to set the output voltage. Furthermore, both line and load regulation are better than standard fixed regulators.

In addition to having higher performance than fixed regulators, the device includes on-chip current limiting, thermal overload protection, and safe-operating-area protection. All overload protection remains fully functional if the ADJUST terminal is disconnected.

Normally, no capacitors are needed unless the device is more than 6 inches from the input filter capacitors, in which case an input bypass capacitor is needed. An optional output capacitor can be added to improve transient response. The ADJUST terminal can be bypassed to achieve very high ripple-rejection ratios, which are difficult to achieve with standard three-terminal regulators.



ORDERING INFORMATION

T _J	PACKAGE†		ORDERABLE PART NUMBER	TOP-SIDE MARKING
0°C to 125°C	POWER-FLEX (KTP)	Reel of 2000	LM317MKTTPR	LM317M
	SOT (DCY)	Tube of 80	LM317MDCY	L4
		Reel of 2500	LM317MDCYR	

† Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

 **TEXAS
INSTRUMENTS**

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absolute maximum ratings over operating temperature range (unless otherwise noted)†

Input-to-output differential voltage, $V_I - V_O$	40 V
Package thermal impedance, θ_{JA} (see Notes 1 and 2): DCY package	53°C/W
(see Notes 1 and 3): KTP package	28°C/W
Operating virtual junction temperature, T_J	150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	28°C
Storage temperature range, T_{stg}	-65°C to 150°C

† Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. Maximum power dissipation is a function of $T_{J(max)}$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_{J(max)} - T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability.
2. The package thermal impedance is calculated in accordance with JESD 51-7.
3. The package thermal impedance is calculated in accordance with JESD 51-5.

recommended operating conditions

	MIN	MAX	UNIT
$V_I - V_O$ Input-to-output voltage differential		37	V
I_O Output current		0.5	A
T_J Operating virtual junction temperature	0	125	°C

electrical characteristics over recommended operating virtual-junction temperature range, $V_I - V_O = 5$ V, $I_O = 0.1$ A (unless otherwise noted)

PARAMETER	TEST CONDITIONS‡		MIN	TYP	MAX	UNIT
Line regulation§	$V_I - V_O = 3$ V to 40 V	$T_J = 25^\circ\text{C}$		0.01	0.04	%V
		Full temperature range		0.02	0.07	
Load regulation	$I_O = 10$ mA to 500 mA	$T_J = 25^\circ\text{C}$		0.1	0.5	% V_O
		Full temperature range		0.3	1.5	
ADJUST terminal current				50	100	μA
Change in ADJUST terminal current	$V_I - V_O = 3$ V to 40 V,	$I_O = 10$ mA to 500 mA		0.2	5	μA
Reference voltage	$V_I - V_O = 3$ V to 40 V,	$I_O = 10$ mA to 500 mA	1.2	1.25	1.3	V
Output-voltage temperature stability				0.7%		
Minimum load current to maintain regulation				3.5	10	mA
Maximum output current	$V_I - V_O \leq 15$ V		500	900		mA
	$V_I - V_O = 40$ V, $P_D \leq P_{D(max)}$	$T_J = 25^\circ\text{C}$	150	250		
RMS output noise voltage (% of V_O)	$f = 10$ Hz to 10 kHz,	$T_J = 25^\circ\text{C}$		0.003		% V_O
Ripple rejection	$V_O = 10$ V, $f = 120$ Hz, $T_J = 25^\circ\text{C}$	$C_{ADJ} = 0$ ¶		65		dB
		$C_{ADJ} = 10$ μF ¶	66	80		
Long-term stability	$T_J = 125^\circ\text{C}$			0.3	1	%/ 1k Hrs

‡ Pulse-testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible.

§ Line voltage regulation is expressed here as the percentage change in output voltage per 1-V change at the input.

¶ C_{ADJ} is connected between the ADJUST terminal and ground.



TYPICAL CHARACTERISTICS

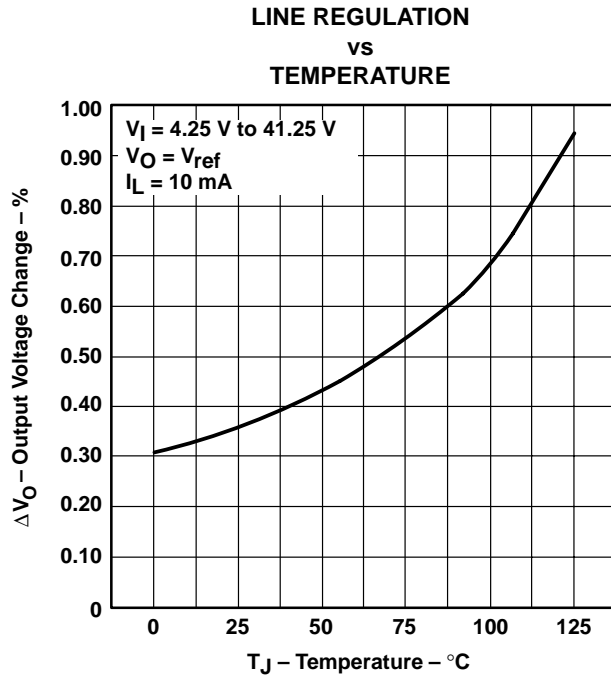


Figure 1

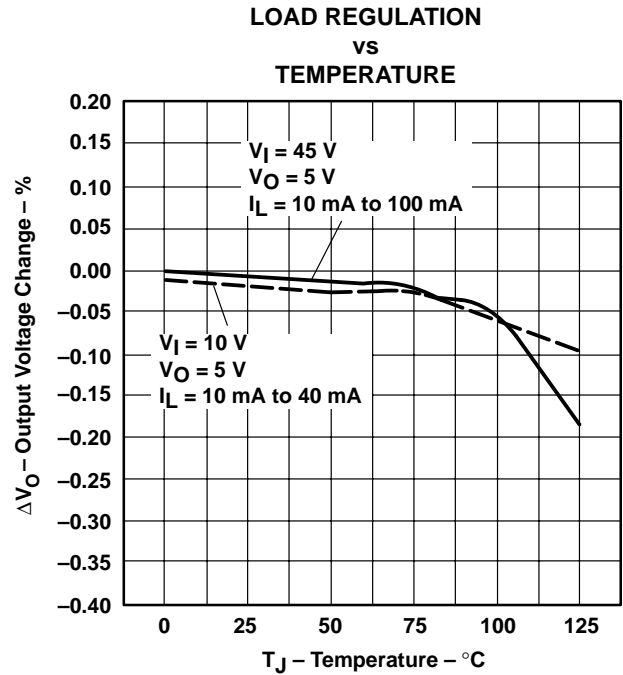


Figure 2

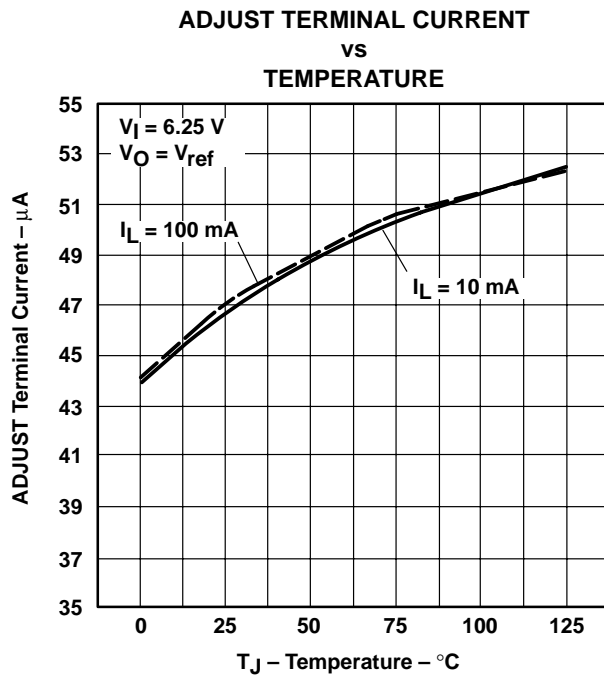


Figure 3

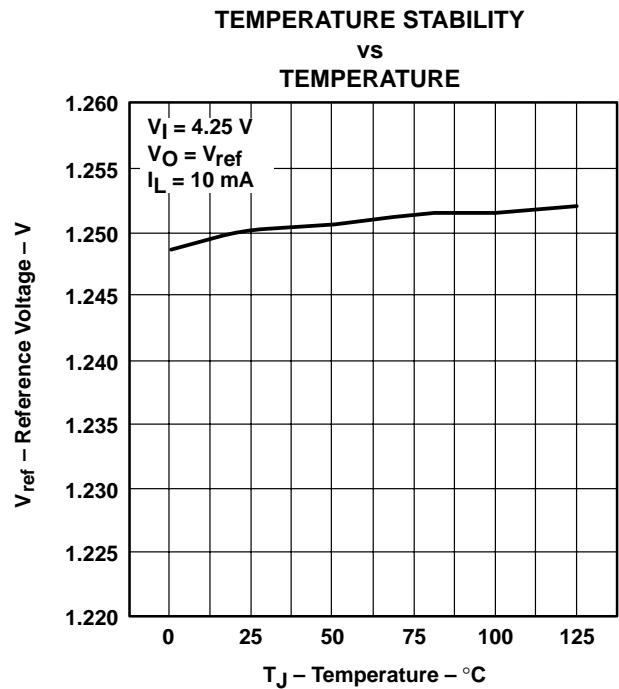


Figure 4

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TYPICAL CHARACTERISTICS

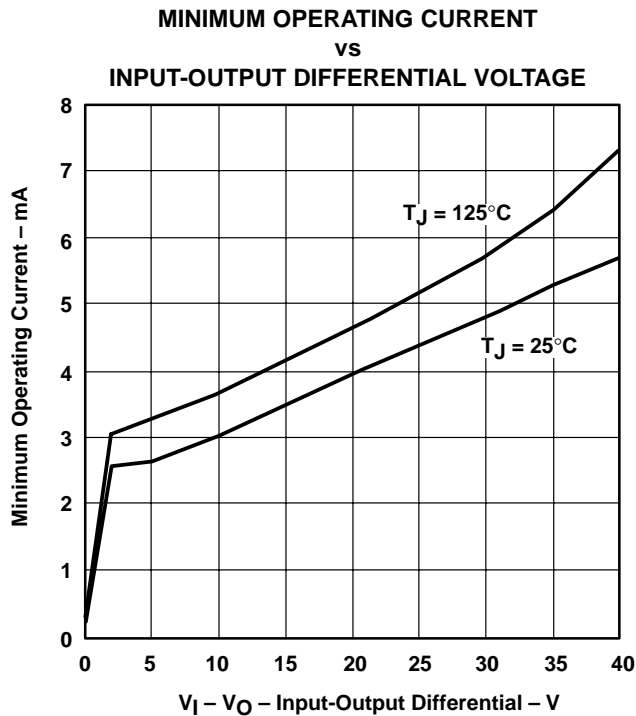


Figure 5

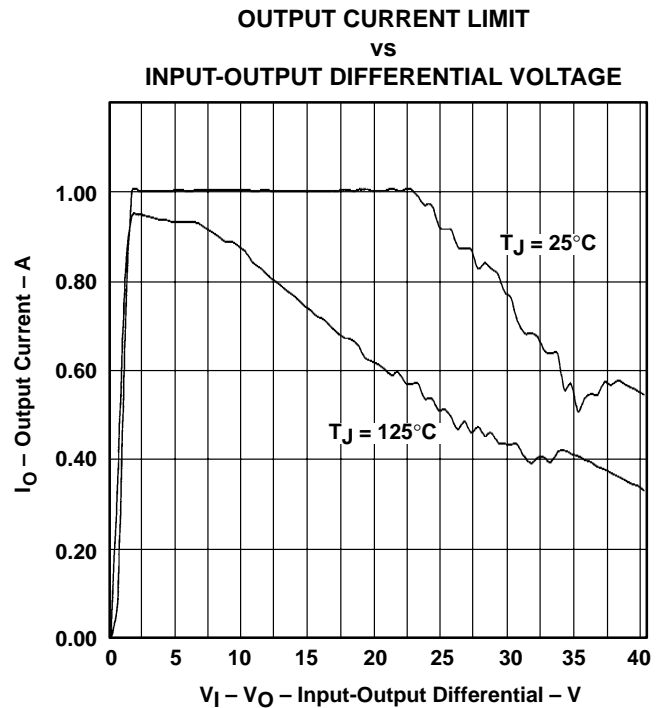


Figure 6

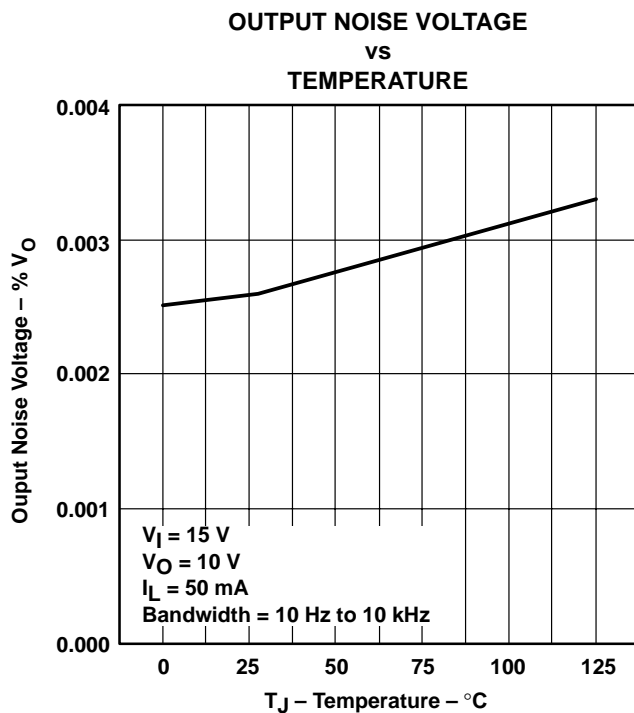


Figure 7

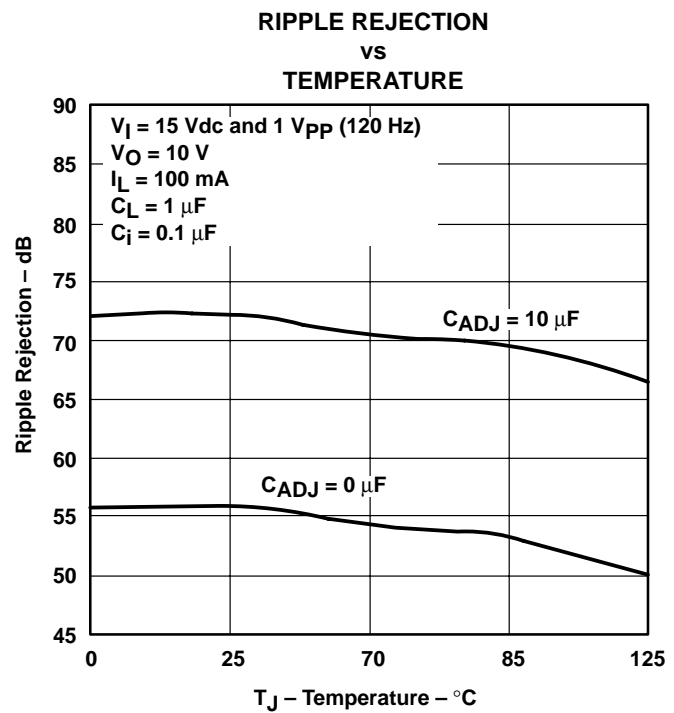
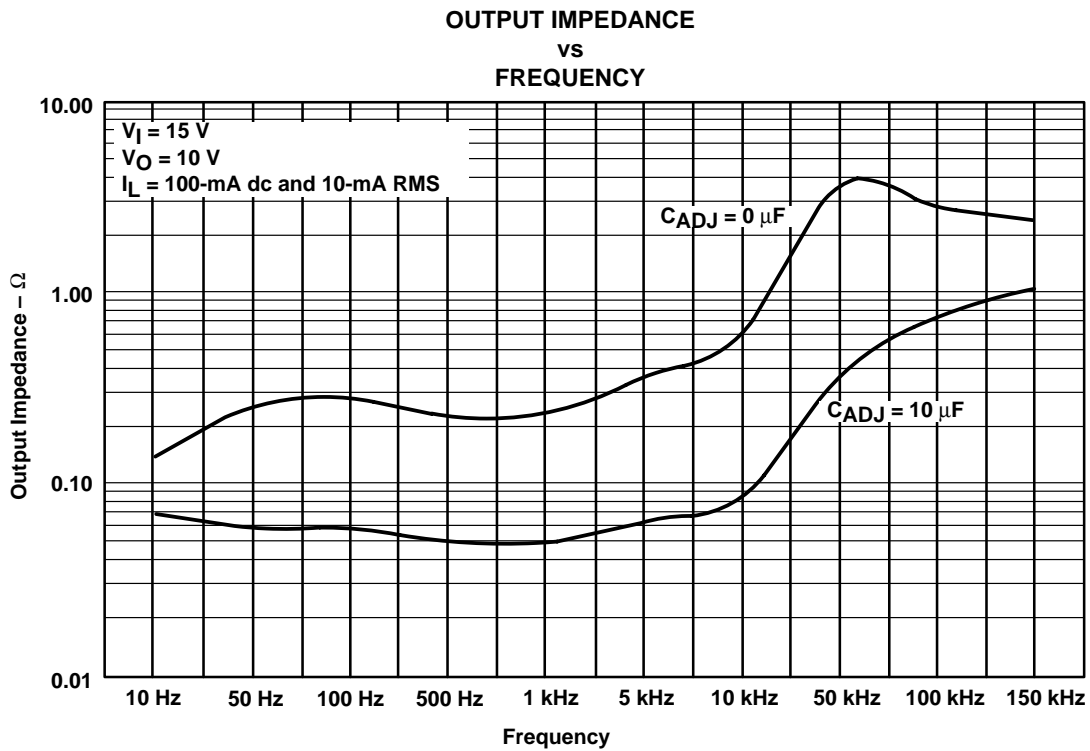
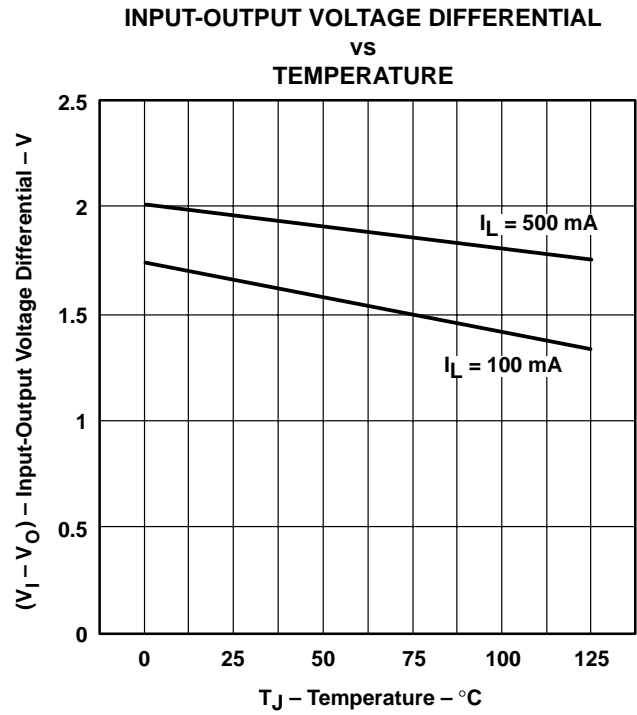
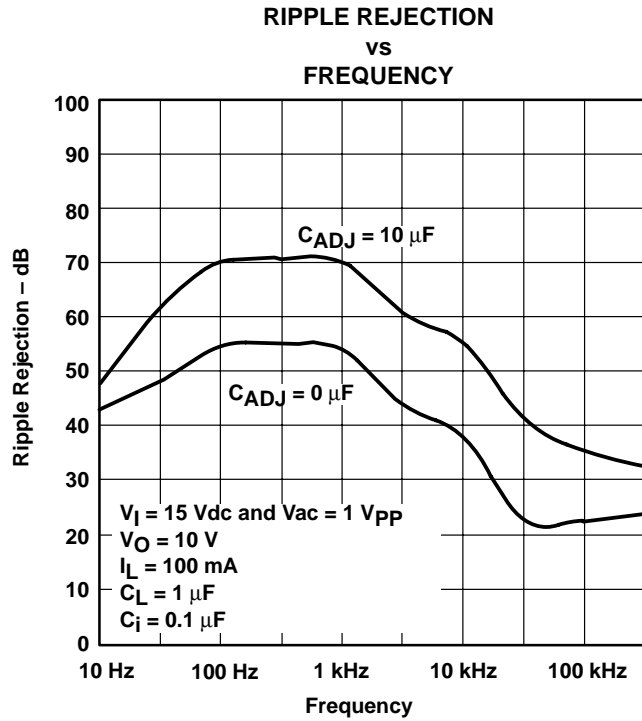


Figure 8



TYPICAL CHARACTERISTICS



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TYPICAL CHARACTERISTICS

LINE TRANSIENT RESPONSE vs TIME

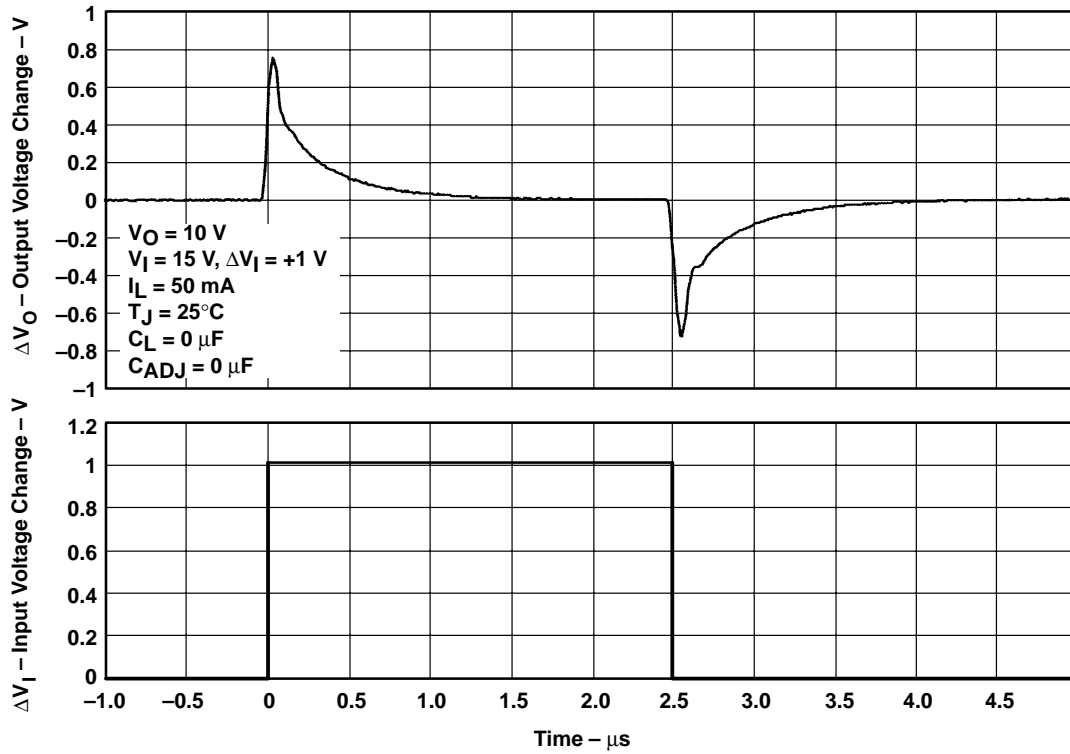


Figure 12

TYPICAL CHARACTERISTICS

LOAD TRANSIENT RESPONSE
vs
TIME

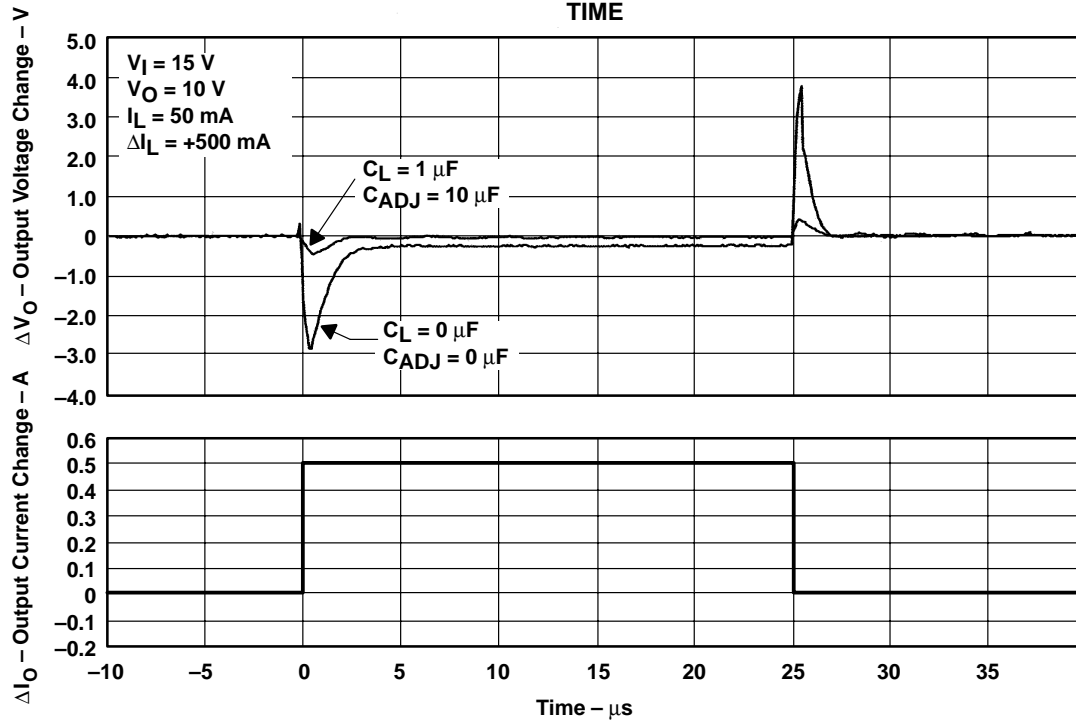


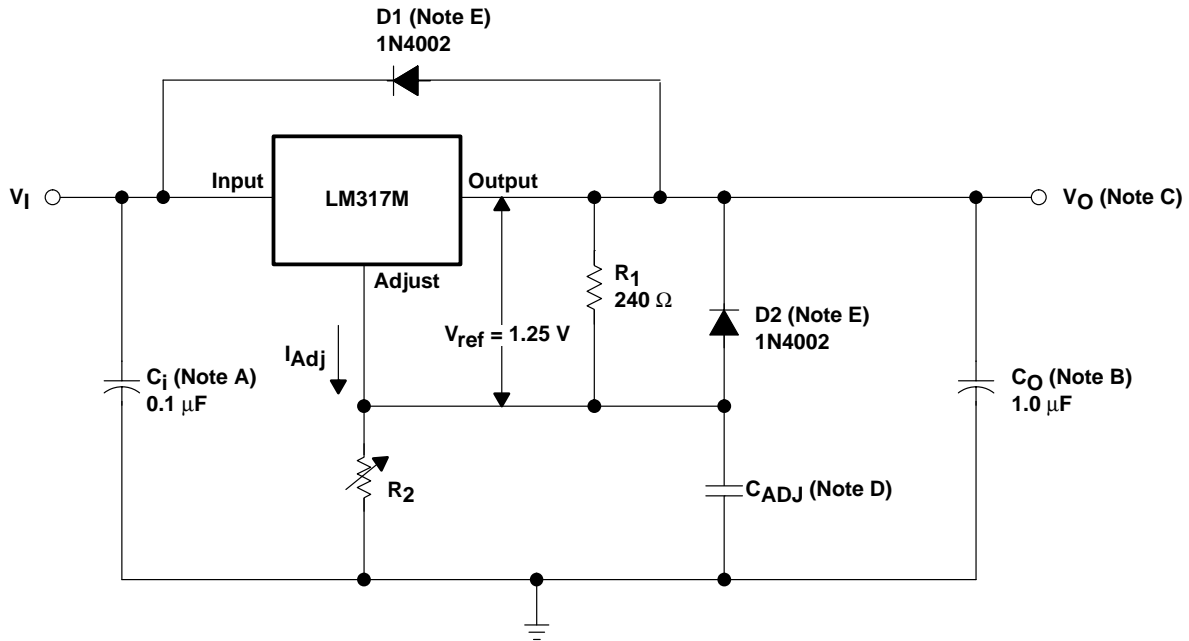
Figure 13

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APPLICATION INFORMATION



NOTES: A. C_i is not required, but is recommended, particularly if the regulator is not in close proximity to the power-supply filter capacitors. A 0.1- μ F disc or 1- μ F tantalum provides sufficient bypassing for most applications, especially when adjustment and output capacitors are used.

B. C_O improves transient response, but is not needed for stability.

C. V_O is calculated as shown:

$$V_O = V_{ref} \left(1 + \frac{R_2}{R_1} \right) + (I_{Adj} \times R_2)$$

Because I_{Adj} typically is 50 μ A, it is negligible in most applications.

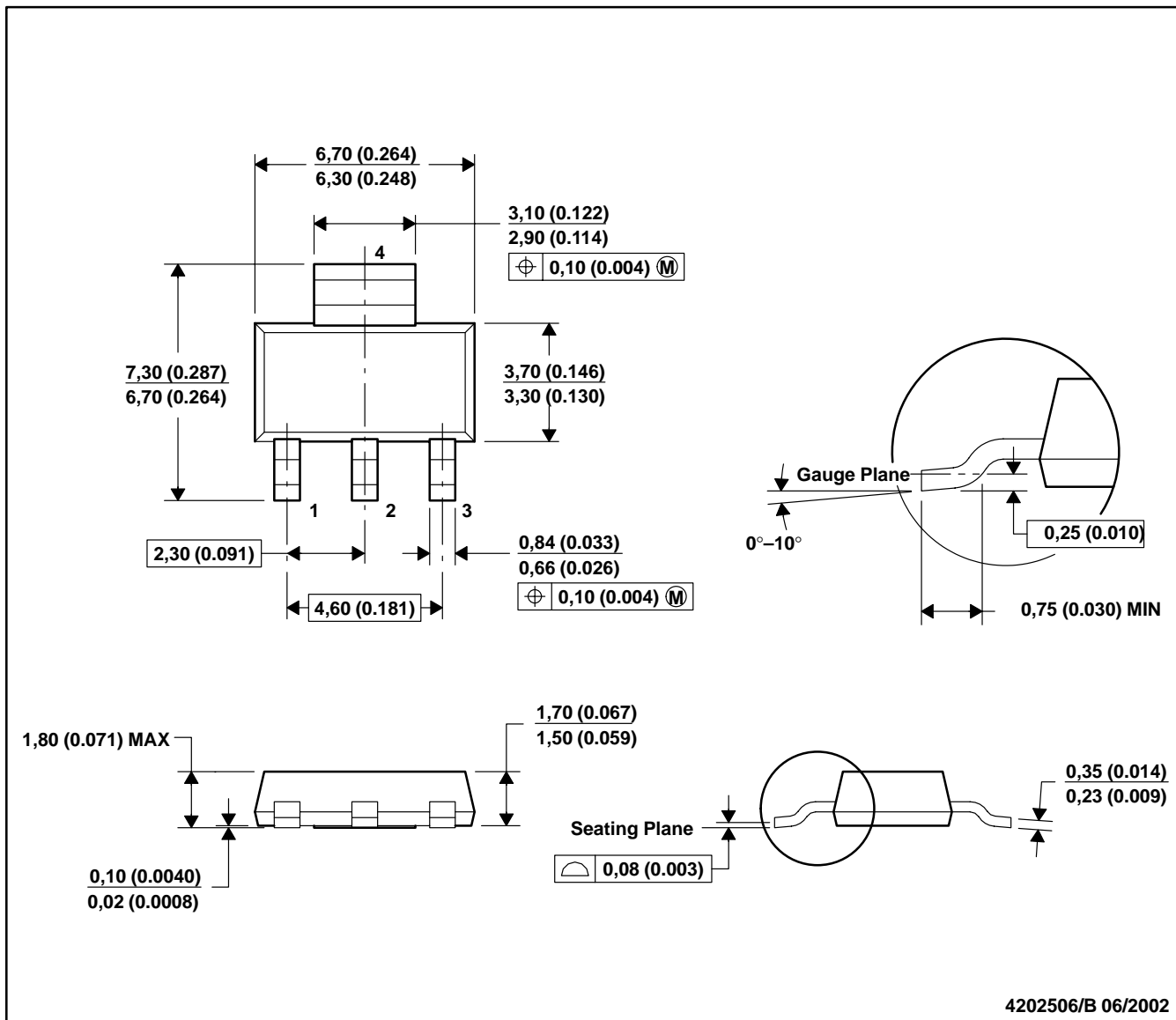
D. C_{ADJ} is used to improve ripple rejection; it prevents amplification of the ripple as the output voltage is adjusted higher. If C_{ADJ} is used, it is best to include protection diodes.

E. If the input is shorted to ground during a fault condition, protection diodes provide measures to prevent the possibility of external capacitors discharging through low-impedance paths in the IC. By providing low-impedance discharge paths for C_O and C_{ADJ} , respectively, D1 and D2 prevent the capacitors from discharging into the output of the regulator.

Figure 14. Adjustable Voltage Regulator

DCY (R-PDSO-G4)

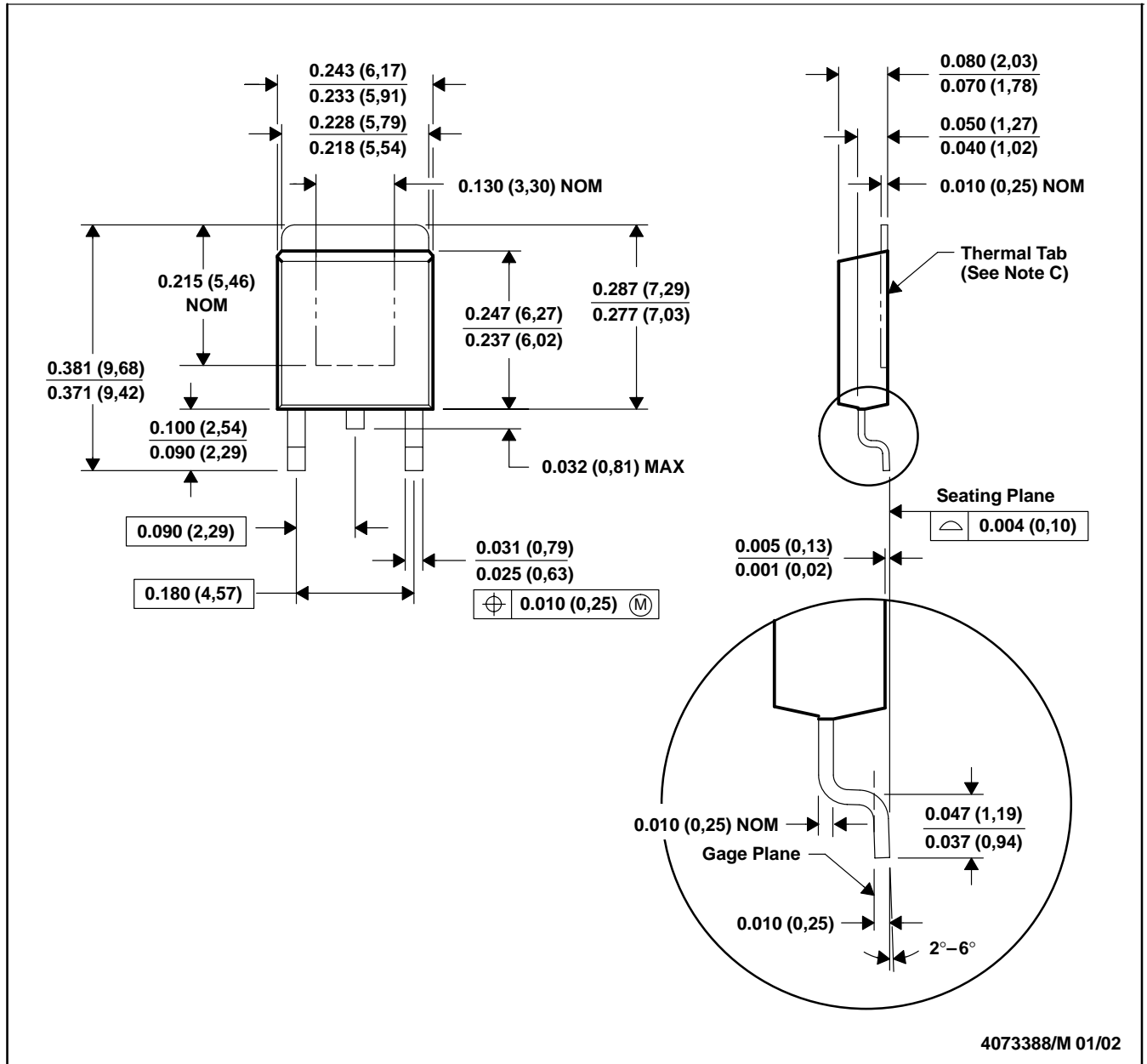
PLASTIC SMALL-OUTLINE



- NOTES: A. All linear dimensions are in millimeters (inches).
 B. This drawing is subject to change without notice.
 C. Body dimensions do not include mold flash or protrusion.
 D. Falls within JEDEC TO-261 Variation AA.

KTP (R-PSFM-G2)

PowerFLEX™ PLASTIC FLANGE-MOUNT PACKAGE



- NOTES: A. All linear dimensions are in inches (millimeters).
 B. This drawing is subject to change without notice.
 C. The center lead is in electrical contact with the thermal tab.
 D. Dimensions do not include mold protrusions, not to exceed 0.006 (0,15).
 E. Falls within JEDEC TO-252 variation AC.

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