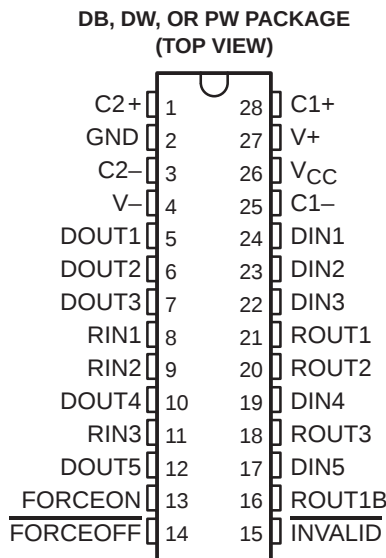


# MAX3238 3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVER/RECEIVER

SLLS349B – JUNE 1999 – REVISED JANUARY 2000

- Meets or Exceeds the Requirements of TIA/EIA-232-F and ITU v.28 Standards
- Operates With 3-V to 5.5-V  $V_{CC}$  Supply
- Always-Active Noninverting Receiver Output (ROUT1B)
- Operates up to 250 kbit/s
- Low Standby Current . . . 1  $\mu$ A Typical
- External Capacitors . . .  $4 \times 0.1 \mu$ F
- Accepts 5-V Logic Input With 3.3-V Supply
- Designed to Be Interchangeable With Maxim MAX3238
- RS-232 Bus-Pin ESD Protection Exceeds  $\pm 15$ -kV Using Human-Body Model (HBM)
- Applications
  - Battery-Powered Systems, PDAs, Notebooks, Subnotebooks, Laptops, Palmtop PCs, Hand-Held Equipment, Modems, and Printers
- Package Options Include Plastic Small-Outline (DW), Shrink Small-Outline (DB), and Thin Shrink Small-Outline (PW) Packages



## description

The MAX3238 device consists of five line drivers, three line receivers, and a dual charge-pump circuit with  $\pm 15$ -kV ESD protection pin to pin (serial-port connection pins, including GND). The device meets the requirements of TIA/EIA-232-F and provides the electrical interface between notebook and subnotebook computer applications. The charge pump and four small external capacitors allow operation from a single 3-V to 5.5-V supply. In addition, the device includes an always-active noninverting output (ROUT1B), which allows applications using the ring indicator to transmit data while the device is powered down. These devices operate at data signaling rates up to 250 kbit/s, and at a maximum of 30-V/ $\mu$ s driver output slew rate.

Flexible control options for power management are featured when the serial port and driver inputs are inactive. The auto-powerdown plus feature functions when FORCEON is low and  $\overline{\text{FORCEOFF}}$  is high. During this mode of operation, if the device does not sense valid signal transitions on all receiver and driver inputs for 30 s, the built-in charge-pump and drivers are powered down, reducing the supply current to 1  $\mu$ A. By disconnecting the serial port or placing the peripheral drivers off, auto-powerdown plus occurs if there is no activity in the logic levels for the driver inputs. Auto-powerdown plus can be disabled when FORCEON and  $\overline{\text{FORCEOFF}}$  are high. With auto-powerdown plus enabled, the device automatically activates once a valid signal is applied to any receiver or driver input.  $\overline{\text{INVALID}}$  is high (valid data) if any receiver input voltage is greater than 2.7 V or less than -2.7 V, or has been between -0.3 V and 0.3 V for less than 30  $\mu$ s.  $\overline{\text{INVALID}}$  is low (invalid data) if all receiver input voltages are between -0.3 V and 0.3 V for more than 30  $\mu$ s. Refer to Figure 5 for receiver input levels.

The MAX3238C is characterized for operation from 0°C to 70°C. The MAX3238I is characterized for operation from -40°C to 85°C.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCT PREVIEW information concerns products in the formative or design phase of development. Characteristic data and other specifications are design goals. Texas Instruments reserves the right to change or discontinue these products without notice.

**TEXAS  
INSTRUMENTS**

POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

Copyright © 2000, Texas Instruments Incorporated

PRODUCT PREVIEW

# MAX3238

## 3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVER/RECEIVER

SLLS349B – JUNE 1999 – REVISED JANUARY 2000

### AVAILABLE OPTIONS

| T <sub>A</sub> | PACKAGED DEVICES                |                       |                                      |
|----------------|---------------------------------|-----------------------|--------------------------------------|
|                | SHRINK<br>SMALL OUTLINE<br>(DB) | SMALL OUTLINE<br>(DW) | THIN SHRINK<br>SMALL OUTLINE<br>(PW) |
| 0°C to 70°C    | MAX3238CDB                      | MAX3238CDW            | MAX3238CPW                           |
| –40°C to 85°C  | MAX3238IDB                      | MAX3238IDW            | MAX3238IPW                           |

The DB, DW, and PW packages are available taped and reeled. Add the suffix R to device type (e.g., MAX3238CDBR).

### Function Tables

#### EACH DRIVER

| INPUTS |         |          |                                                  | OUTPUT<br>DOUT | DRIVER STATUS                                         |
|--------|---------|----------|--------------------------------------------------|----------------|-------------------------------------------------------|
| DIN    | FORCEON | FORCEOFF | TIME ELAPSED SINCE LAST<br>RIN OR DIN TRANSITION |                |                                                       |
| X      | X       | L        | X                                                | Z              | Powered off                                           |
| L      | H       | H        | X                                                | H              | Normal operation with<br>auto-powerdown plus disabled |
| H      | H       | H        | X                                                | L              |                                                       |
| L      | L       | H        | <30 s                                            | H              | Normal operation with<br>auto-powerdown plus enabled  |
| H      | L       | H        | <30 s                                            | L              |                                                       |
| L      | L       | H        | >30 s                                            | Z              | Powered off by<br>auto-powerdown plus feature         |
| H      | L       | H        | >30 s                                            | Z              |                                                       |

H = high level, L = low level, X = irrelevant, Z = high impedance

#### EACH RECEIVER

| INPUTS |                    |          |                                                  | OUTPUTS |      | RECEIVER STATUS                                                  |
|--------|--------------------|----------|--------------------------------------------------|---------|------|------------------------------------------------------------------|
| RIN2   | RIN1,<br>RIN3–RIN5 | FORCEOFF | TIME ELAPSED SINCE LAST RIN<br>OR DIN TRANSITION | ROUT1B  | ROUT |                                                                  |
| L      | X                  | L        | X                                                | L       | Z    | Powered off while<br>ROUT1B is active                            |
| H      | X                  | L        | X                                                | H       | Z    |                                                                  |
| L      | L                  | H        | <30 s                                            | L       | H    | Normal operation with<br>auto-powerdown plus<br>disabled/enabled |
| L      | H                  | H        | <30 s                                            | L       | L    |                                                                  |
| H      | L                  | H        | <30 s                                            | H       | H    |                                                                  |
| H      | H                  | H        | <30 s                                            | H       | L    |                                                                  |
| Open   | Open               | H        | >30 s                                            | L       | H    |                                                                  |

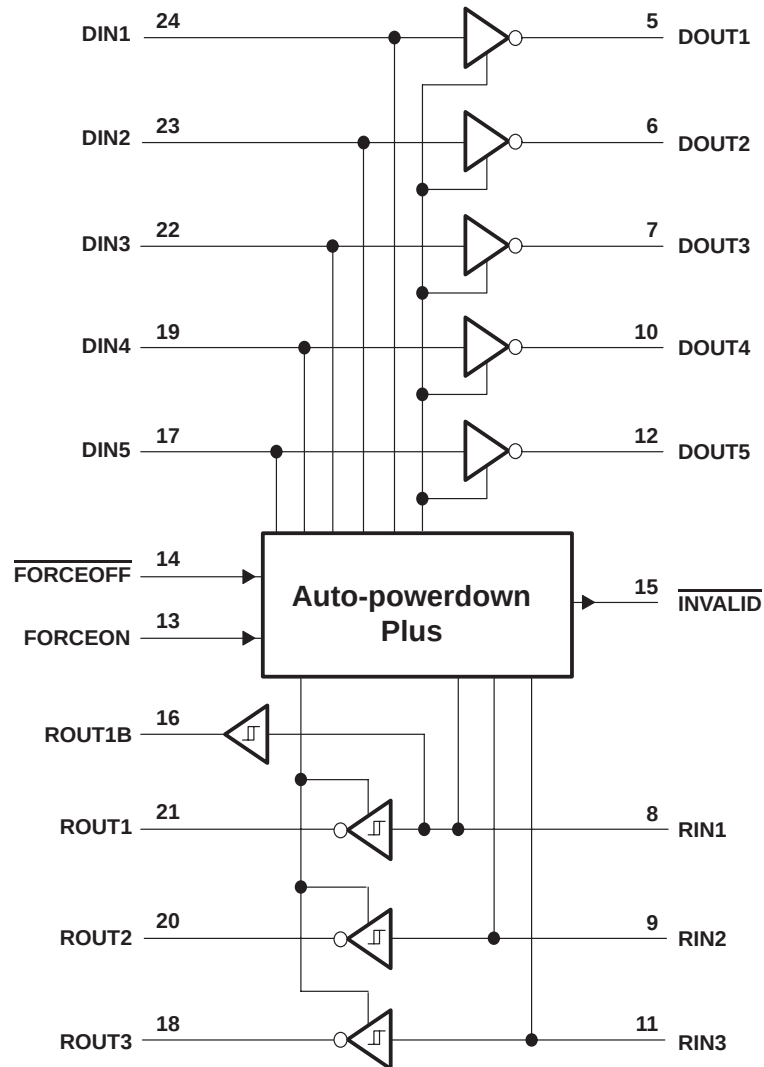
H = high level, L = low level, X = irrelevant, Z = high impedance (off), Open = input disconnected or connected driver off

# MAX3238

## 3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVER/RECEIVER

SLLS349B – JUNE 1999 – REVISED JANUARY 2000

logic diagram (positive logic)



PRODUCT PREVIEW



POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

# MAX3238

## 3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVER/RECEIVER

SLLS349B – JUNE 1999 – REVISED JANUARY 2000

### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

|                                                                                         |                            |
|-----------------------------------------------------------------------------------------|----------------------------|
| Supply voltage range, $V_{CC}$ (see Note 1)                                             | –0.3 V to 6 V              |
| Positive output supply voltage range, $V_+$ (see Note 1)                                | –0.3 V to 7 V              |
| Negative output supply voltage range, $V_-$ (see Note 1)                                | 0.3 V to –7 V              |
| Supply voltage difference, $V_+ - V_-$ (see Note 1)                                     | 13 V                       |
| Input voltage range, $V_I$ : Driver ( $\overline{\text{FORCEOFF}}$ , $\text{FORCEON}$ ) | –0.3 V to 6 V              |
| Receiver                                                                                | –25 V to 25 V              |
| Output voltage range, $V_O$ : Driver                                                    | –13.2 V to 13.2 V          |
| Receiver ( $\overline{\text{INVALID}}$ )                                                | –0.3 V to $V_{CC} + 0.3$ V |
| Package thermal impedance, $\theta_{JA}$ (see Note 2): DB package                       | 62°C/W                     |
| DW package                                                                              | 46°C/W                     |
| PW package                                                                              | 62°C/W                     |
| Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds                            | 260°C                      |
| Storage temperature range, $T_{stg}$                                                    | –65°C to 150°C             |

<sup>†</sup> Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltages are with respect to network GND.  
2. The package thermal impedance is calculated in accordance with JESD 51.

### recommended operating conditions (see Note 3 and Figure 6)

|                                                      |                                                          |                  | MIN | NOM | MAX | UNIT |
|------------------------------------------------------|----------------------------------------------------------|------------------|-----|-----|-----|------|
| Supply voltage                                       |                                                          | $V_{CC} = 3.3$ V | 3   | 3.3 | 3.6 | V    |
|                                                      |                                                          | $V_{CC} = 5$ V   | 4.5 | 5   | 5.5 |      |
| $V_{IH}$ Driver and control high-level input voltage | $\text{DIN}, \overline{\text{FORCEOFF}}, \text{FORCEON}$ | $V_{CC} = 3.3$ V | 2   |     |     | V    |
|                                                      |                                                          | $V_{CC} = 5$ V   | 2.4 |     |     |      |
| $V_{IL}$ Driver and control low-level input voltage  | $\text{DIN}, \overline{\text{FORCEOFF}}, \text{FORCEON}$ |                  |     |     | 0.8 | V    |
| $V_I$ Driver and control input voltage               | $\text{DIN}, \overline{\text{FORCEOFF}}, \text{FORCEON}$ |                  | 0   |     | 5.5 | V    |
| $V_I$ Receiver input voltage                         |                                                          |                  | –25 |     | 25  | V    |
| $T_A$ Operating free-air temperature                 |                                                          | MAX3238C         | 0   |     | 70  | °C   |
|                                                      |                                                          | MAX3238I         | –40 |     | 85  |      |

NOTE 3: Testing supply conditions are C1–C4 = 0.1  $\mu$ F at  $V_{CC} = 3.3$  V  $\pm$  0.15 V; C1–C4 = 0.22  $\mu$ F at  $V_{CC} = 3.3$  V  $\pm$  0.3 V; and C1 = 0.047  $\mu$ F and C2–C4 = 0.33  $\mu$ F at  $V_{CC} = 5$  V  $\pm$  0.5 V.

### electrical characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Note 3 and Figure 6)

| PARAMETER |                       | TEST CONDITIONS                              | MIN | TYP <sup>‡</sup> | MAX     | UNIT    |
|-----------|-----------------------|----------------------------------------------|-----|------------------|---------|---------|
| $I_I$     | Input leakage current | $\overline{\text{FORCEOFF}}, \text{FORCEON}$ |     | $\pm 0.01$       | $\pm 1$ | $\mu$ A |
| $I_{CC}$  | Supply current        | Auto-powerdown plus disabled                 |     | 0.5              | 2       | $\mu$ A |
|           |                       | Powered off                                  |     | 1                | 10      |         |
|           |                       | Auto-powerdown plus enabled                  |     | 1                | 10      |         |

<sup>‡</sup> All typical values are at  $V_{CC} = 3.3$  V or  $V_{CC} = 5$  V, and  $T_A = 25^\circ\text{C}$ .

NOTE 3: Testing supply conditions are C1–C4 = 0.1  $\mu$ F at  $V_{CC} = 3.3$  V  $\pm$  0.15 V; C1–C4 = 0.22  $\mu$ F at  $V_{CC} = 3.3$  V  $\pm$  0.3 V; and C1 = 0.047  $\mu$ F and C2–C4 = 0.33  $\mu$ F at  $V_{CC} = 5$  V  $\pm$  0.5 V.

## DRIVER SECTION

**electrical characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Note 3 and Figure 6)**

| PARAMETER                                                 | TEST CONDITIONS                                                                    | MIN | TYP <sup>†</sup> | MAX  | UNIT |
|-----------------------------------------------------------|------------------------------------------------------------------------------------|-----|------------------|------|------|
| V <sub>OH</sub> High-level output voltage                 | All DOUT at R <sub>L</sub> = 3 kΩ to GND                                           | 5   | 5.4              |      | V    |
| V <sub>OL</sub> Low-level output voltage                  | All DOUT at R <sub>L</sub> = 3 kΩ to GND                                           | –5  | –5.4             |      | V    |
| I <sub>IH</sub> High-level input current                  | V <sub>I</sub> = V <sub>CC</sub>                                                   |     | ±0.01            | ±1   | μA   |
| I <sub>IL</sub> Low-level input current                   | V <sub>I</sub> at GND                                                              |     | ±0.01            | ±1   | μA   |
| I <sub>OS</sub> Short-circuit output current <sup>‡</sup> | V <sub>CC</sub> = 3.6 V, V <sub>O</sub> = 0 V                                      |     | ±35              | ±60  | mA   |
|                                                           | V <sub>CC</sub> = 5.5 V, V <sub>O</sub> = 0 V                                      |     | ±40              | ±100 |      |
| r <sub>O</sub> Output resistance                          | V <sub>CC</sub> , V <sub>+</sub> , and V <sub>–</sub> = 0 V, V <sub>O</sub> = ±2 V | 300 | 10M              |      | Ω    |
| I <sub>off</sub> Output leakage current                   | FORCEOFF = GND, V <sub>O</sub> = ±12 V, V <sub>CC</sub> = 0 to 5.5 V               |     |                  | ±25  | μA   |

<sup>†</sup> All typical values are at V<sub>CC</sub> = 3.3 V or V<sub>CC</sub> = 5 V, and T<sub>A</sub> = 25°C.

<sup>‡</sup> Short-circuit durations should be controlled to prevent exceeding the device absolute power dissipation ratings, and not more than one output should be shorted at a time.

NOTE 3: Testing supply conditions are C1–C4 = 0.1 μF at V<sub>CC</sub> = 3.3 V ± 0.15 V; C1–C4 = 0.22 μF at V<sub>CC</sub> = 3.3 V ± 0.3 V; and C1 = 0.047 μF and C2–C4 = 0.33 μF at V<sub>CC</sub> = 5 V ± 0.5 V.

**switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Note 3 and Figure 6)**

| PARAMETER                                          | TEST CONDITIONS                                                                   | MIN                                | TYP <sup>†</sup> | MAX | UNIT   |
|----------------------------------------------------|-----------------------------------------------------------------------------------|------------------------------------|------------------|-----|--------|
| Maximum data rate                                  | C <sub>L</sub> = 1000 pF, One DOUT switching, R <sub>L</sub> = 3 kΩ, See Figure 1 | 250                                |                  |     | kbit/s |
| t <sub>sk(p)</sub> Pulse skew <sup>§</sup>         | C <sub>L</sub> = 150 pF to 2500 pF, R <sub>L</sub> = 3 kΩ to 7 kΩ, See Figure 2   |                                    | 100              |     | ns     |
| SR(tr) Slew rate, transition region (see Figure 1) | V <sub>CC</sub> = 3.3 V, R <sub>L</sub> = 3 kΩ to 7 kΩ                            | C <sub>L</sub> = 150 pF to 1000 pF | 6                | 30  | V/μs   |
|                                                    |                                                                                   | C <sub>L</sub> = 150 pF to 2500 pF | 4                | 30  |        |

<sup>†</sup> All typical values are at V<sub>CC</sub> = 3.3 V or V<sub>CC</sub> = 5 V, and T<sub>A</sub> = 25°C.

<sup>§</sup> Pulse skew is defined as |t<sub>pLH</sub> – t<sub>pHL</sub>| of each channel of the same device.

NOTE 3: Testing supply conditions are C1–C4 = 0.1 μF at V<sub>CC</sub> = 3.3 V ± 0.15 V; C1–C4 = 0.22 μF at V<sub>CC</sub> = 3.3 V ± 0.3 V; and C1 = 0.047 μF and C2–C4 = 0.33 μF at V<sub>CC</sub> = 5 V ± 0.5 V.

# MAX3238

## 3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVER/RECEIVER

SLLS349B – JUNE 1999 – REVISED JANUARY 2000

### RECEIVER SECTION

electrical characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Note 3 and Figure 6)

| PARAMETER                                                                | TEST CONDITIONS                | MIN                     | TYP <sup>†</sup>        | MAX | UNIT |
|--------------------------------------------------------------------------|--------------------------------|-------------------------|-------------------------|-----|------|
| V <sub>OH</sub> High-level output voltage                                | I <sub>OH</sub> = -1 mA        | V <sub>CC</sub> - 0.6 V | V <sub>CC</sub> - 0.1 V |     | V    |
| V <sub>OL</sub> Low-level output voltage                                 | I <sub>OL</sub> = 1.6 mA       |                         |                         | 0.4 | V    |
| V <sub>IT+</sub> Positive-going input threshold voltage                  | V <sub>CC</sub> = 3.3 V        |                         | 1.5                     | 2.4 | V    |
|                                                                          | V <sub>CC</sub> = 5 V          |                         | 1.8                     | 2.4 |      |
| V <sub>IT-</sub> Negative-going input threshold voltage                  | V <sub>CC</sub> = 3.3 V        | 0.6                     | 1.2                     |     | V    |
|                                                                          | V <sub>CC</sub> = 5 V          | 0.8                     | 1.5                     |     |      |
| V <sub>hys</sub> Input hysteresis (V <sub>IT+</sub> - V <sub>IT-</sub> ) |                                |                         | 0.3                     |     | V    |
| I <sub>off</sub> Output leakage current (except ROUT1B)                  | FORCEOFF = 0 V                 |                         | ±0.05                   | ±10 | µA   |
| r <sub>i</sub> Input resistance                                          | V <sub>I</sub> = ±3 V to ±25 V | 3                       | 5                       | 7   | kΩ   |

<sup>†</sup> All typical values are at V<sub>CC</sub> = 3.3 V or V<sub>CC</sub> = 5 V, and T<sub>A</sub> = 25°C.

NOTE 3: Testing supply conditions are C1–C4 = 0.1 µF at V<sub>CC</sub> = 3.3 V ± 0.15 V; C1–C4 = 0.22 µF at V<sub>CC</sub> = 3.3 V ± 0.3 V; and C1 = 0.047 µF and C2–C4 = 0.33 µF at V<sub>CC</sub> = 5 V ± 0.5 V.

switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Note 3)

| PARAMETER                                                          | TEST CONDITIONS                                              | MIN | TYP <sup>†</sup> | MAX | UNIT |
|--------------------------------------------------------------------|--------------------------------------------------------------|-----|------------------|-----|------|
| t <sub>PLH</sub> Propagation delay time, low- to high-level output | C <sub>L</sub> = 150 pF, See Figure 3                        |     | 150              |     | ns   |
| t <sub>PHL</sub> Propagation delay time, high- to low-level output |                                                              |     | 150              |     | ns   |
| t <sub>en</sub> Output enable time                                 | C <sub>L</sub> = 150 pF, R <sub>L</sub> = 3 kΩ, See Figure 4 |     | 200              |     | ns   |
| t <sub>dis</sub> Output disable time                               |                                                              |     | 200              |     | ns   |
| t <sub>sk(p)</sub> Pulse skew <sup>‡</sup>                         | See Figure 3                                                 |     | 50               |     | ns   |

<sup>†</sup> All typical values are at V<sub>CC</sub> = 3.3 V or V<sub>CC</sub> = 5 V, and T<sub>A</sub> = 25°C.

<sup>‡</sup> Pulse skew is defined as |t<sub>PLH</sub> - t<sub>PHL</sub>| of each channel of the same device.

NOTE 3: Testing supply conditions are C1–C4 = 0.1 µF at V<sub>CC</sub> = 3.3 V ± 0.15 V; C1–C4 = 0.22 µF at V<sub>CC</sub> = 3.3 V ± 0.3 V; and C1 = 0.047 µF and C2–C4 = 0.33 µF at V<sub>CC</sub> = 5 V ± 0.5 V.

PRODUCT PREVIEW



POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

## AUTO-POWERDOWN PLUS SECTION

electrical characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 5)

| PARAMETER               | TEST CONDITIONS                                                                                                    | MIN                   | TYP <sup>†</sup> | MAX | UNIT |
|-------------------------|--------------------------------------------------------------------------------------------------------------------|-----------------------|------------------|-----|------|
| V <sub>T+(valid)</sub>  | Receiver input threshold for <u>INVALID</u> high-level output voltage<br>FORCEON = GND, FORCEOFF = V <sub>CC</sub> |                       |                  | 2.7 | V    |
| V <sub>T-(valid)</sub>  | Receiver input threshold for <u>INVALID</u> high-level output voltage<br>FORCEON = GND, FORCEOFF = V <sub>CC</sub> | -2.7                  |                  |     | V    |
| V <sub>T(invalid)</sub> | Receiver input threshold for <u>INVALID</u> low-level output voltage<br>FORCEON = GND, FORCEOFF = V <sub>CC</sub>  | -0.3                  |                  | 0.3 | V    |
| V <sub>OH</sub>         | <u>INVALID</u> high-level output voltage<br>I <sub>OH</sub> = -1 mA, FORCEON = GND, FORCEOFF = V <sub>CC</sub>     | V <sub>CC</sub> - 0.6 |                  |     | V    |
| V <sub>OL</sub>         | <u>INVALID</u> low-level output voltage<br>I <sub>OL</sub> = 1.6 mA, FORCEON = GND, FORCEOFF = V <sub>CC</sub>     |                       |                  | 0.4 | V    |

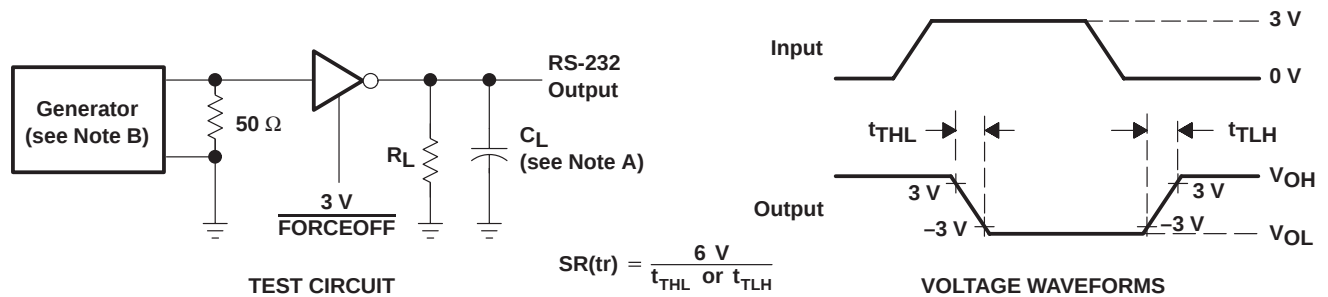
<sup>†</sup> All typical values are at V<sub>CC</sub> = 3.3 V or V<sub>CC</sub> = 5 V, and T<sub>A</sub> = 25°C.

switching characteristics over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 5)

| PARAMETER            | MIN | TYP <sup>†</sup> | MAX | UNIT |
|----------------------|-----|------------------|-----|------|
| t <sub>valid</sub>   |     | 0.1              |     | μs   |
| t <sub>invalid</sub> |     | 50               |     | μs   |
| t <sub>en</sub>      |     | 25               |     | μs   |
| t <sub>dis</sub>     | 15  | 30               | 60  | s    |

<sup>†</sup> All typical values are at V<sub>CC</sub> = 3.3 V or V<sub>CC</sub> = 5 V, and T<sub>A</sub> = 25°C.

## PARAMETER MEASUREMENT INFORMATION



NOTES: A. C<sub>L</sub> includes probe and jig capacitance.

B. The pulse generator has the following characteristics: PRR = 250 kbit/s, Z<sub>O</sub> = 50 Ω, 50% duty cycle, t<sub>r</sub> ≤ 10 ns, t<sub>f</sub> ≤ 10 ns.

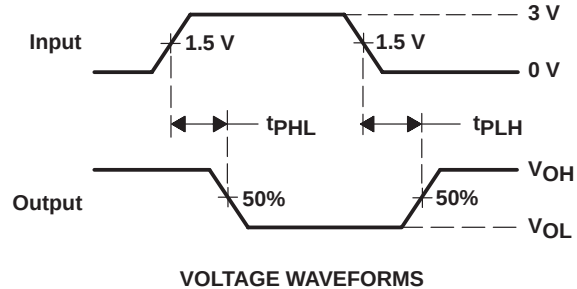
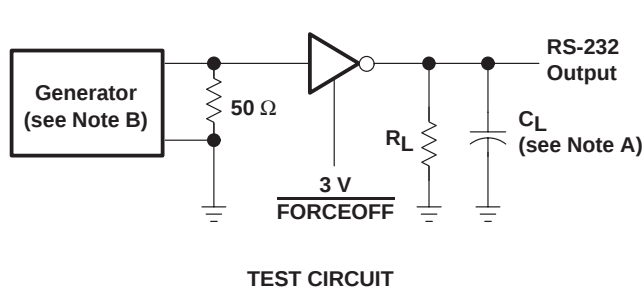
Figure 1. Driver Slew Rate

# MAX3238

## 3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVER/RECEIVER

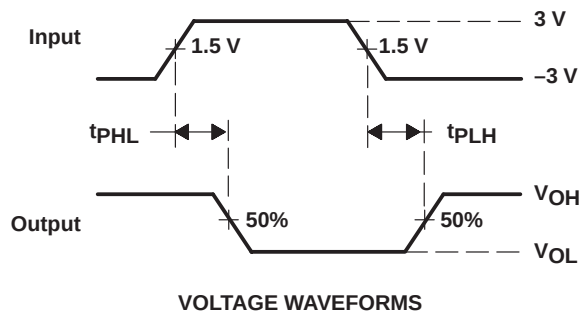
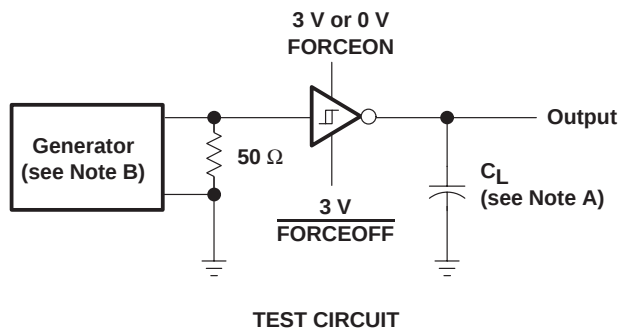
SLLS349B – JUNE 1999 – REVISED JANUARY 2000

### PARAMETER MEASUREMENT INFORMATION



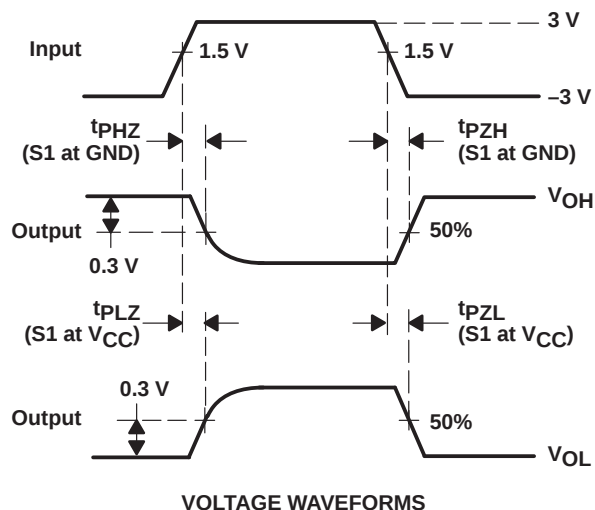
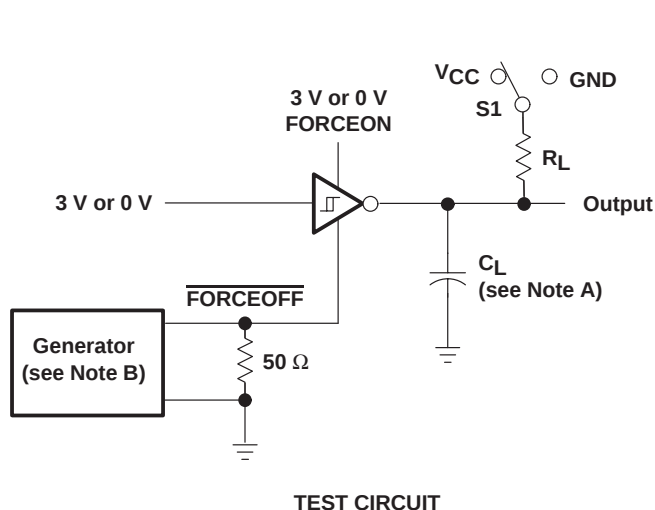
NOTES: A.  $C_L$  includes probe and jig capacitance.  
B. The pulse generator has the following characteristics: PRR = 250 kbit/s,  $Z_O = 50 \Omega$ , 50% duty cycle,  $t_r \leq 10$  ns,  $t_f \leq 10$  ns.

Figure 2. Driver Pulse Skew



NOTES: A.  $C_L$  includes probe and jig capacitance.  
B. The pulse generator has the following characteristics:  $Z_O = 50 \Omega$ , 50% duty cycle,  $t_r \leq 10$  ns,  $t_f \leq 10$  ns.

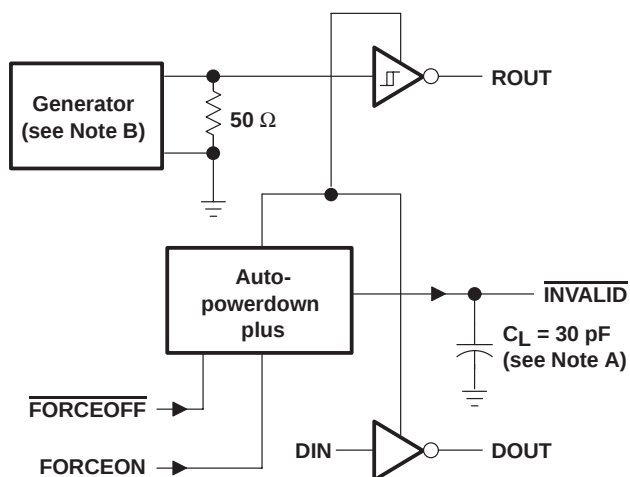
Figure 3. Receiver Propagation Delay Times



NOTES: A.  $C_L$  includes probe and jig capacitance.  
B. The pulse generator has the following characteristics:  $Z_O = 50 \Omega$ , 50% duty cycle,  $t_r \leq 10$  ns,  $t_f \leq 10$  ns.  
C.  $t_{PLZ}$  and  $t_{PHZ}$  are the same as  $t_{dis}$ .  
D.  $t_{PZL}$  and  $t_{PZH}$  are the same as  $t_{en}$ .

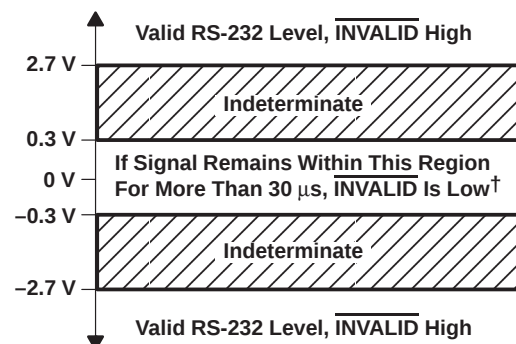
Figure 4. Receiver Enable and Disable Times

## PARAMETER MEASUREMENT INFORMATION

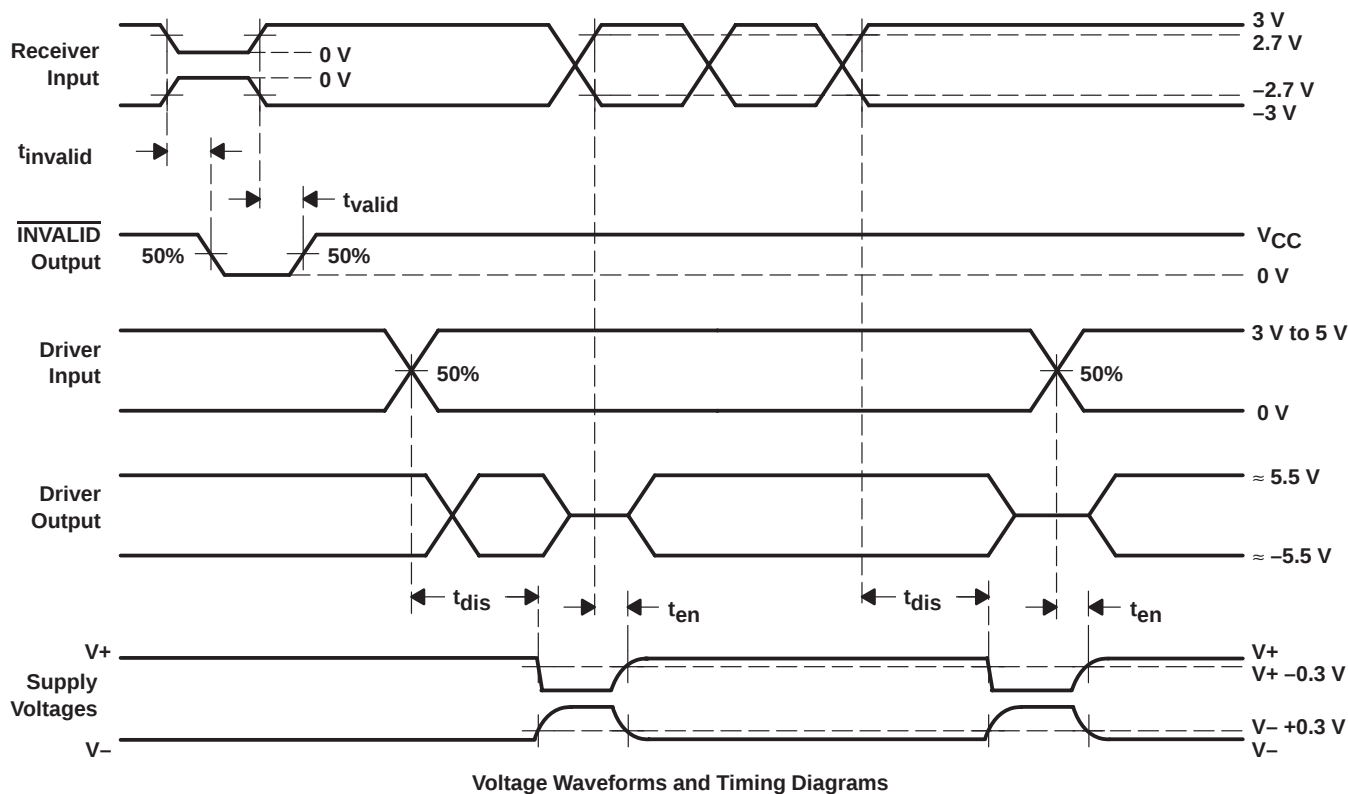


**TEST CIRCUIT**

- NOTES: A.  $C_L$  includes probe and jig capacitance.  
 B. The pulse generator has the following characteristics: PRR = 5 kbit/s,  $Z_O = 50 \Omega$ , 50% duty cycle,  $t_r \leq 10$  ns,  $t_f \leq 10$  ns.



† Auto-powerdown plus disables drivers and reduces supply current to 1  $\mu$ A.



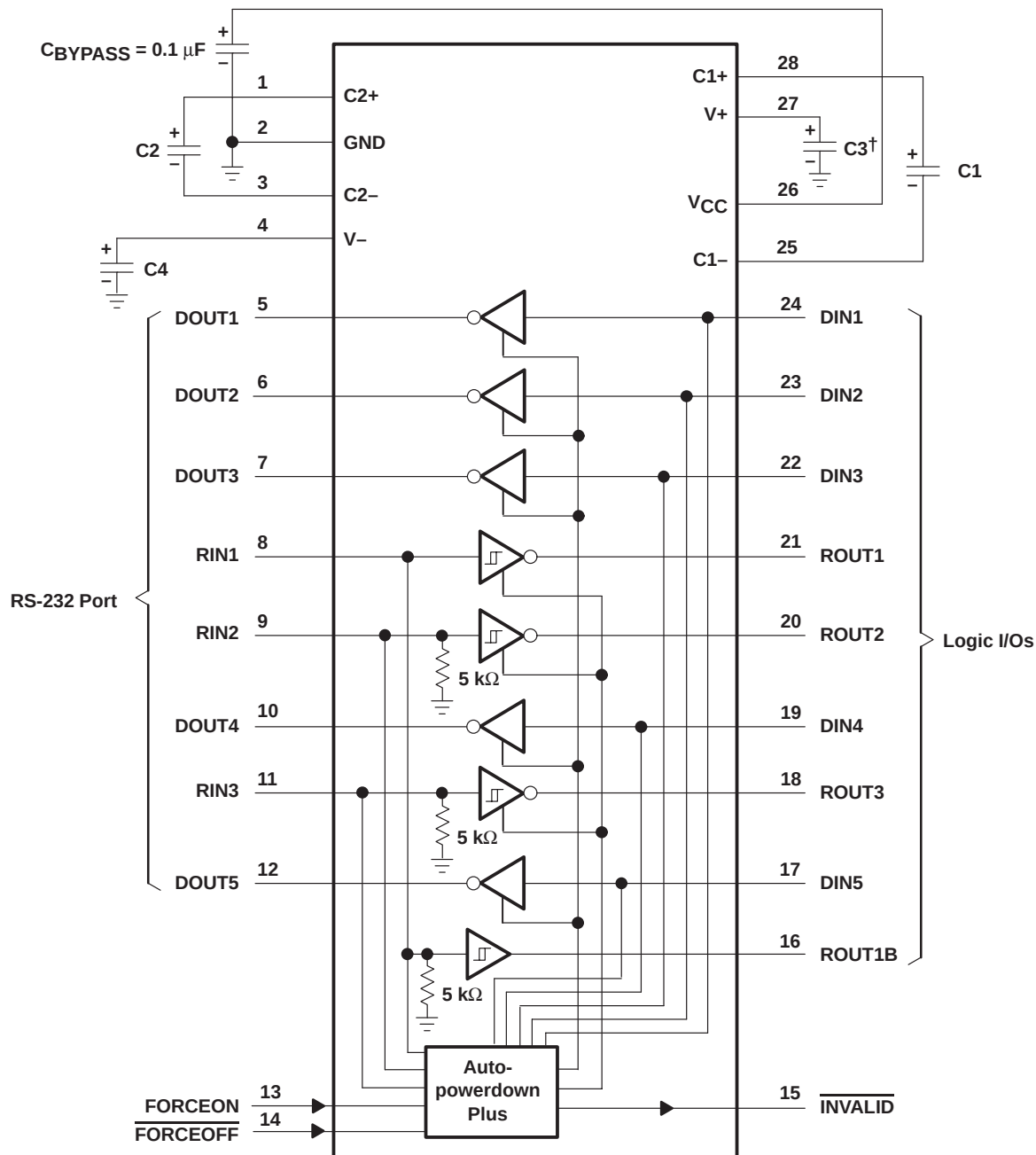
**Figure 5.  $\overline{\text{INVALID}}$  Propagation Delay Times and Supply Enabling Time**

# MAX3238

## 3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVER/RECEIVER

SLLS349B – JUNE 1999 – REVISED JANUARY 2000

### APPLICATION INFORMATION



† C3 can be connected to V<sub>CC</sub> or GND.

NOTE A: Resistor values shown are nominal.

#### V<sub>CC</sub> vs CAPACITOR VALUES

| V <sub>CC</sub> | C1       | C2, C3, and C4 |
|-----------------|----------|----------------|
| 3.3 V ± 0.15 V  | 0.1 μF   | 0.1 μF         |
| 3.3 V ± 0.3 V   | 0.22 μF  | 0.22 μF        |
| 5 V ± 0.5 V     | 0.047 μF | 0.33 μF        |
| 3 V to 5.5 V    | 0.22 μF  | 1 μF           |

Figure 6. Typical Operating Circuit and Capacitor Values

## **IMPORTANT NOTICE**

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.