

MC33063A, MC34063A

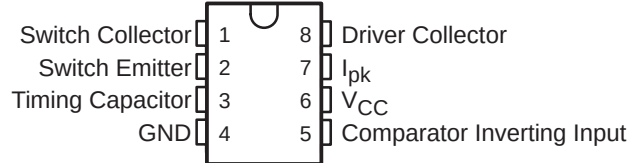
1.5-A PEAK BOOST/BUCK/INVERTING SWITCHING REGULATORS

SLLS636D – DECEMBER 2004 – REVISED MARCH 2005

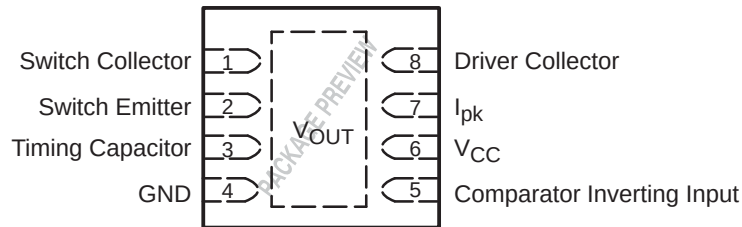
- Wide Input Voltage Range . . . 3 V to 40 V
- High Output Switch Current . . . Up to 1.5 A
- Adjustable Output Voltage
- Oscillator Frequency . . . Up to 100 kHz
- Precision Internal Reference . . . 2%
- Short-Circuit Current Limiting
- Low Standby Current

D (SOIC) OR P (PDIP) PACKAGE

(TOP VIEW)



DRJ (QFN) PACKAGE
(TOP VIEW)



description/ordering information

The MC33063A and MC34063A are easy-to-use ICs containing all the primary circuitry needed for building simple dc-dc converters. These devices primarily consist of an internal temperature-compensated reference, a comparator, an oscillator, a PWM controller with active current limiting, a driver, and a high-current output switch; thus, the devices require minimal external components to build converters in the boost, buck, and inverting topologies.

The MC33063A is characterized for operation from -40°C to 85°C , while the MC34063A is characterized for operation from 0°C to 70°C .

ORDERING INFORMATION

T_A	PACKAGE [†]		ORDERABLE PART NUMBER	TOP-SIDE MARKING
-40°C to 85°C	PDIP (P)	Tube of 50	MC33063AP	MC33063AP
	QFN (DRJ)	Reel of 1000	MC33063ADRJR	PREVIEW
	SOIC (D)	Tube of 75	MC33063AD	M33063A
		Reel of 2500	MC33063ADR	
0°C to 70°C	PDIP (P)	Tube of 50	MC34063AP	MC34063AP
	QFN (DRJ)	Reel of 1000	MC34063ADRJR	PREVIEW
	SOIC (D)	Tube of 75	MC34063AD	M34063A
		Reel of 2500	MC34063ADR	

[†] Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

**TEXAS
INSTRUMENTS**

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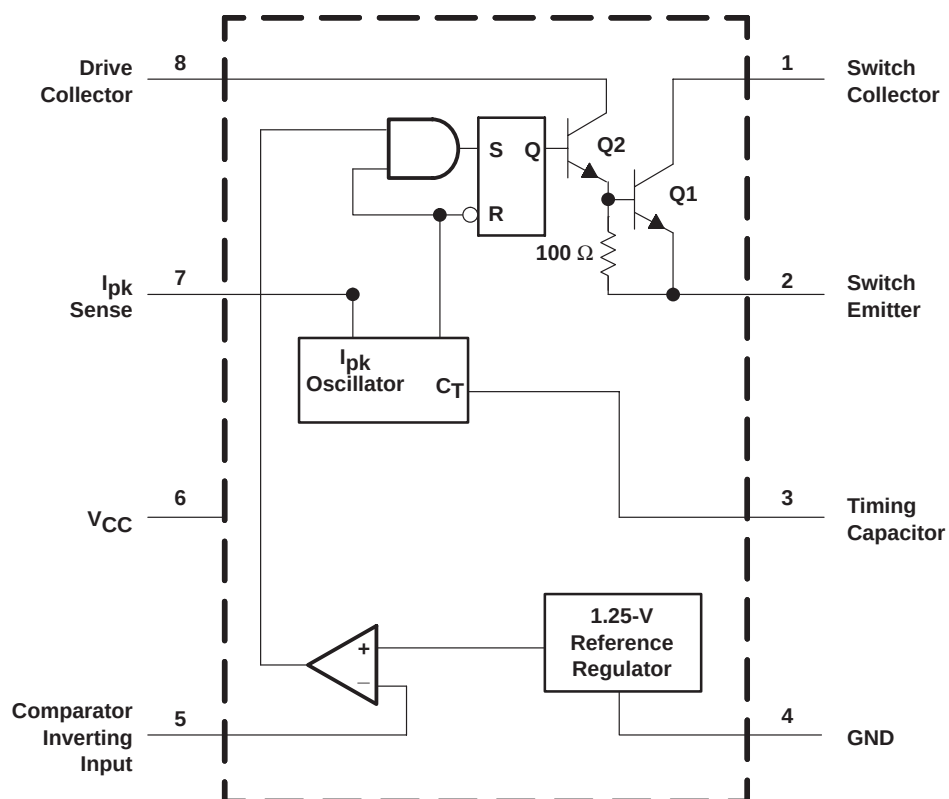
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functional block diagram



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

Supply voltage, V_{CC}	40 V
Comparator Inverting Input voltage range, V_{IR}	-0.3 V to 40 V
Switch Collector voltage, $V_{C(switch)}$	40V
Switch Emitter voltage ($V_{PIN1} = 40$ V), $V_{E(switch)}$	40V
Switch Collector to Switch Emitter voltage, $V_{CE(switch)}$	40V
Driver Collector voltage, $V_{C(driver)}$	40V
Driver Collector current, $I_{C(driver)}$	100 mA
Switch current, I_{SW}	1.5 A
Package thermal impedance, θ_{JA} (see Notes 1 and 2):	
D package	97°C/W
DRJ package	TBD°C/W
P package	85°C/W
Operating virtual junction temperature, T_J	150°C
Storage temperature range, T_{stg}	-65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. Maximum power dissipation is a function of $T_J(max)$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) - T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability.
2. The package thermal impedance is calculated in accordance with JESD 51-7.

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recommended operating conditions

			MIN	MAX	UNIT
V _{CC}	Supply voltage		3	40	V
T _A	Operating free-air temperature range	MC33063A	-40	85	°C
		MC34063A	0	70	

electrical characteristics, V_{CC} = 5 V, T_A = full operating range (unless otherwise noted) (see block diagram)

OSCILLATOR

PARAMETER		TEST CONDITIONS	T _A	MIN	TYP	MAX	UNIT
f _{osc}	Oscillator frequency	V _{PIN5} = 0 V, C _T = 1 nF	25°C	24	33	42	kHz
I _{chg}	Charge current	V _{CC} = 5 V to 40 V	25°C	24	35	42	μA
I _{dischg}	Discharge current	V _{CC} = 5 V to 40 V	25°C	140	220	260	μA
I _{dischg} /I _{chg}	Discharge-to-charge current ratio	V _{PIN7} = V _{CC}	25°C	5.2	6.5	7.5	
V _{Ipk}	Current-limit sense voltage	I _{dischg} = I _{chg}	25°C	250	300	350	mV

OUTPUT SWITCH (see Note 3)

PARAMETER	TEST CONDITIONS	T _A	MIN	TYP	MAX	UNIT
V _{CE(sat)}	Saturation voltage – Darlington connection	I _{SW} = 1 A, pins 1 and 8 connected	Full range	1	1.3	V
V _{CE(sat)}	Saturation voltage – non-Darlington connection (see Note 4)	I _{SW} = 1 A, R _{PIN8} = 82 Ω to V _{CC} , forced β ~ 20	Full range	0.45	0.7	V
h _{FE}	DC current gain	I _{SW} = 1 A, V _{CE} = 5 V	25°C	50	75	
I _{C(off)}	Collector off-state current	V _{CE} = 40 V	Full range	0.01	100	μA

NOTES: 3. Low duty-cycle pulse testing is used to maintain junction temperature as close to ambient temperature as possible.
 4. In the non-Darlington configuration, if the output switch is driven into hard saturation at low switch currents (≤300 mA) and high driver currents (≥30 mA), it may take up to 2 μs for the switch to come out of saturation. This condition effectively shortens the off time at frequencies ≥30 kHz, becoming magnified as temperature increases. The following output drive condition is recommended in the non-Darlington configuration:
 Forced β of output switch = I_{C,SW} / (I_{C,driver} – 7 mA) ≥ 10,
 where ~ 7 mA is required by the 100-Ω resistor in the emitter of the driver to forward bias the V_{BE} of the switch.

COMPARATOR

PARAMETER	TEST CONDITIONS	T _A	MIN	TYP	MAX	UNIT
V _{th}	Threshold voltage	25°C	1.225	1.25	1.275	V
		Full range	1.21		1.29	
ΔV _{th}	Threshold-voltage line regulation	V _{CC} = 5 V to 40 V	Full range	1.4	5	mV
I _{IB}	Input bias current	V _{IN} = 0 V	Full range	-20	-400	nA

TOTAL DEVICE

PARAMETER	TEST CONDITIONS	T _A	MIN	MAX	UNIT
I _{CC}	Supply current	V _{CC} = 5 V to 40 V, C _T = 1 nF, V _{PIN7} = V _{CC} , V _{PIN5} > V _{th} , V _{PIN2} = GND, all other pins open	Full range	4	mA



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TYPICAL CHARACTERISTICS

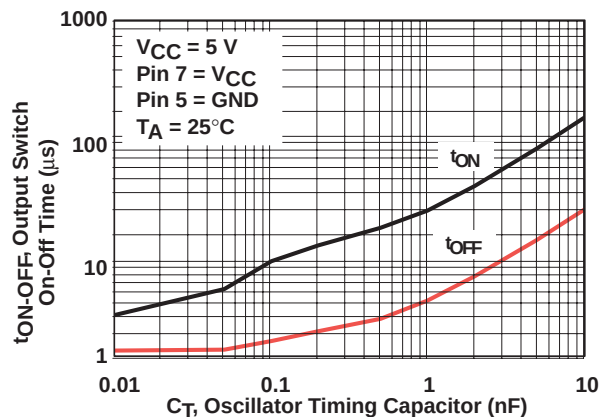


Figure 1. Output Switch On-Off Time vs Oscillator Timing Capacitor

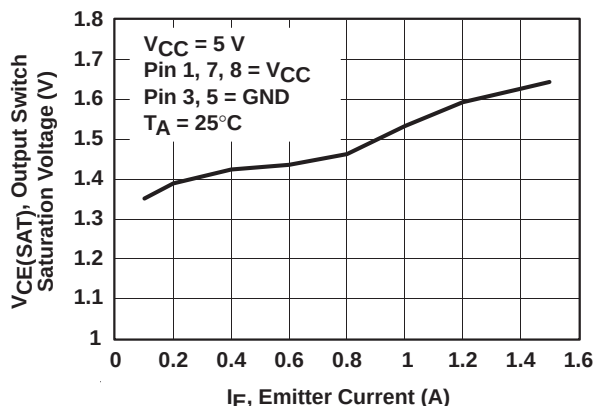


Figure 2. Output Switch Saturation Voltage vs Emitter Current (Emitter-Follower Configuration)

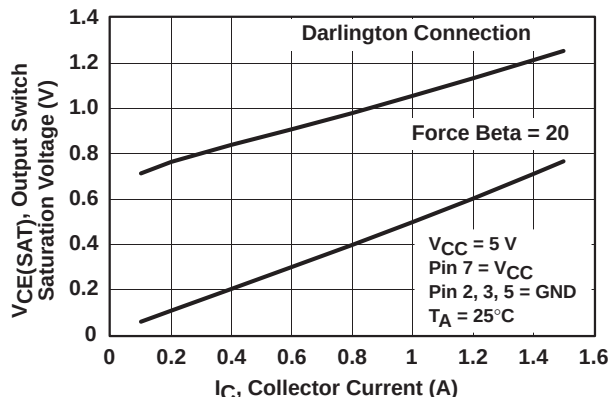


Figure 3. Output Switch Saturation Voltage vs Collector Current (Common-Emitter Configuration)

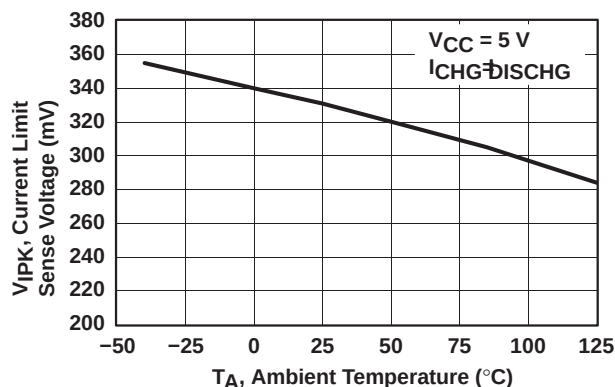


Figure 4. Current-Limit Sense Voltage vs Temperature

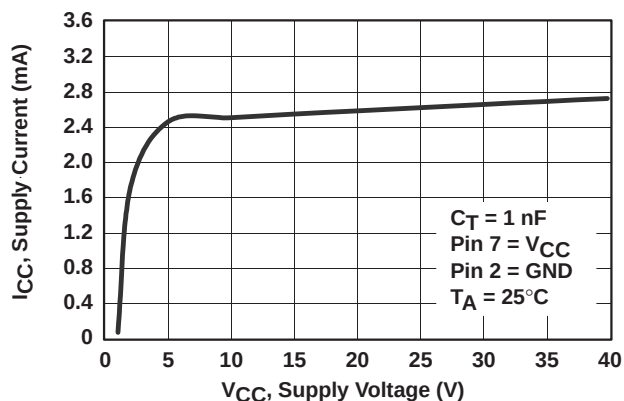


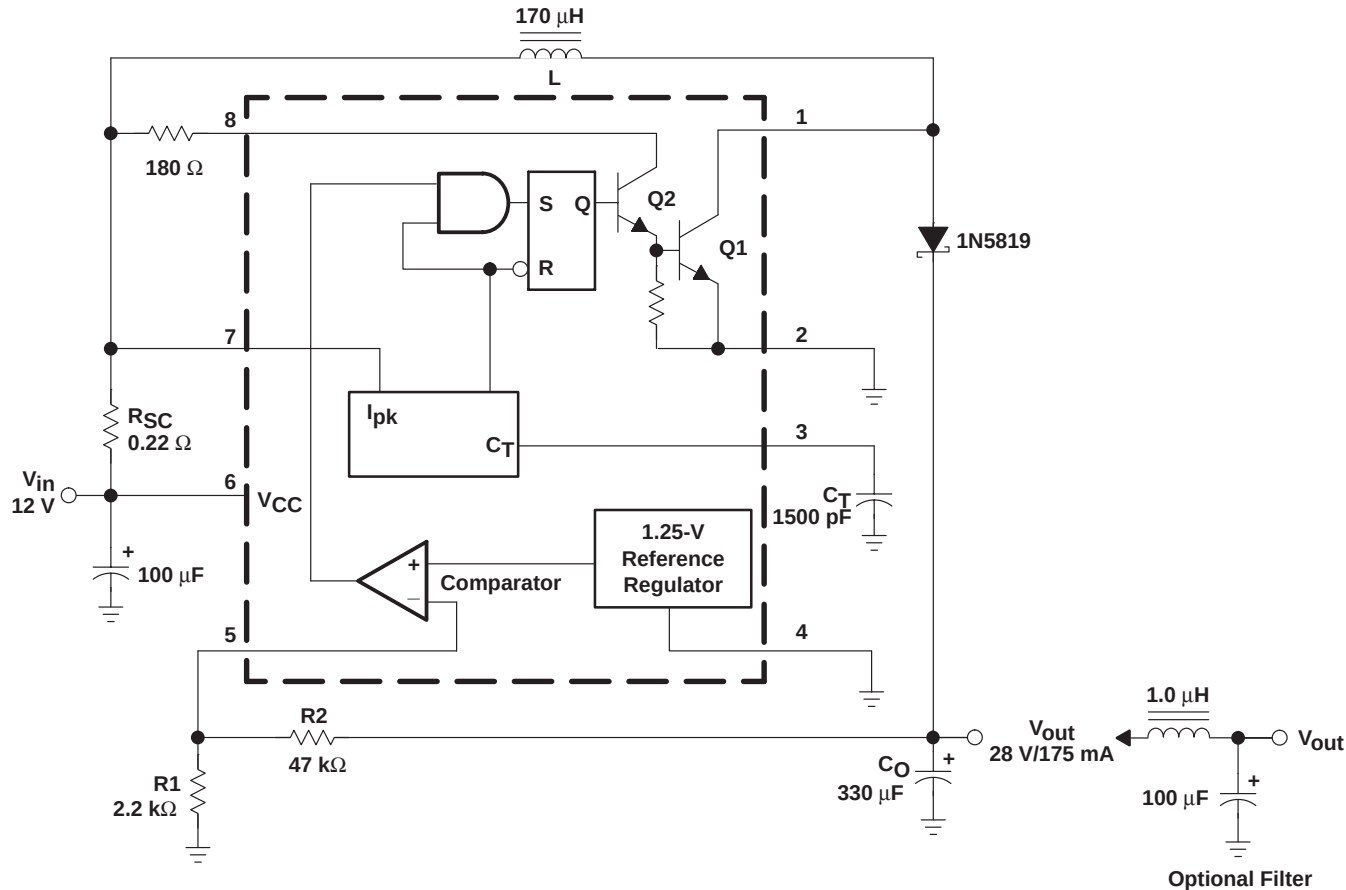
Figure 5. Standby Supply Current vs Supply Voltage

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TYPICAL CHARACTERISTICS



TEST	CONDITIONS	RESULTS
Line Regulation	$V_{IN} = 8 \text{ V to } 16 \text{ V}$, $I_O = 175 \text{ mA}$	30 mV $\pm$ 0.05%
Load Regulation	$V_{IN} = 12 \text{ V}$, $I_O = 75 \text{ mA to } 175 \text{ mA}$	10 mV $\pm$ 0.017%
Output Ripple	$V_{IN} = 12 \text{ V}$, $I_O = 175 \text{ mA}$	400 mV _{pp}
Efficiency	$V_{IN} = 12 \text{ V}$, $I_O = 175 \text{ mA}$	87.7%
Output Ripple With Optional Filter	$V_{IN} = 12 \text{ V}$, $I_O = 175 \text{ mA}$	40 mV _{pp}

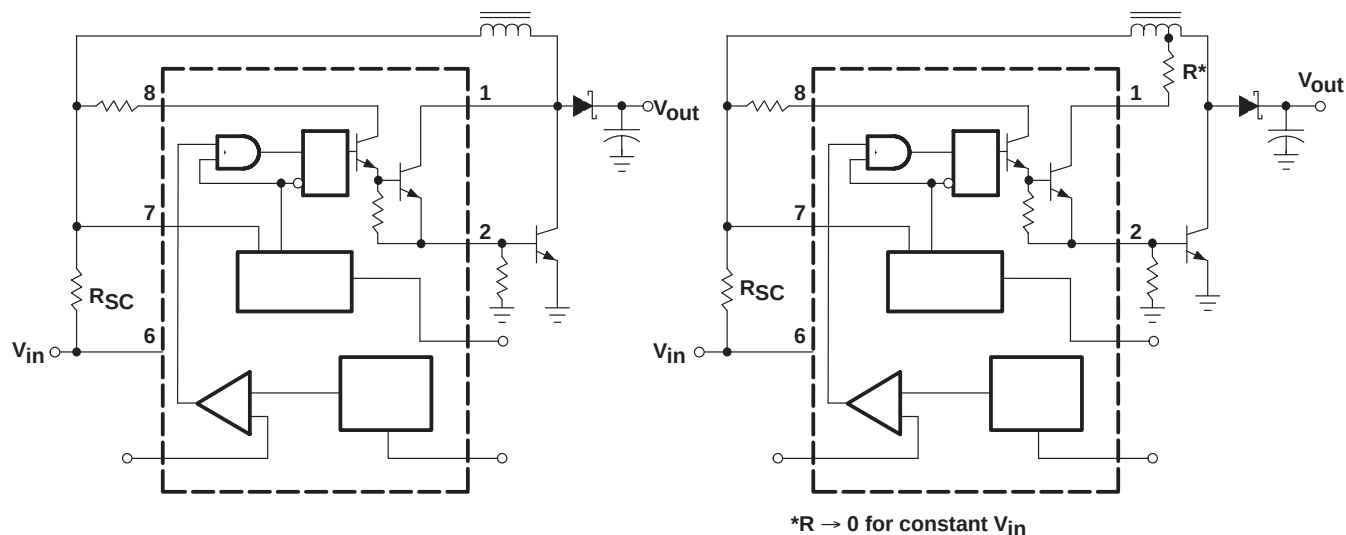
Figure 6. Step-Up Converter

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TYPICAL CHARACTERISTICS



2a. External npn Switch

2b. External pnp Saturated Switch (see Note 5)

NOTE A: If the output switch is driven into hard saturation (non-Darlington configuration) at low switch currents (≤ 300 mA) and high driver currents (≥ 30 mA), it may take up to $2 \mu\text{s}$ to come out of saturation. This condition will shorten the off time at frequencies ≥ 30 kHz and is magnified at high temperatures. This condition does not occur with a Darlington configuration because the output switch cannot saturate. If a non-Darlington configuration is used, the output drive configuration in Figure 2b is recommended.

Figure 7. External Current-Boost Connections for I_C Peak Greater Than 1.5 A

The circuit diagram illustrates a 5-V, 500-mA buck converter. The input voltage V_{in} is 25 V, connected to pin 8. A resistor R_{SC} (0.33 Ω) is connected between pin 8 and pin 7. Pin 7 is connected to the V_{CC} pin of the integrated circuit. Pin 6 is connected to the V_{CC} pin. A 100 μF capacitor is connected between pin 6 and ground. Pin 5 is connected to the V_{SS} pin. A resistor $R1$ (1.2 k Ω) is connected between pin 5 and ground. Pin 4 is connected to the V_{SS} pin. A resistor $R2$ (3.8 k Ω) is connected between pin 4 and ground. The output voltage V_{out} is 5 V/500 mA, connected to pin 2. A 1N5819 diode is connected between pin 2 and ground. A 470 pF capacitor C_T is connected between pin 3 and ground. A 220 μH inductor L is connected between pin 3 and the output. An optional filter consisting of a 1.0 μH inductor and a 100 μF capacitor is connected to the output. The integrated circuit is a 14-pin DIP package with pins 1 through 14 labeled on the right side.

TEST	CONDITIONS	RESULTS
Line Regulation	$V_{IN} = 15\text{ V to }25\text{ V}, I_O = 500\text{ mA}$	$12\text{ mV} \pm 0.12\%$
Load Regulation	$V_{IN} = 25\text{ V}, I_O = 50\text{ mA to }500\text{ mA}$	$3\text{ mV} \pm 0.03\%$
Output Ripple	$V_{IN} = 25\text{ V}, I_O = 500\text{ mA}$	120 mV_{PP}
Short-Circuit Current	$V_{IN} = 25\text{ V}, R_L = 0.1\ \Omega$	1.1 A
Efficiency	$V_{IN} = 25\text{ V}, I_O = 500\text{ mA}$	83.7%
Output Ripple With Optional Filter	$V_{IN} = 25\text{ V}, I_O = 500\text{ mA}$	40 mV_{PP}



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TYPICAL CHARACTERISTICS

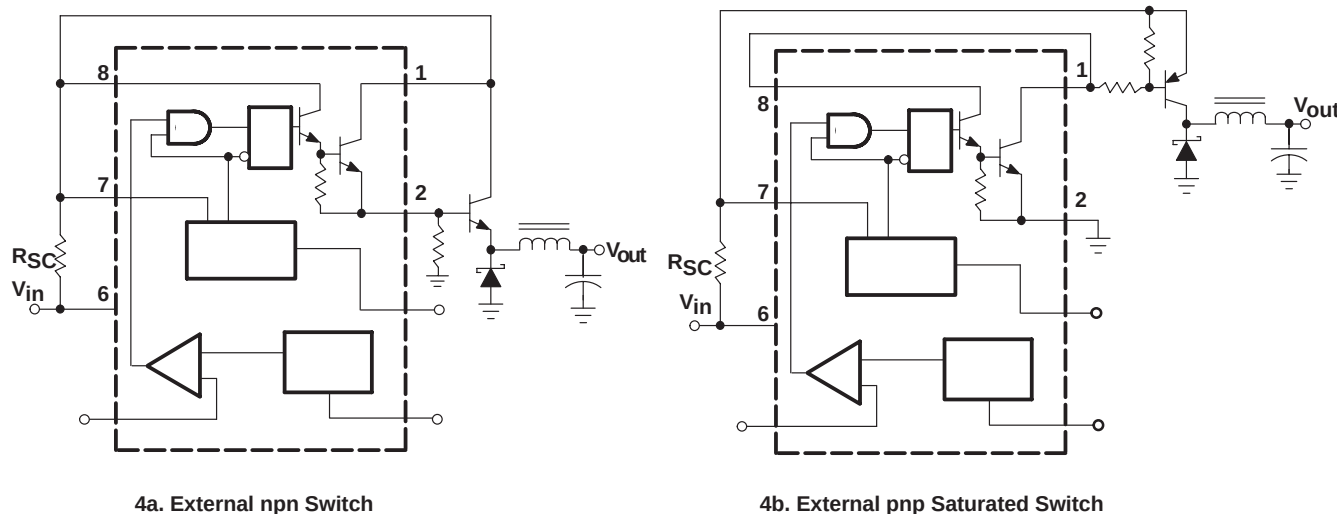


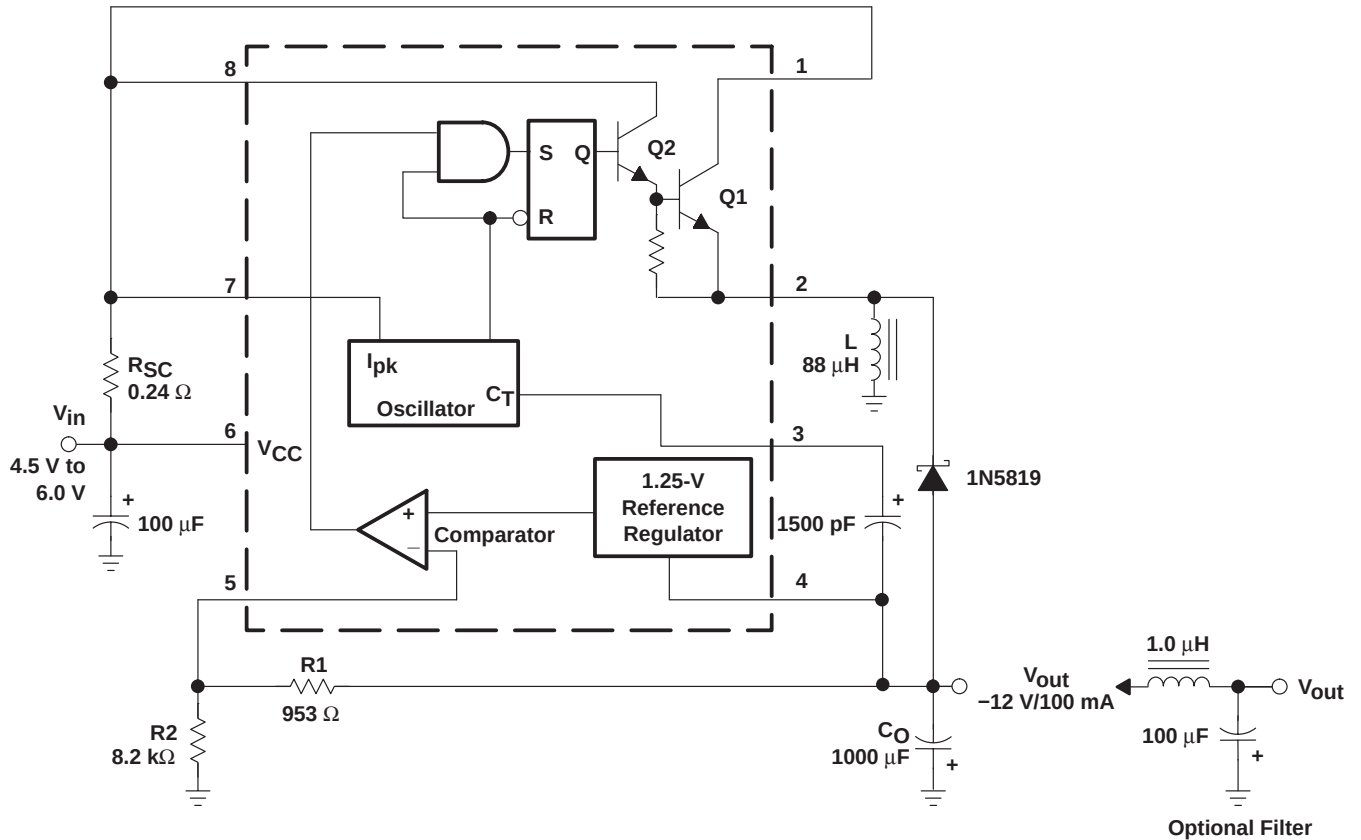
Figure 9. External Current-Boost Connections for I_C Peak Greater Than 1.5 A

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TYPICAL CHARACTERISTICS



TEST	CONDITIONS	RESULTS
Line Regulation	V _{IN} = 4.5 V to 6 V, I _O = 100 mA	3 mV ± 0.12%
Load Regulation	V _{IN} = 5 V, I _O = 10 mA to 100 mA	0.022 V ± 0.09%
Output Ripple	V _{IN} = 5 V, I _O = 100 mA	500 mV _{pp}
Short-Circuit Current	V _{IN} = 5 V, R _L = 0.1 Ω	910 mA
Efficiency	V _{IN} = 5 V, I _O = 100 mA	62.2%
Output Ripple With Optional Filter	V _{IN} = 5 V, I _O = 100 mA	70 mV _{pp}

Figure 10. Voltage-Inverting Converter

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TYPICAL CHARACTERISTICS

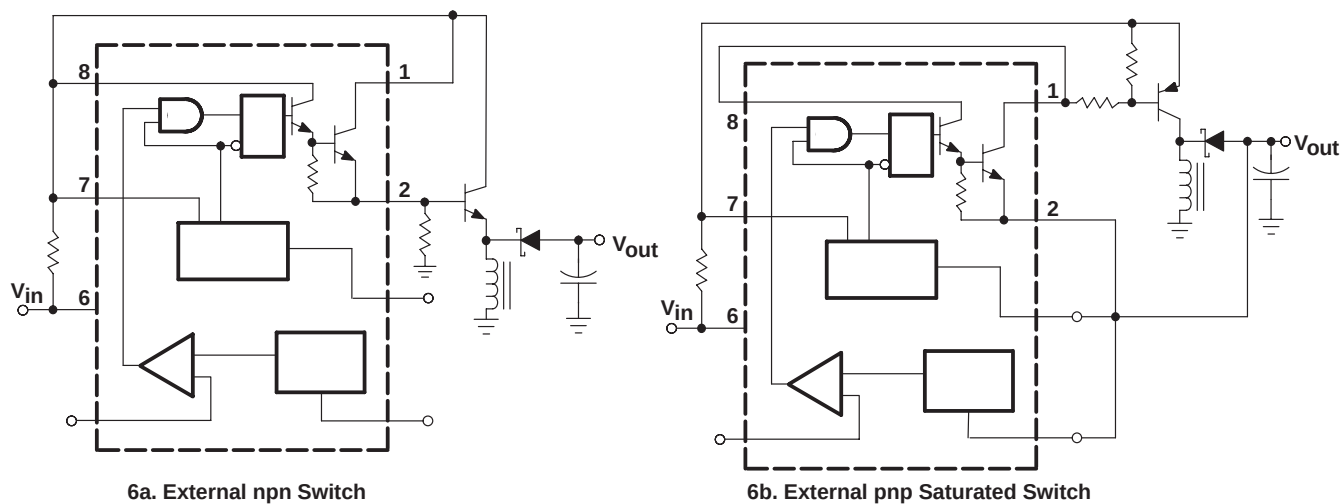


Figure 11. External Current-Boost Connections for I_C Peak Greater Than 1.5 A

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APPLICATION INFORMATION

calculations of key parameters

CALCULATION	STEP UP	STEP DOWN	VOLTAGE INVERTING
t_{on}/t_{off}	$\frac{V_{out} + V_F - V_{in(min)}}{V_{in(min)} - V_{sat}}$	$\frac{V_{out} + V_F}{V_{in(min)} - V_{sat} - V_{out}}$	$\frac{ V_{out} + V_F}{V_{in} - V_{sat}}$
$(t_{on} + t_{off})$	$\frac{1}{f}$	$\frac{1}{f}$	$\frac{1}{f}$
t_{off}	$\frac{t_{on} + t_{off}}{\frac{t_{on}}{t_{off}} + 1}$	$\frac{t_{on} + t_{off}}{\frac{t_{on}}{t_{off}} + 1}$	$\frac{t_{on} + t_{off}}{\frac{t_{on}}{t_{off}} + 1}$
t_{on}	$(t_{on} + t_{off}) - t_{off}$	$(t_{on} + t_{off}) - t_{off}$	$(t_{on} + t_{off}) - t_{off}$
C_T	$4 \times 10^{-5} t_{on}$	$4 \times 10^{-5} t_{on}$	$4 \times 10^{-5} t_{on}$
$I_{pk(switch)}$	$2I_{out(max)} \left(\frac{t_{on}}{t_{off}} + 1 \right)$	$2I_{out(max)}$	$2I_{out(max)} \left(\frac{t_{on}}{t_{off}} + 1 \right)$
R_{sc}	$\frac{0.3}{I_{pk(switch)}}$	$\frac{0.3}{I_{pk(switch)}}$	$\frac{0.3}{I_{pk(switch)}}$
$L_{(min)}$	$\left(\frac{(V_{in(min)} - V_{sat})}{I_{pk(switch)}} \right) t_{on(max)}$	$\left(\frac{(V_{in(min)} - V_{sat} - V_{out})}{I_{pk(switch)}} \right) t_{on(max)}$	$\left(\frac{(V_{in(min)} - V_{sat})}{I_{pk(switch)}} \right) t_{on(max)}$
C_O	$9 \frac{I_{out} t_{on}}{V_{ripple(pp)}}$	$\frac{I_{pk(switch)}(t_{on} + t_{off})}{8V_{ripple(pp)}}$	$9 \frac{I_{out} t_{on}}{V_{ripple(pp)}}$

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
MC33063AD	ACTIVE	SOIC	D	8	75	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
MC33063ADR	ACTIVE	SOIC	D	8	2500	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
MC33063AP	ACTIVE	PDIP	P	8	50	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
MC34063AD	ACTIVE	SOIC	D	8	75	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
MC34063ADR	ACTIVE	SOIC	D	8	2500	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
MC34063AP	ACTIVE	PDIP	P	8	50	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBsolete: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - May not be currently available - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

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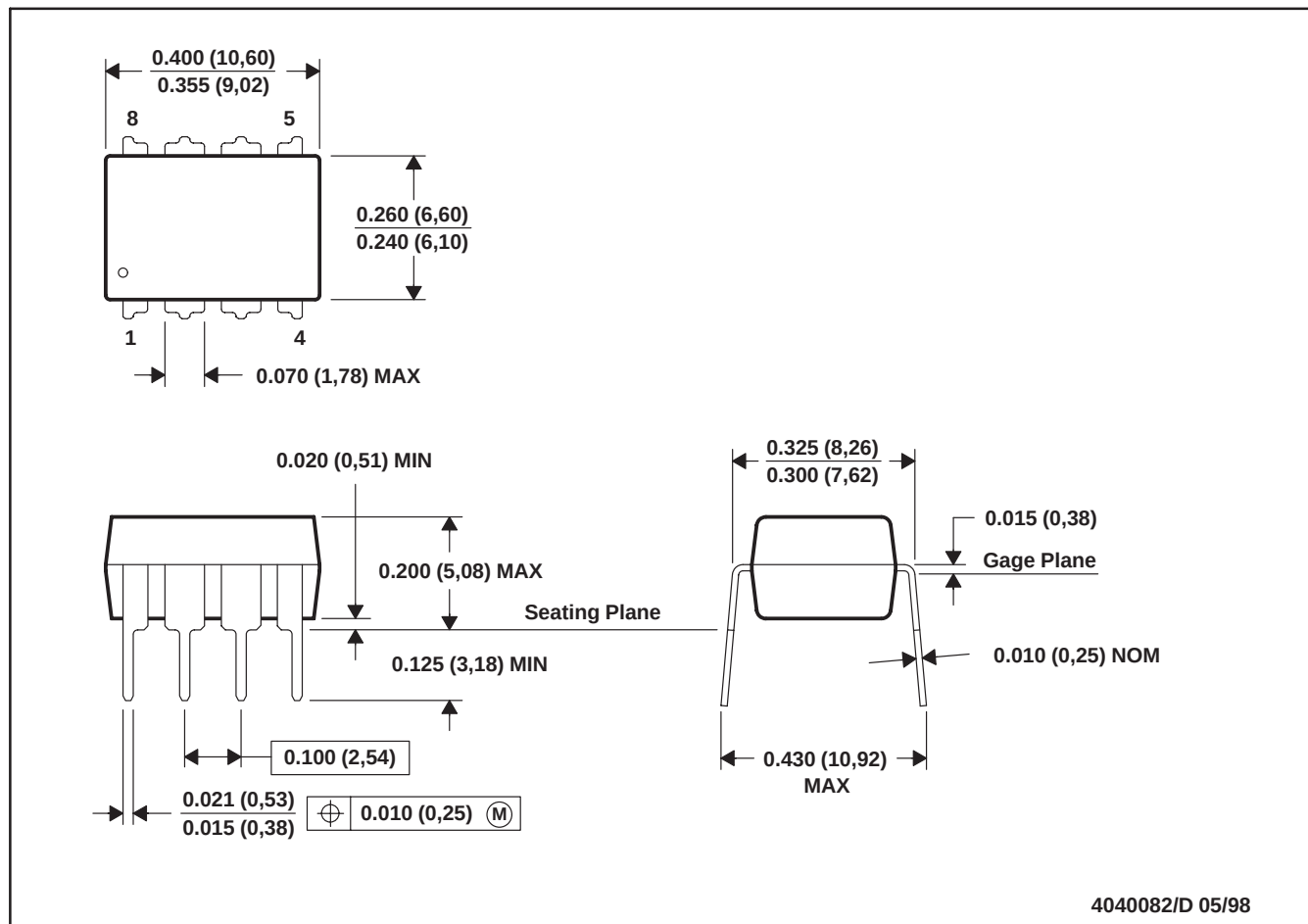
⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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P (R-PDIP-T8)

PLASTIC DUAL-IN-LINE

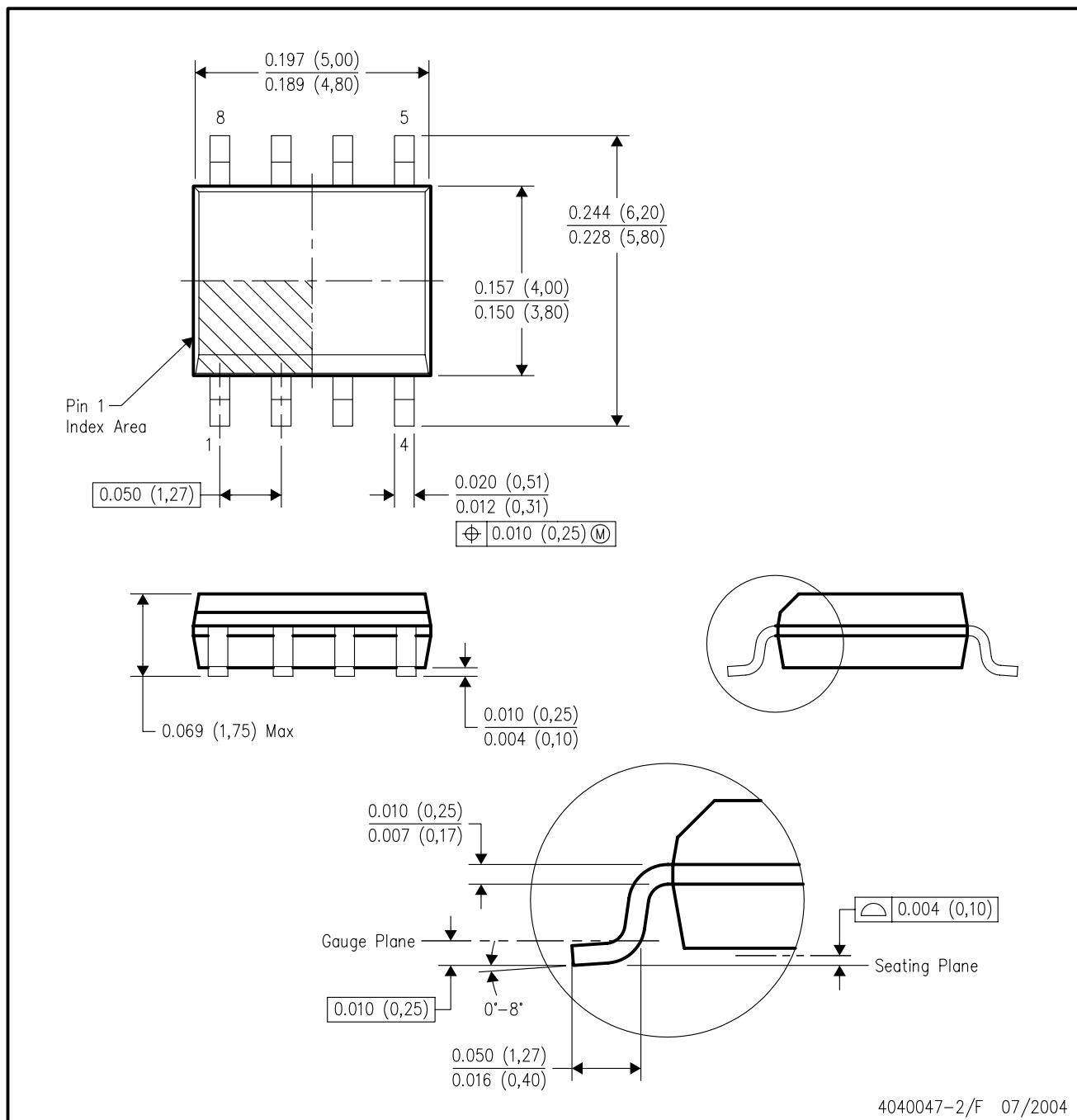


- NOTES: A. All linear dimensions are in inches (millimeters).
 B. This drawing is subject to change without notice.
 C. Falls within JEDEC MS-001

For the latest package information, go to http://www.ti.com/sc/docs/package/pkg_info.htm

D (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



- NOTES:
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 - This drawing is subject to change without notice.
 - Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
 - Falls within JEDEC MS-012 variation AA.

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