- Designed for Digital Data Transmission Over 50-Ω to 500-Ω Coaxial Cable, Strip Line, or Twisted Pair
- High Speed t_{pd} = 20 ns Maximum at C_L = 15 pF
- TTL Compatible With Single 5-V Supply
- 2.4-V Output at I_{OH} = -75 mA
- Uncommitted Emitter-Follower Output Structure for Party-Line Operation
- Short-Circuit Protection
- AND-OR Logic Configuration
- Designed for Use With Triple Line Receivers SN55122, SN75122
- Designed to Be Interchangeable With Signetics N8T13

description

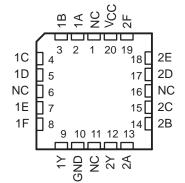
The SN55121 and SN75121 dual line drivers are designed for digital data transmission over lines having impedances from 50 to 500 Ω . They are also compatible with standard TTL logic and supply-voltage levels.

The low-impedance emitter-follower outputs of the SN55121 and SN75121 can drive terminated lines such as coaxial cable or twisted pair. Having the outputs uncommitted allows wired-OR logic to be performed in party-line applications. Output short-circuit protection is provided by an internal clamping network that turns on when the output voltage drops below approximately 1.5 V. All of the inputs are in conventional TTL configuration and the gating can be used during power-up and power-down sequences to ensure that no noise is introduced to the line.

The SN55121 is characterized for operation over the full military temperature range of -55° C to 125°C. The SN75121 is characterized for operation from 0°C to 70°C.

| SN55121 J PACKAGE SN75121 D OR N PACKAGE (TOP VIEW) | | | | | | |
|-----------------------------------------------------------|--------------------------------------|---------------------------------------------|---------------------------------------------------------------------------|--|--|--|
| 1A [1B [1C [1D [1F [1Y [GND [| 1 2 3 4 5 6 7 8 | 16 15 14 13 12 11 10 9 |] V _{CC}] 2F] 2E] 2D] 2C] 2B] 2A] 2Y | | | |
| 1 | | | | | | |

| SN55121 | FK PACKAGE |
|---------|------------|
| (TOP | VIEW) |



NC-No internal connection

THE SN75121 IS NOT RECOMMENDED FOR NEW DESIGNS



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SN55121, SN75121 DUAL LINE DRIVERS

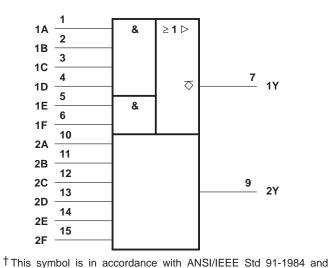
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| FU | NCT | ION | TA | BLE | = |
|----|-----|-----|----|-----|---|

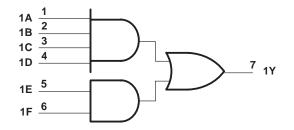
| | I UNCTION TABLE | | | | | | | | |
|---|-----------------|--------|---|---|---|---|--|--|--|
| | | OUTPUT | | | | | | | |
| Α | В | С | D | Е | F | Y | | | |
| Н | Н | Н | Н | Х | Х | Н | | | |
| Х | Х | Х | Х | Н | Н | н | | | |
| | All othe | L | | | | | | | |

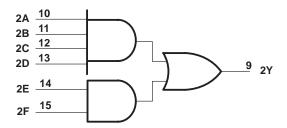
H = high level, L = low level, X = irrelevant

logic symbol[†]



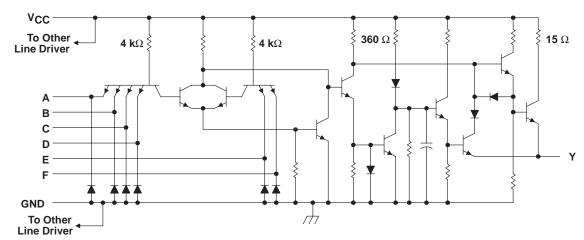
logic diagram (positive logic)





IEC Publication 617-12. Pin numbers shown are for the D, J, and N packages.

schematic (each driver)



All resistor values shown are nominal.



absolute maximum ratings over operating free-air temperature (unless otherwise noted)[†]

| Supply voltage, V _{CC} (see Note 1) Input voltage Output voltage | |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------|
| Continuous total power dissipation | . See Dissipation Rating Table |
| Case temperature for 60 seconds: FK packageLead temperature 1,6 mm (1/16 inch) from case for 60 seconds: J package Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds: D or N package . | 300°C |

⁺ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: All voltage values are with respect to both ground terminals connected together.

| DISSIPATION RATING TABLE | | | | | | | |
|--------------------------|---------------------------------------|------------------------------------------------|---------------------------------------|----------------------------------------|--|--|--|
| PACKAGE | T _A ≤ 25°C POWER RATING | DERATING FACTOR ABOVE T _A = 25°C | T _A = 70°C POWER RATING | T _A = 125°C POWER RATING | | | |
| D | 950 mW | 7.6 mW/°C | 608 mW | _ | | | |
| FK‡ | 1375 mW | 11.0 mW/°C | 880 mW | 275 mW | | | |
| ‡ر | 1375 mW | 11.0 mW/°C | 880 mW | 275 mW | | | |
| Ν | 1150 mW | 9.2 mW/°C | 736 mW | — | | | |

[‡] In the FK and J packages, SN55121 chips are either silver glass or alloy mounted.

recommended operating conditions

| | SN55121 | | | SN75121 | | | | |
|------------------------------------------------|---------|-----|------|---------|-----|------|------|--|
| | MIN | NOM | MAX | MIN | NOM | MAX | UNIT | |
| Supply voltage, V _{CC} | 4.75 | 5 | 5.25 | 4.75 | 5 | 5.25 | V | |
| High-level input voltage, V _{IH} | 2 | | | 2 | | | V | |
| Low-level input voltage, VIL | | | 0.8 | | | 0.8 | V | |
| High-level output current, IOH | | | -75 | | | -75 | mA | |
| Operating free-air temperature, T _A | -55 | | 125 | 0 | | 70 | °C | |



electrical characteristics over recommended ranges of supply voltage and operating free-air temperature range (unless otherwise noted)

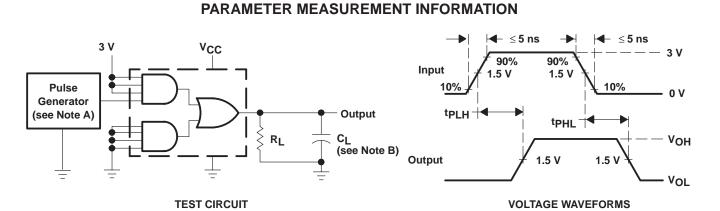
| | PARAMETER | | TEST CONDITIONS | 5 | MIN | MAX | UNIT |
|-------------------|-------------------------------------------|-----------------------------------------|----------------------------------------|------------------------|------|------|------|
| VIK | Input clamp voltage | $V_{CC} = 5 V,$ | $I_I = -12 \text{ mA}$ | | | -1.5 | V |
| V _(BR) | Breakdown voltage | $V_{CC} = 5 V,$ | lı = 10 mA | | 5.5 | | V |
| VOH | High-level output voltage | V _{IH} = 2 V, | I _{OH} = -75 mA, | See Note 2 | 2.4 | | V |
| IOH | High-level output current | $V_{CC} = 5 V,$ $T_A = 25^{\circ}C,$ | V _{IH} = 4.5 V, See Note 2 | V _{OH} = 2 V, | -100 | -250 | mA |
| IOL | Low-level output current | V _{IL} = 0.8 V, | V _{OL} = 0.4 V, | See Note 2 | | -800 | μA |
| IO(off) | Off-state output current | V _{CC} = 3 V, | V _O = 3 V | | | 500 | μA |
| Iн | High-level output current | V _I = 4.5 V | | | | 40 | μA |
| Ι _Ι | Low-level output current | V _I = 0.4 V | | | -0.1 | -1.6 | mA |
| los | Short-circuit output current [†] | V _{CC} = 5 V, | T _A = 25°C | | | -30 | mA |
| ІССН | Supply current, outputs high | V _{CC} = 5.25 V, | All inputs at 2 V, | Outputs open | | 28 | mA |
| ICCL | Supply current, outputs low | V _{CC} = 5.25 V, | All inputs at 0.8 V, | Outputs open | | 60 | mA |

[†] Not more than one output should be shorted at a time.

NOTE 2: The output voltage and current limits are valid for any appropriate combination of high and low inputs specified by the function table for the desired output.

switching characteristics, V_{CC} = 5 V, T_A = 25° C

| PARAMETER | | TEST CONDITIONS | | | MIN | TYP | MAX | UNIT |
|------------------|--------------------------------------------------|-------------------|---------------------------|--------------|-----|-----|-----|------|
| ^t PLH | Propagation delay time, low-to-high level output | $P_{1} = 27.0$ | C _I = 15 pF, | See Figure 1 | | 11 | 20 | |
| ^t PHL | Propagation delay time, high-to-low level output | $K_{L} = 57.52$, | CL = 15 pr, | See Figure 1 | | 8 | 20 | ns |
| ^t PLH | Propagation delay time, low-to-high level output | $P_{1} = 27.0$ | C 1000 pE | Soo Eiguro 1 | | 22 | 50 | |
| ^t PHL | Propagation delay time, high-to-low level output | $K_{L} = 37.32$, | C _L = 1000 pF, | See Figure 1 | | 20 | 50 | ns |



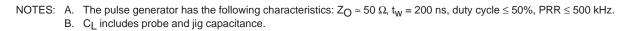
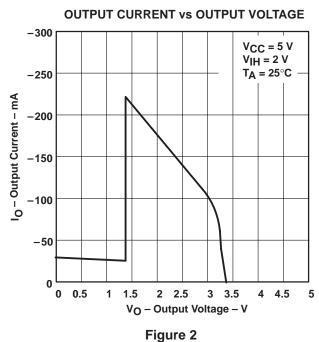


Figure 1. Test Circuit and Voltage Waveforms





TYPICAL CHARACTERISTICS



SN55121, SN75121 DUAL LINE DRIVERS

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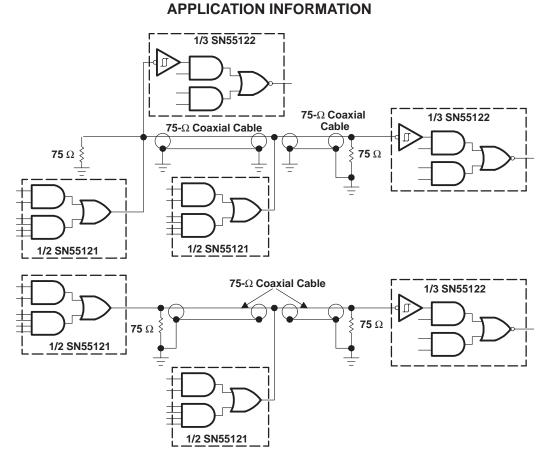


Figure 3. Single-Ended Party-Line Circuits



PACKAGING INFORMATION

| Orderable Device | Status ⁽¹⁾ | Package Type | Package Drawing | Pins | Package Qty | Eco Plan ⁽²⁾ | Lead/Ball Finish | MSL Peak Temp ⁽³⁾ |
|------------------|-----------------------|-----------------|--------------------|------|----------------|-------------------------|------------------|-------------------------------------------|
| SN55121J | OBSOLETE | CDIP | J | 16 | | None | Call TI | Call TI |
| SN75121D | OBSOLETE | SOIC | D | 16 | | None | Call TI | Call TI |
| SN75121N | ACTIVE | PDIP | Ν | 16 | 25 | Pb-Free (RoHS) | CU NIPDAU | Level-NC-NC-NC |
| SN75121NSR | ACTIVE | SO | NS | 16 | 2000 | Pb-Free (RoHS) | CU NIPDAU | Level-2-260C-1 YEAF Level-1-235C-UNLIM |
| SNJ55121FK | OBSOLETE | LCCC | FK | 20 | | None | Call TI | Call TI |
| SNJ55121J | OBSOLETE | CDIP | J | 16 | | None | Call TI | Call TI |

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - May not be currently available - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

None: Not yet available Lead (Pb-Free).

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean "Pb-Free" and in addition, uses package materials that do not contain halogens, including bromine (Br) or antimony (Sb) above 0.1% of total product weight.

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDECindustry standard classifications, and peak solder temperature.

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