

8961726 TEXAS INSTR (OPTO)

62C 37044 D

T-33-15

TIPL757, TIPL757A
N-P-N SILICON POWER TRANSISTORS
electrical characteristics at 25°C case temperature (unless otherwise noted)

PARAMETER	TEST CONDITIONS	TIPL757			TIPL757A			UNIT
		MIN	TYP	MAX	MIN	TYP	MAX	
V _{CEO(sus)}	I _C = 0.1 A, L = 25 mH, See Note 2	375			420			V
I _{CEO}	V _{CE} = 375 V, I _B = 0			50				μA
	V _{CE} = 420 V, I _B = 0					50		
I _{CES}	V _{CE} = 800 V, V _{BE} = 0			50				μA
	V _{CE} = 800 V, V _{BE} = 0, T _C = 100°C			100				
	V _{CE} = 1000 V, V _{BE} = 0					50		
	V _{CE} = 1000 V, V _{BE} = 0, T _C = 100°C					100		
I _{EBO}	V _{EB} = 10 V, I _C = 0			1			1	mA
h _{FE}	V _{CE} = 5 V, I _C = 0.5 A, See Notes 3 and 4	15	60	15	60			V
	I _C = 5 A, I _B = 1 A, See Notes 3 and 4		0.5		0.5			
	I _C = 10 A, I _B = 2 A, See Notes 3 and 4		1		1			
	I _C = 15 A, I _B = 3 A, See Notes 3 and 4		2.5		2.5			
	I _C = 15 A, I _B = 3 A, T _C = 100°C, See Notes 3 and 4		5		5			
V _{BE(sat)}	I _C = 5 A, I _B = 1 A, See Notes 3 and 4		1.4		1.4			V
	I _C = 10 A, I _B = 2 A, See Notes 3 and 4		1.5		1.5			
	I _C = 15 A, I _B = 3 A, See Notes 3 and 4		1.7		1.7			
	I _C = 15 A, I _B = 3 A, T _C = 100°C, See Notes 3 and 4		1.8		1.8			
f _T	V _{CE} = 10 V, I _C = 0.5 A, See Note 5	11.5			11.5			MHz
C _{obo}	V _{CB} = 20 V, I _E = 0, f = 0.1 MHz	200			200			pF

- NOTES: 2. Inductive loop switching measurement.
 3. These parameters must be measured using pulse techniques, t_w = 300 μs, duty cycle < 2 %.
 4. These parameters are measured with voltage-sensing contacts separate from the current-carrying contacts and located within 3.2 mm (0.125 inch) from the device body.
 5. To obtain f_T, the |h_{fe}| response is extrapolated at the rate of -6 dB per octave from f = 1 MHz to the frequency at which |h_{fe}| = 1.

TIPL Devices



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62C 37045 D

TIPL757, TIPL757A
N-P-N SILICON POWER TRANSISTORS

T-33-15

thermal characteristics

PARAMETER	MIN	TYP	MAX	UNIT
$R_{\theta JC}$			0.9	$^{\circ}C/W$

resistive-load switching characteristics at 25°C case temperature (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t_{on}	$I_C = 15 A, V_{CC} = 250 V, T = 25^{\circ}C$ $I_{B1} = 3 A, I_{B2} = -3.75 A$			0.75	μs
t_s				2.6	μs
t_f				0.6	μs
t_{on}	$I_C = 15 A, V_{CC} = 250 V, T_C = 100^{\circ}C$ $I_{B1} = 3 A, I_{B2} = -3.75 A$			1	μs
t_s				3	μs
t_f				1.2	μs

inductive-load switching characteristics at 25°C case temperature (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t_{sv}	$I_C = 15 A, V_{BE(off)} = -10 V, T_C = 25^{\circ}C$ $I_{B1} = 3 A, I_{B2} = -5 A$			2.6	μs
t_{rv}				0.2	μs
t_{fi}				0.3	μs
t_{ti}				0.05	μs
t_{xo}				0.5	μs
t_{sv}	$I_C = 15 A, V_{BE(off)} = -10 V, T_C = 100^{\circ}C$ $I_{B1} = 3 A, I_{B2} = -5 A$			3	μs
t_{rv}				0.36	μs
t_{fi}				0.4	μs
t_{ti}				0.08	μs
t_{xo}				0.8	μs

TIPL Devices



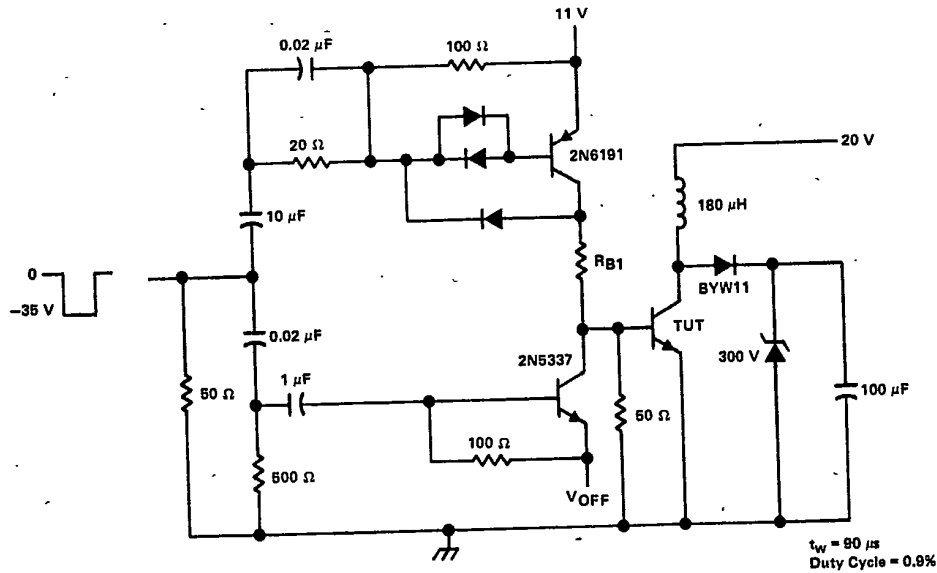
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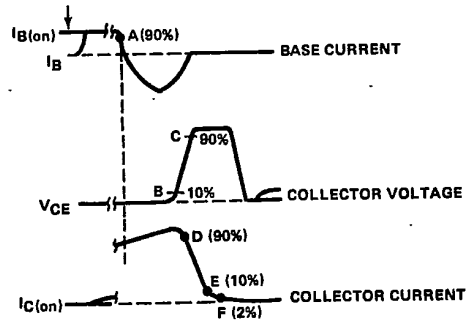
TIPL757, TIPL757A
N-P-N SILICON POWER TRANSISTORS

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PARAMETER MEASUREMENT INFORMATION



TEST CIRCUIT



VOLTAGE AND CURRENT WAVEFORMS

NOTES: A. Waveforms are monitored on an oscilloscope with the following characteristics: $t_r < 15 \text{ ns}$, $R_{in} \geq 10 \text{ M}\Omega$, $C_{in} < 11.5 \text{ pF}$.
B. Resistors must be noninductive types.

FIGURE 2. INDUCTIVE-LOAD SWITCHING

TIPL Devices



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TIPL757, TIPL757A
N-P-N SILICON POWER TRANSISTORS

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TYPICAL CHARACTERISTICS

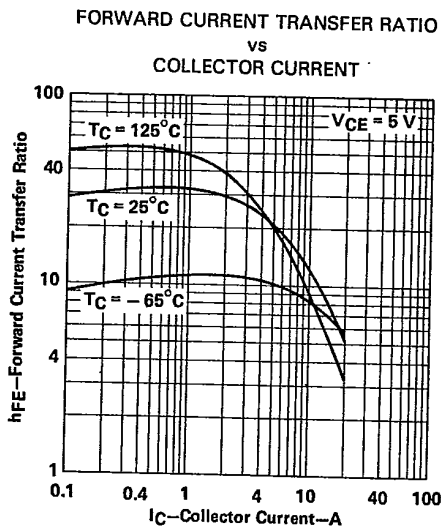


FIGURE 3

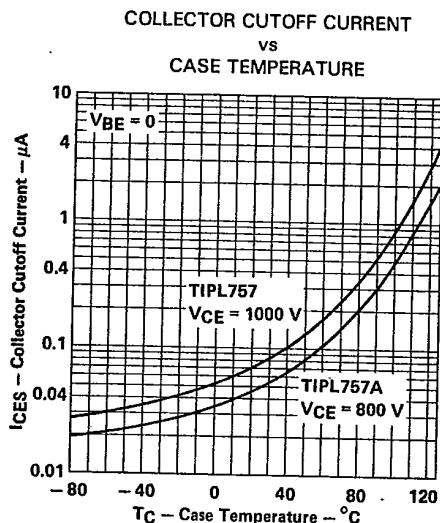


FIGURE 4

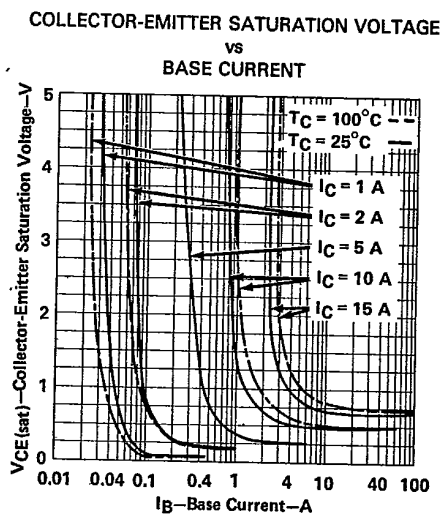


FIGURE 5

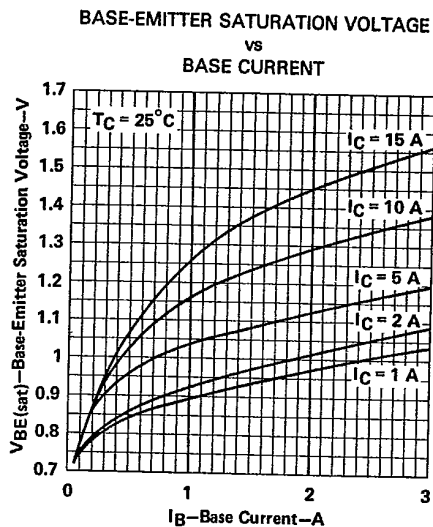


FIGURE 6

TIPL Devices



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TIPL757, TIPL757A
N-P-N SILICON POWER TRANSISTORS

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TYPICAL CHARACTERISTICS

RESISTIVE-LOAD
TURN-ON AND TURN-OFF TIMES
vs
COLLECTOR CURRENT

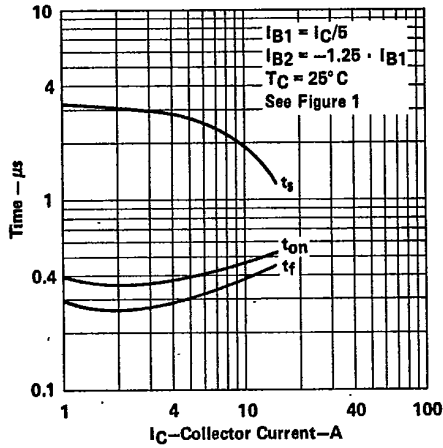


FIGURE 7

RESISTIVE-LOAD TURN-OFF TIME
vs
BASE CUTOFF CURRENT

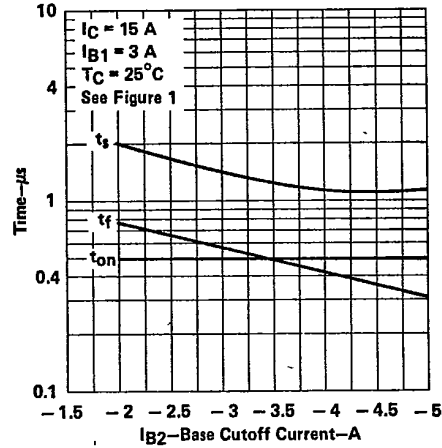


FIGURE 8

INDUCTIVE-LOAD TURN-OFF TIME
vs
BASE CUTOFF CURRENT

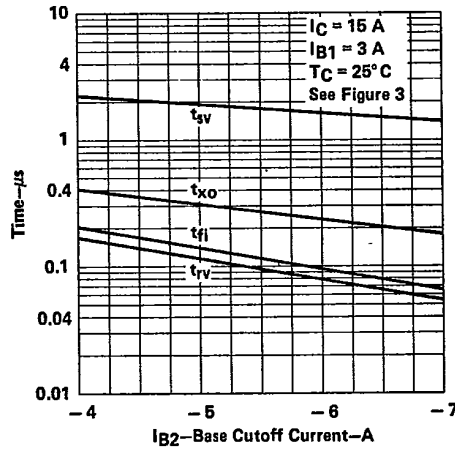


FIGURE 9

TIPL Devices



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62C 37050 D

TIPL757, TIPL757A
N-P-N SILICON POWER TRANSISTORS

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MAXIMUM SAFE OPERATING AREA

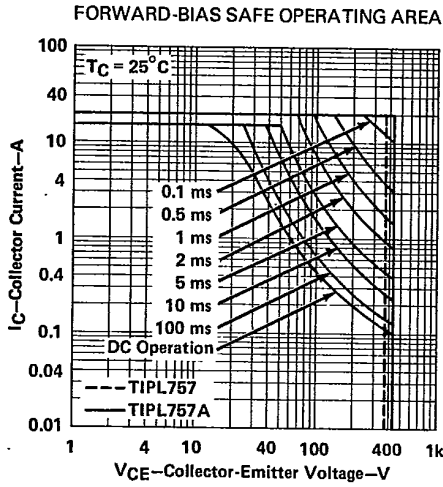


FIGURE 10

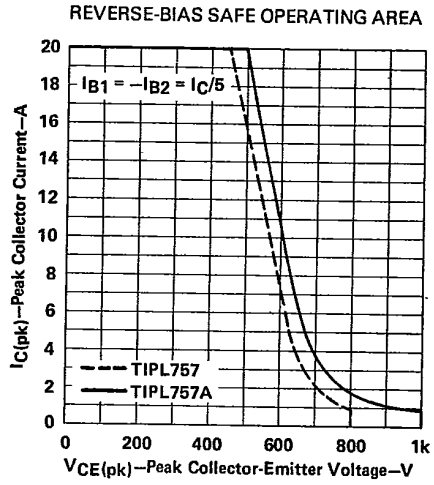


FIGURE 11

LIMITING CONDITIONS FOR POWER-DOWN TRANSIENT

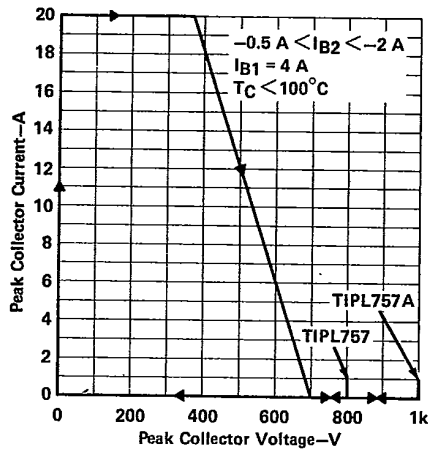


FIGURE 12

TIPL Devices



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62C 37051 D

TIPL757, TIPL757A
N-P-N SILICON POWER TRANSISTORS

T-33-15

MAXIMUM SAFE OPERATING AREA

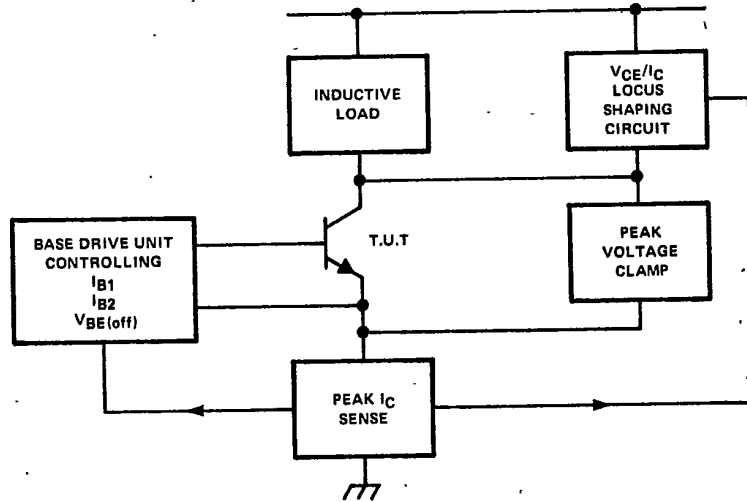


FIGURE 13. TEST CIRCUIT FOR LIMITING CONDITIONS FOR POWER-DOWN TRANSIENT

THERMAL INFORMATION
DISSIPATION DERATING CURVE

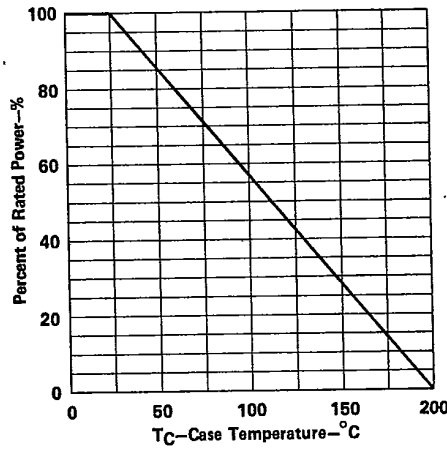


FIGURE 14

TIPL Devices



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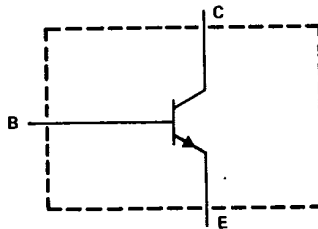
62C 37053 D

TIPL760, TIPL760A, TIPL761, TIPL761A
N-P-N SILICON POWER TRANSISTORS

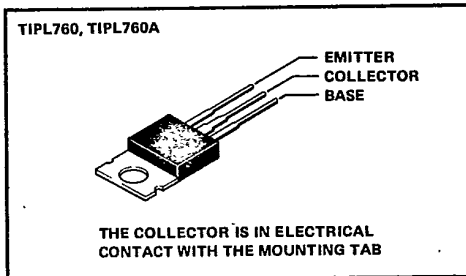
T-33-13
REVISED OCTOBER 1984

- 80 W/100 W at 25°C Case Temperature
- 4 A Continuous Collector Current
- Operation Characteristics Fully Guaranteed at 100°C
- Transient Power Dissipation Guaranteed at 100°C
- ICES < 100 μA at Max VCE at 100°C
- 1000 V Blocking Capability
- Designed for High-Voltage, Inductive-Load Switching Applications

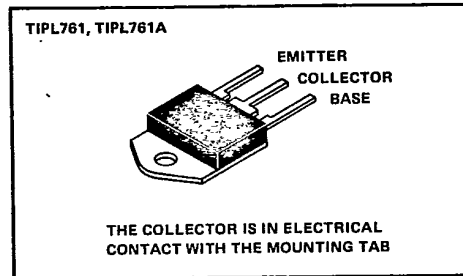
device schematic



TO-220AB PACKAGE



TO-218AA PACKAGE



TIPL Devices

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

	TIPL760	TIPL760A	TIPL761	TIPL761A
Collector-base voltage	800 V	1000 V	800 V	1000 V
Collector-emitter voltage (V _{BE} = 0)	800 V	1000 V	800 V	1000 V
Collector-emitter voltage (I _B = 0)	375 V	420 V	375 V	420 V
Continuous device dissipation at (or below) 25°C case temperature (see Figure 14)	80 W	80 W	100 W	100 W
Base-emitter voltage	10 V			
Continuous collector current	4 A			
Peak collector current (see Note 1)	8 A			
Operating junction and storage temperature range	- 65°C to 150°C			

NOTE 1: This value applies for, t_w < 10 ms, duty cycle < 2%.

8961726 TEXAS INSTR (OPTO)

62C 37054 D

TIPL760, TIPL760A, TIPL761, TIPL761A
N-P-N SILICON POWER TRANSISTORS

T-33-13

electrical characteristics at 25°C case temperature (unless otherwise noted)

PARAMETER	TEST CONDITIONS	TIPL760 TIPL761		TIPL760A TIPL761A		UNIT	
		MIN	TYP	MAX	MIN		TYP
V _{CEO(sus)}	I _C = 100 mA, L = 25 mH, See Note 2	375			420		V
I _{CEO}	V _{CE} = 375 V, I _B = 0		50				μA
	V _{CE} = 420 V, I _B = 0				50		
I _{CES}	V _{CE} = 800 V, V _{BE} = 0		50				μA
	V _{CE} = 800 V, V _{BE} = 0, T _C = 100°C		100				
	V _{CE} = 1000 V, V _{BE} = 0					50	
	V _{CE} = 1000 V, V _{BE} = 0, T _C = 100°C					100	
I _{EBO}	V _{EB} = 10 V, I _C = 0		1		1		mA
h _{FE}	V _{CE} = 5 V, I _C = 0.5 A, See Notes 3 and 4	20	60	20	60		
V _{CE(sat)}	I _C = 1 A, I _B = 0.2 A, See Notes 3 and 4		0.5		0.5		V
	I _C = 2.5 A, I _B = 0.5 A, See Notes 3 and 4		1		1		
	I _C = 4 A, I _B = 0.8 A, See Notes 3 and 4		2.5		2.5		
	I _C = 4 A, I _B = 0.8 A, T _C = 100°C, See Notes 3 and 4		5		5		
	I _C = 1 A, I _B = 0.2 A, See Notes 3 and 4		1		1		
V _{BE(sat)}	I _C = 2.5 A, I _B = 0.5 A, See Notes 3 and 4		1.2		1.2		V
	I _C = 4 A, I _B = 0.8 A, See Notes 3 and 4		1.4		1.4		
	I _C = 4 A, I _B = 0.8 A, T _C = 100°C, See Notes 3 and 4		1.3		1.3		
	I _C = 1 A, I _B = 0.2 A, See Notes 3 and 4		1		1		
f _T	V _{CE} = 10 V, I _C = 0.5 A, See Note 5	12		12			MHz
C _{obo}	V _{CB} = 20 V, I _E = 0, f = 0.1 MHz	110		110			pF

- NOTES: 2. Inductive loop switching measurement.
 3. These parameters must be measured using pulse techniques, $t_w = 300 \mu s$, duty cycle $\leq 2\%$.
 4. These parameters are measured with voltage-sensing contacts separate from the current-carrying contacts and located within 3.2 mm (0.125 inch) from the device body.
 5. To obtain f_T , the $|h_{fe}|$ response is extrapolated at the rate of -6 dB per octave from $f = 1$ MHz to the frequency at which $|h_{fe}| = 1$.

TIPL Devices



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62C 37055 D

TIPL760, TIPL760A, TIPL761, TIPL761A
N-P-N SILICON POWER TRANSISTORS

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thermal characteristics

PARAMETER	TIPL760 TIPL760A		TIPL761 TIPL761A		UNIT	
	MIN	TYP	MAX	MIN		TYP
$R_{\theta JC}$			1.56		1.25	°C/W

resistive-load switching characteristics

PARAMETER	TEST CONDITIONS			MIN	TYP	MAX	UNIT
t_{on}	$I_C = 4 A,$ $V_{CC} = 200 V,$	$I_{B1} = -I_{B2} = 0.8 A,$ See Figure 1	$T_C = 25^\circ C,$			0.56	μs
t_s						2.5	
t_f						0.5	
t_{on}	$I_C = 4 A,$ $V_{CC} = 200 V,$	$I_{B1} = -I_{B2} = 0.8 A,$ See Figure 1	$T_C = 100^\circ C,$			0.65	μs
t_s						3	
t_f						1	

inductive-load switching characteristics

PARAMETER	TEST CONDITIONS			MIN	TYP	MAX	UNIT
t_{sv}	$I_C = 4 A,$ $V_{BE(off)} = -5 V,$	$I_{B1} = 0.8 A,$ See Figure 2	$T_C = 25^\circ C,$			2.5	μs
t_{rv}						0.3	
t_{fi}						0.25	
t_{ti}						0.15	
t_{xo}						0.4	
t_{sv}	$I_C = 4 A,$ $V_{BE(off)} = -5 V,$	$I_{B1} = 0.8 A,$ See Figure 2	$T_C = 100^\circ C,$			3	μs
t_{rv}						0.5	
t_{fi}						0.25	
t_{ti}						0.15	
t_{xo}						0.75	

TIPL Devices



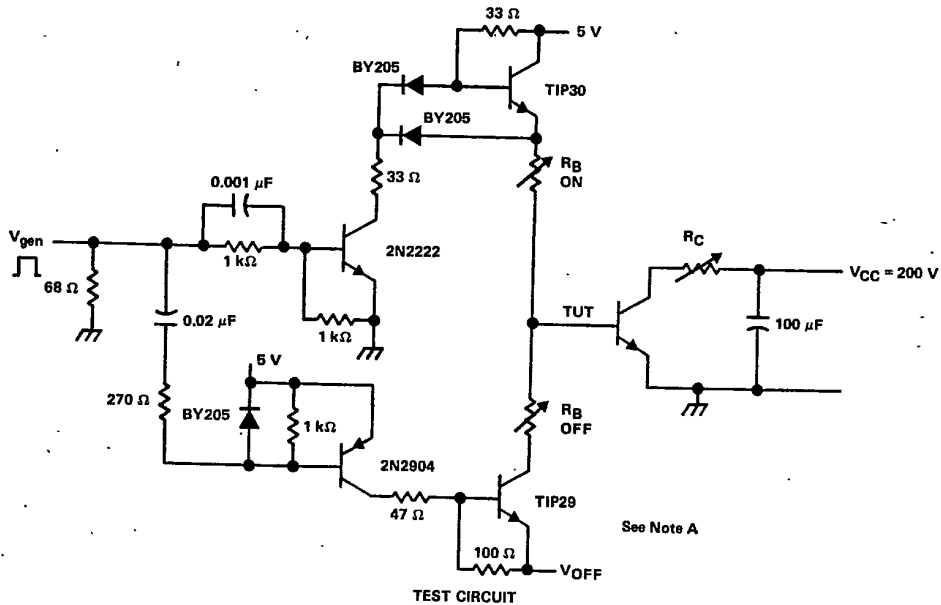
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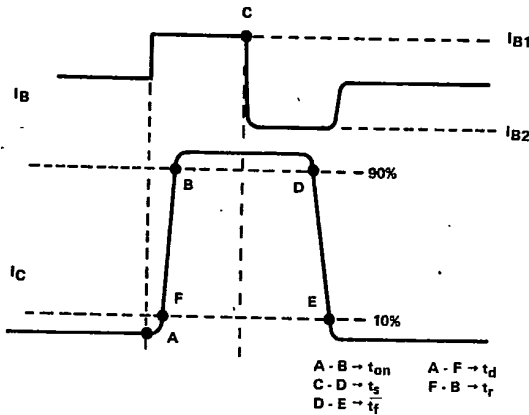
TIPL760, TIPL760A, TIPL761, TIPL761A
N-P-N SILICON POWER TRANSISTORS

T-33-13

PARAMETER MEASUREMENT INFORMATION



TIPL Devices



CURRENT WAVEFORMS

- NOTES: A. The V_{gen} waveform is supplied by the following characteristics: $t_r \leq 15$ ns, $t_f \leq 15$ ns, $Z_{out} = 50 \Omega$, $t_w = 20 \mu s$, duty cycle $\leq 2\%$.
 B. Waveforms are monitored on an oscilloscope with the following characteristics: $t_r \leq 15$ ns, $R_{in} \geq 10$ M Ω , $C_{in} \leq 11.5$ pF.
 C. Resistors must be noninductive types.

FIGURE 1. RESISTIVE-LOAD SWITCHING

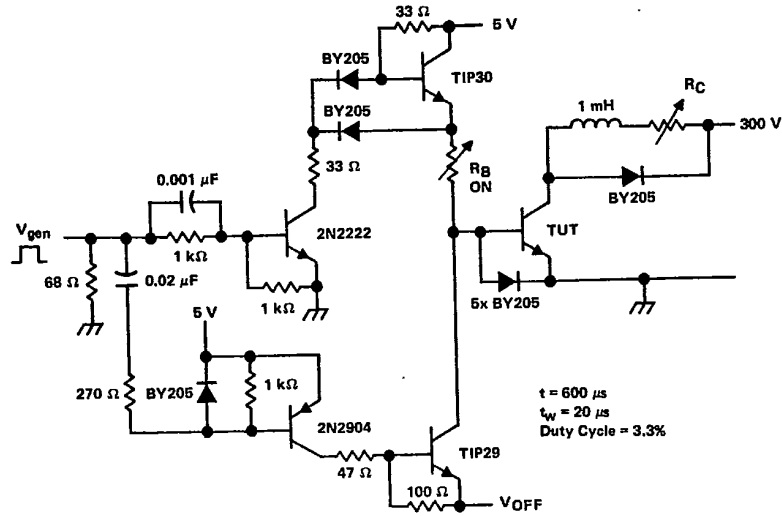
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N-P-N SILICON POWER TRANSISTORS

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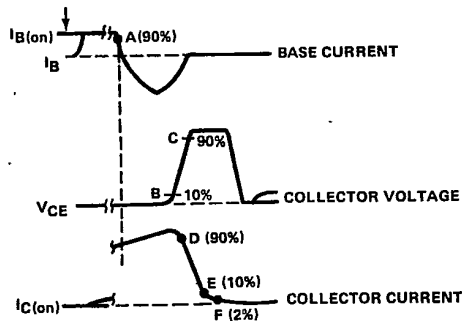
PARAMETER MEASUREMENT INFORMATION



$t = 600 \mu s$
 $t_w = 20 \mu s$
Duty Cycle = 3.3%

ADJUST R_C FOR REQUIRED I_C

TEST CIRCUIT



A-B = t_{sv}
A-C = t_{rv}
D-E = t_{fi}
E-F = t_{ti}
B-E = t_{xo}

VOLTAGE AND CURRENT WAVEFORMS

NOTES: A. Waveforms are monitored on an oscilloscope with the following characteristics: $t_r \leq 15 \text{ ns}$, $R_{in} \geq 10 \Omega$, $C_{in} \leq 11.5 \text{ pF}$.
B. Resistors must be noninductive types.

FIGURE 2. INDUCTIVE-LOAD SWITCHING

TIPL Devices



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TIPL760, TIPL760A, TIPL761, TIPL761A
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TYPICAL CHARACTERISTICS

RESISTIVE-LOAD TURN-ON TIME
vs
COLLECTOR CURRENT

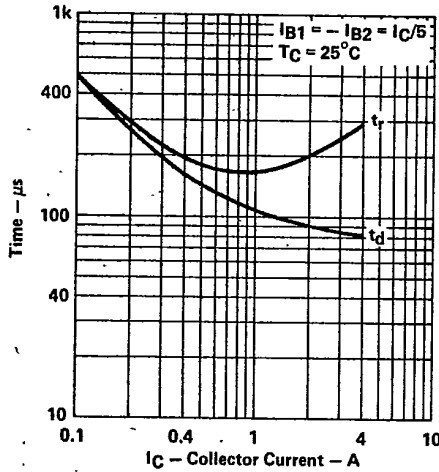


FIGURE 3

RESISTIVE-LOAD TURN-OFF TIME
vs
COLLECTOR CURRENT

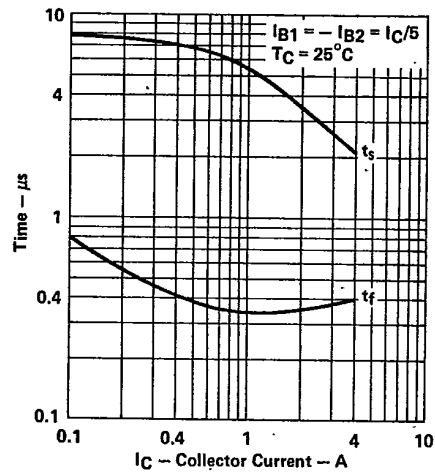


FIGURE 4

COLLECTOR-EMITTER SATURATION VOLTAGE
vs
BASE CURRENT

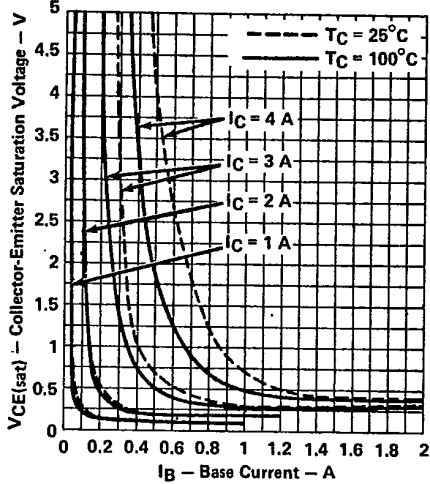


FIGURE 5

BASE-EMITTER SATURATION VOLTAGE
vs
BASE CURRENT

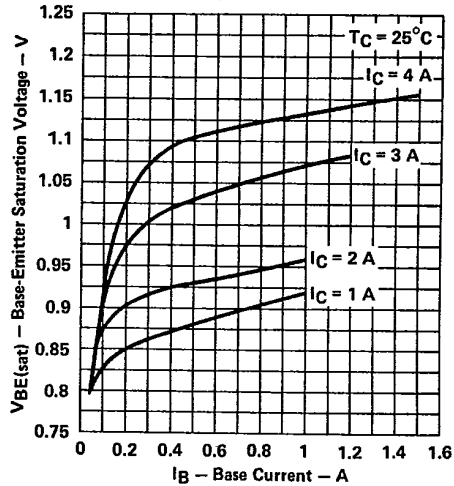


FIGURE 6

TIPL Devices



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N-P-N SILICON POWER TRANSISTORS

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TYPICAL CHARACTERISTICS

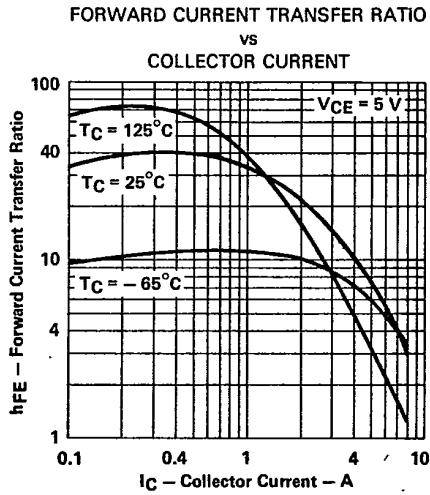


FIGURE 7

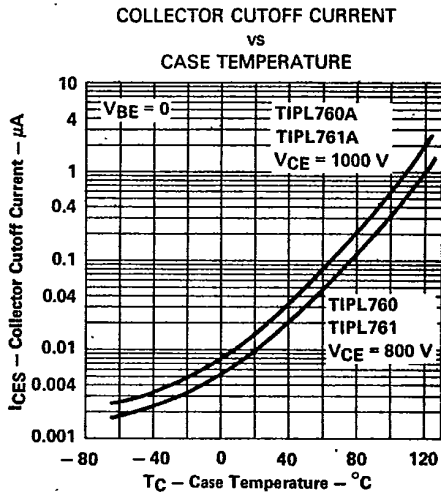


FIGURE 8

MAXIMUM SAFE OPERATING AREA

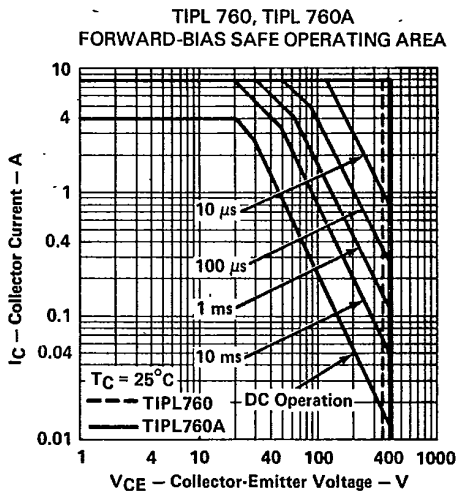


FIGURE 9

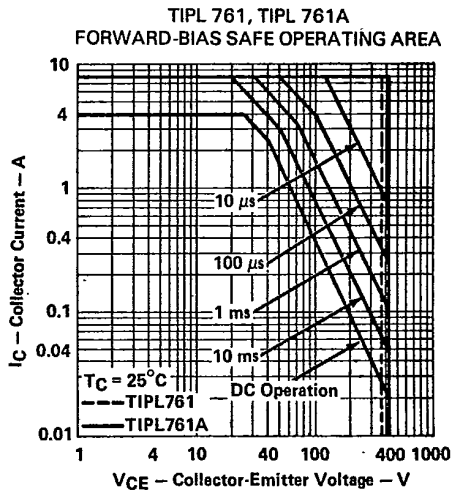


FIGURE 10

TIPL Devices



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TIPL760, TIPL760A, TIPL761, TIPL761A
N-P-N SILICON POWER TRANSISTORS

THERMAL INFORMATION

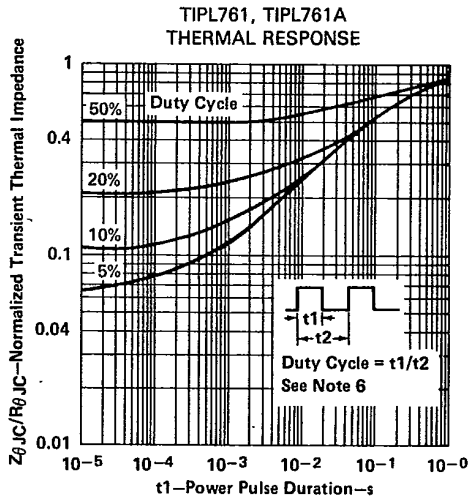


FIGURE 11

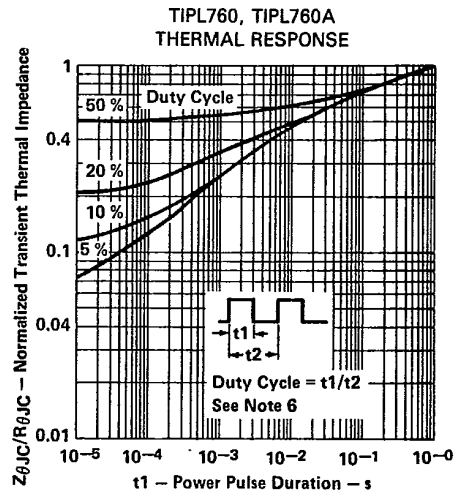


FIGURE 12

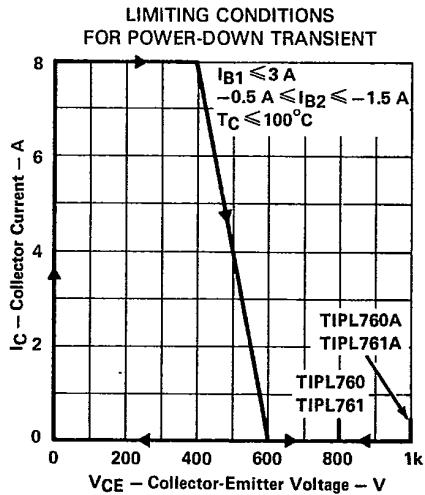


FIGURE 13

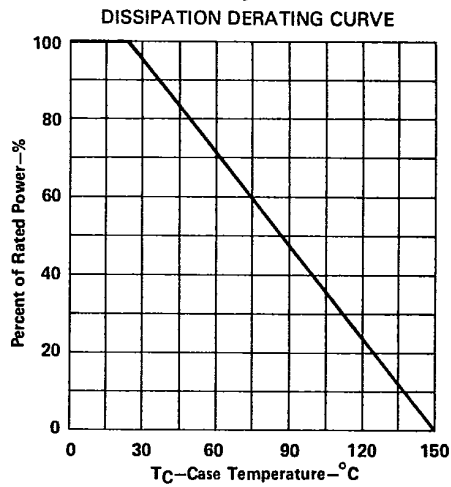


FIGURE 14

TIPL Devices



NOTE 6: Read time at end of t_1 , $T_J(\max) - T_C = P_{D(\text{peak})} \cdot \left(\frac{Z_{\theta JC}}{R_{\theta JC}} \right) \cdot R_{\theta JC(\max)}$.