

2N2222A

Small Signal Switching Transistor

NPN Silicon

Features

- MIL-PRF-19500/255 Qualified
- Available as JAN, JANTX, and JANTXV

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Value	Unit
Collector - Emitter Voltage	V_{CEO}	50	Vdc
Collector - Base Voltage	V_{CBO}	75	Vdc
Emitter - Base Voltage	V_{EBO}	6.0	Vdc
Collector Current - Continuous	I_C	800	mA dc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$	P_T	500	mW
Total Device Dissipation @ $T_C = 25^\circ\text{C}$	P_T	1.0	W
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-65 to +200	$^\circ\text{C}$

THERMAL CHARACTERISTICS

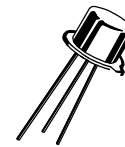
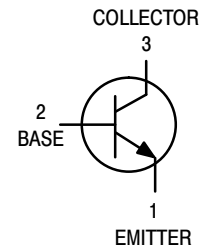
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	325	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case	$R_{\theta JC}$	150	$^\circ\text{C/W}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



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TO-18
CASE 206AA
STYLE 1

ORDERING INFORMATION

Device	Package	Shipping
JAN2N2222A	TO-18	Bulk
JANTX2N2222A		
JANTXV2N2222A		

2N2222A

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

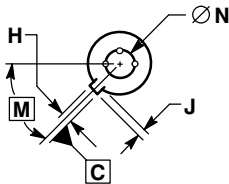
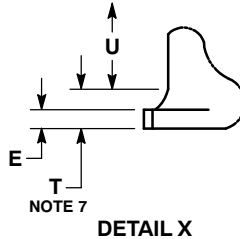
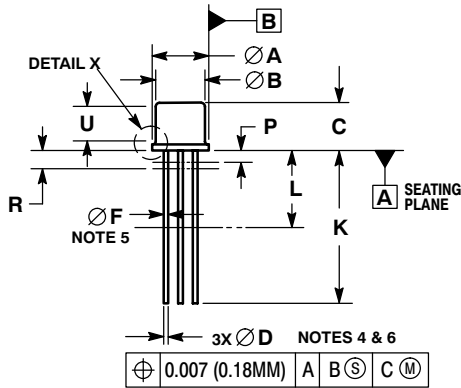
Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage (I _C = 10 mAdc)	V _{(BR)CEO}	50	–	Vdc
Collector–Base Cutoff Current (V _{CB} = 75 Vdc) (V _{CB} = 60 Vdc)	I _{CBO}	– –	10 10	μAdc nAdc
Emitter–Base Cutoff Current (V _{EB} = 6.0 Vdc) (V _{EB} = 4.0 Vdc)	I _{EBO}	– –	10 10	μAdc nAdc
Collector–Emitter Cutoff Current (V _{CE} = 50 Vdc)	I _{CES}	–	50	nAdc
ON CHARACTERISTICS (Note 1)				
DC Current Gain (I _C = 0.1 mAdc, V _{CE} = 10 Vdc) (I _C = 1.0 mAdc, V _{CE} = 10 Vdc) (I _C = 10 mAdc, V _{CE} = 10 Vdc) (I _C = 150 mAdc, V _{CE} = 10 Vdc) (I _C = 500 mAdc, V _{CE} = 10 Vdc)	h _{FE}	50 75 100 100 30	– 325 – 300 –	–
Collector – Emitter Saturation Voltage (I _C = 150 mAdc, I _B = 15 mAdc) (I _C = 500 mAdc, I _B = 50 mAdc)	V _{CE(sat)}	– –	0.3 1.0	Vdc
Base – Emitter Saturation Voltage (I _C = 150 mAdc, I _B = 15 mAdc) (I _C = 500 mAdc, I _B = 50 mAdc)	V _{BE(sat)}	0.6 –	1.2 2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Magnitude of Small-Signal Current Gain (I _C = 20 mAdc, V _{CE} = 20 Vdc, f = 100 MHz)	h _{fe}	2.5	–	–
Small-Signal Current Gain (I _C = 1.0 mAdc, V _{CE} = 10 Vdc, f = 1 kHz)	h _{fe}	50	–	–
Input Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, 100 kHz ≤ f ≤ 1.0 MHz)	C _{ibo}	–	25	pF
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, 100 kHz ≤ f ≤ 1.0 MHz)	C _{obo}	–	8.0	pF
SWITCHING (SATURATED) CHARACTERISTICS				
Turn–On Time (Reference Figure in MIL–PRF–19500/255)	t _{on}	–	35	ns
Turn–Off Time (Reference Figure in MIL–PRF–19500/255)	t _{off}	–	300	ns

1. Pulse Test: Pulse Width = 300 μs, Duty Cycle ≤ 2.0%.

2N2222A

PACKAGE DIMENSIONS

TO-18 3 CASE 206AA ISSUE A



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: INCHES.
3. DIMENSION J MEASURED FROM DIAMETER A TO EDGE.
4. LEAD TRUE POSITION TO BE DETERMINED AT THE GAUGE PLANE DEFINED BY DIMENSION R.
5. DIMENSION F APPLIES BETWEEN DIMENSION P AND L.
6. DIMENSION D APPLIES BETWEEN DIMENSION L AND K.
7. BODY CONTOUR OPTIONAL WITHIN ZONE DEFINED BY DIMENSIONS A, B, AND T.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	5.31	5.84	0.209	0.230
B	4.52	4.95	0.178	0.195
C	4.32	5.33	0.170	0.210
D	0.41	0.53	0.016	0.021
E	---	0.76	---	0.030
F	0.41	0.48	0.016	0.019
H	0.91	1.17	0.036	0.046
J	0.71	1.22	0.028	0.048
K	12.70	19.05	0.500	0.750
L	6.35	---	0.250	---
M	45° BSC		45° BSC	
N	2.54 BSC		0.100 BSC	
P	---	1.27	---	0.050
R	1.37 BSC		0.054 BSC	
T	---	0.76	---	0.030
U	2.54	---	0.100	---

STYLE 1:

1. PIN 1. EMITTER
2. BASE
3. COLLECTOR

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