Amplifier Transistors NPN Silicon

MAXIMUM RATINGS

Rating	Symbol	2N5088	2N5089	Unit	
Collector–Emitter Voltage	VCEO	30	25	Vdc	
Collector-Base Voltage	VCBO	35	30	Vdc	
Emitter-Base Voltage	VEBO	3.0		Vdc	
Collector Current — Continuous	IC	50		mAdc	
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	625 5.0		mW mW/°C	
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	1.5 12		Watts mW/°C	
Operating and Storage Junction Temperature Range	TJ, Tstg	-55 to +150		°C	

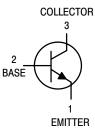
2N5088

2N5089

CASE 29-11, STYLE 1 TO-92 (TO-226AA)

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}^{(1)}$	200	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	83.3	°C/W



ELECTRICAL CHARACTERISTICS (T_A = 25° C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit	
OFF CHARACTERISTICS						
Collector–Emitter Breakdown Voltage(2) (I _C = 1.0 mAdc, I _B = 0)	2N5088 2N5089	V _(BR) CEO	30 25		Vdc	
Collector–Base Breakdown Voltage (I _C = 100 μAdc, I _E = 0)	2N5088 2N5089	V(BR)CBO	35 30		Vdc	
Collector Cutoff Current $(V_{CB} = 20 \text{ Vdc}, I_E = 0)$ $(V_{CB} = 15 \text{ Vdc}, I_E = 0)$	2N5088 2N5089	ICBO	_	50 50	nAdc	
Emitter Cutoff Current (VEB(off) = 3.0 Vdc, IC = 0) (VEB(off) = 4.5 Vdc, IC = 0)		IEBO		50 100	nAdc	

1. R_{0JA} is measured with the device soldered into a typical printed circuit board. 2. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%.

ELECTRICAL CHARACTERISTICS (T _A = 25°C unless otherwise noted) (Continued	I)
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Characteristic			Min	Max	Unit
ON CHARACTERISTICS					
DC Current Gain (I _C = 100 μAdc, V _{CE} = 5.0 Vdc)	2N5088 2N5089	hFE	300 400	900 1200	_
$(I_C = 1.0 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc})$	2N5088 2N5089		350 450	_ _	
$(I_C = 10 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc})^{(2)}$	2N5088 2N5089		300 400		
Collector–Emitter Saturation Voltage (I _C = 10 mAdc, I _B = 1.0 mAdc)		V _{CE(sat)}	_	0.5	Vdc
Base–Emitter On Voltage (I _C = 10 mAdc, V _{CE} = 5.0 Vdc) ⁽²⁾		V _{BE(on)}	_	0.8	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current–Gain — Bandwidth Product ($I_C = 500 \ \mu Adc$, $V_{CE} = 5.0 \ Vdc$, f = 20 MHz)		fT	50	—	MHz
Collector–Base Capacitance ($V_{CB} = 5.0 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz}$)		C _{cb}	—	4.0	pF
Emitter–Base Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, f = 1.0 MHz)		C _{eb}	—	10	pF
Small–Signal Current Gain (I _C = 1.0 mAdc, V _{CE} = 5.0 Vdc, f = 1.0 kHz)	2N5088 2N5089	h _{fe}	350 450	1400 1800	_
Noise Figure (I _C = 100 μ Adc, V _{CE} = 5.0 Vdc, R _S = 1.0 k Ω , f = 1.0 kHz)	2N5088 2N5089	NF		3.0 2.0	dB

2. Pulse Test: Pulse Width \leq 300 $\mu s,$ Duty Cycle \leq 2.0%.

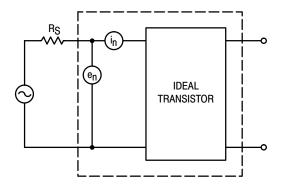
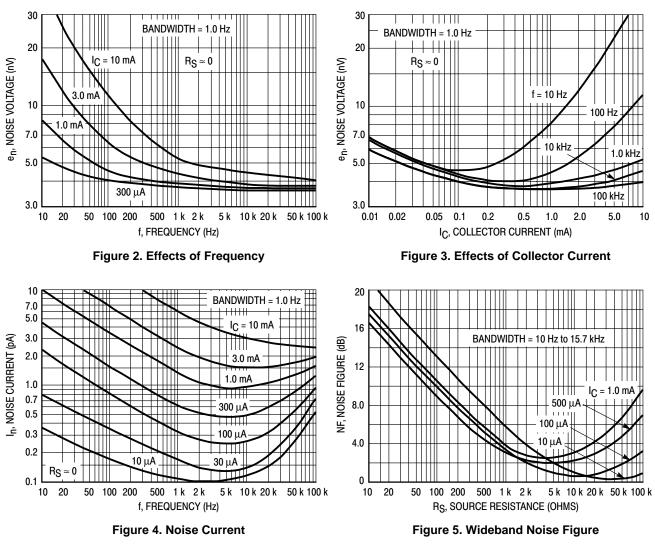


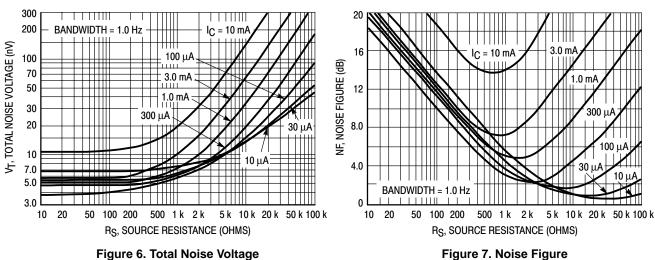
Figure 1. Transistor Noise Model

NOISE CHARACTERISTICS

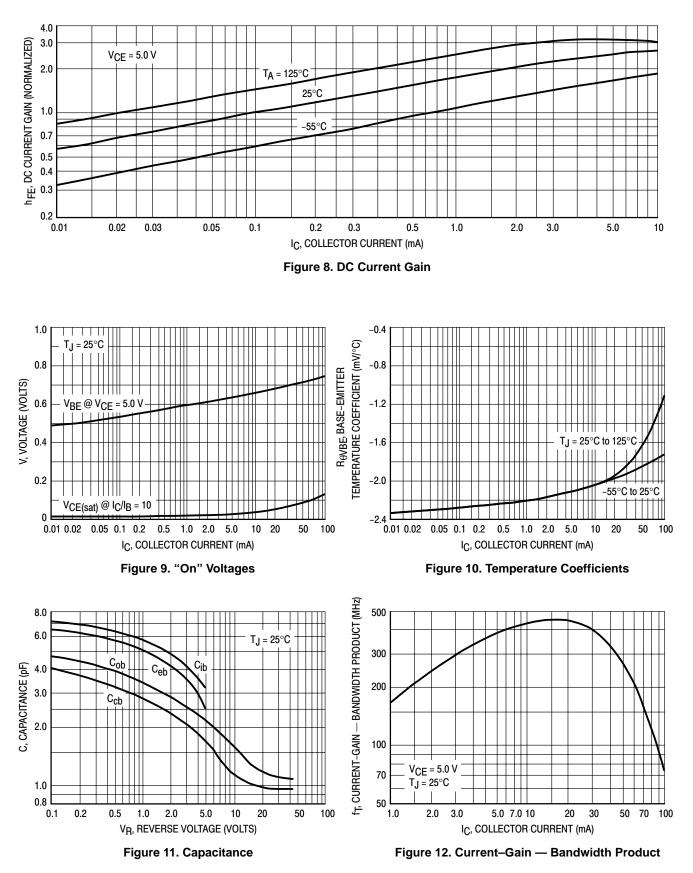
 $(V_{CE} = 5.0 \text{ Vdc}, \text{ T}_{A} = 25^{\circ}\text{C})$

NOISE VOLTAGE



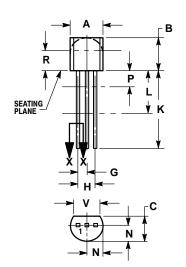


100 Hz NOISE DATA



PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AL







STYLE 1: PIN 1. EMITTER 2. BASE 3. COLLECTOR

NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
Ν	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	

<u>Notes</u>

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