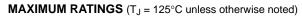
Preferred Device

Dual Series Schottky Barrier Diodes

These Schottky barrier diodes are designed for high speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand held and portable applications where space is limited.

Features

- Extremely Fast Switching Speed
- Low Forward Voltage -0.35 Volts (Typ) @ $I_F = 10$ mAdc
- Pb-Free Package is Available



| Rating | Symbol | Value | Unit |
|---|------------------|-------------|-------------|
| Reverse Voltage | V _R | 30 | V |
| Forward Power Dissipation @ T _A = 25°C Derate above 25°C | P _F | 200 1.6 | mW mW/°C |
| Forward Current (DC) | IF | 200 Max | mA |
| Junction Temperature | TJ | 125 Max | °C |
| Storage Temperature Range | T _{stg} | -55 to +150 | °C |

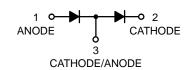
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



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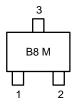
30 VOLT DUAL SERIES SCHOTTKY BARRIER DIODES





(SC-70) SOT-323 CASE 419 STYLE 9

MARKING DIAGRAM



B8 = Device Code M = Date Code

ORDERING INFORMATION

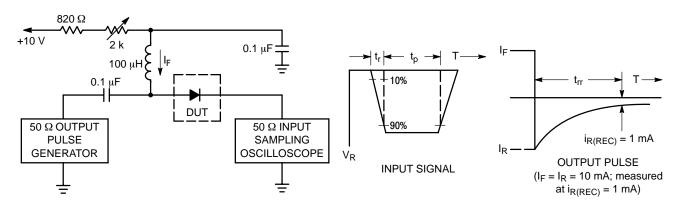
| Device | Package | Shipping [†] |
|------------|----------------------|-----------------------|
| BAT54SWT1 | SOT-323 | 3000/Tape & Reel |
| BAT54SWT1G | SOT-323 (Pb-Free) | 3000/Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (EACH DIODE)

| Characteristic | Symbol | Min | Тур | Max | Unit |
|--|--------------------|-----|------|------|------|
| Reverse Breakdown Voltage (I _R = 10 μA) | V _{(BR)R} | 30 | _ | _ | V |
| Total Capacitance (V _R = 1.0 V, f = 1.0 MHz) | C _T | _ | 7.6 | 10 | pF |
| Reverse Leakage (V _R = 25 V) | I _R | _ | 0.5 | 2.0 | μAdc |
| Forward Voltage (I _F = 0.1 mAdc) | V _F | _ | 0.22 | 0.24 | Vdc |
| Forward Voltage (I _F = 30 mAdc) | V _F | _ | 0.41 | 0.5 | Vdc |
| Forward Voltage (I _F = 100 mAdc) | V _F | _ | 0.52 | 0.8 | Vdc |
| Reverse Recovery Time $(I_F = I_R = 10 \text{ mAdc}, I_{R(REC)} = 1.0 \text{ mAdc}, Figure 1)$ | t _{rr} | _ | _ | 5.0 | ns |
| Forward Voltage (I _F = 1.0 mAdc) | V _F | _ | 0.29 | 0.32 | Vdc |
| Forward Voltage (I _F = 10 mAdc) | V _F | _ | 0.35 | 0.40 | Vdc |
| Forward Current (DC) | I _F | - | - | 200 | mAdc |
| Repetitive Peak Forward Current | I _{FRM} | - | _ | 300 | mAdc |
| Non-Repetitive Peak Forward Current (t < 1.0 s) | I _{FSM} | _ | _ | 600 | mAdc |



Notes: 1. A 2.0 $k\Omega$ variable resistor adjusted for a Forward Current (I_F) of 10 mA.

2. Input pulse is adjusted so $I_{R(peak)}$ is equal to 10 mA.

3. $t_p \gg t_{rr}$

Figure 1. Recovery Time Equivalent Test Circuit

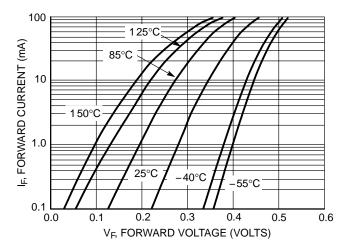


Figure 2. Forward Voltage

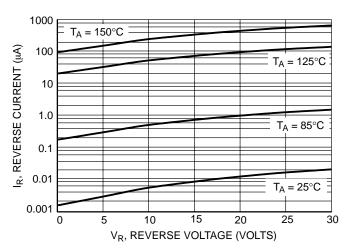


Figure 3. Leakage Current

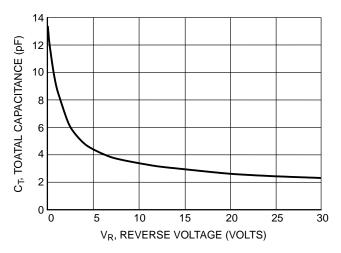
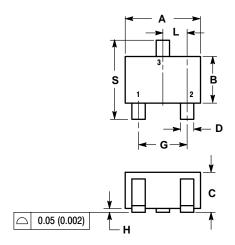
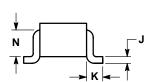


Figure 4. Total Capacitance

PACKAGE DIMENSIONS

(SC-70) SOT-323 PLASTIC PACKAGE CASE 419-04 ISSUE L





NOTES:

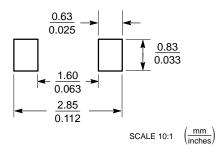
- DIMENSIONING AND TOLERANCING PER ANSI
 V14 5M 1982
- Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

| | INCHES | | MILLIMETERS | |
|-----|-----------|-------|-------------|------|
| DIM | MIN | MAX | MIN | MAX |
| Α | 0.071 | 0.087 | 1.80 | 2.20 |
| В | 0.045 | 0.053 | 1.15 | 1.35 |
| С | 0.032 | 0.040 | 0.80 | 1.00 |
| D | 0.012 | 0.016 | 0.30 | 0.40 |
| G | 0.047 | 0.055 | 1.20 | 1.40 |
| Н | 0.000 | 0.004 | 0.00 | 0.10 |
| J | 0.004 | 0.010 | 0.10 | 0.25 |
| K | 0.017 REF | | 0.425 REF | |
| L | 0.026 BSC | | 0.650 BSC | |
| N | 0.028 REF | | 0.700 REF | |
| S | 0.079 | 0.095 | 2.00 | 2.40 |

STYLE 9:

- PIN 1. ANODE 2. CATHODE
- 2. CATHODE 3. CATHODE-ANODE

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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