

# BC847ATT1, BC847BTT1, BC847CTT1

## General Purpose Transistors

### NPN Silicon

These transistors are designed for general purpose amplifier applications. They are housed in the SC-75/SOT-416 package which is designed for low power surface mount applications.

#### Features

- Pb-Free Packages are Available\*

#### MAXIMUM RATINGS (T<sub>A</sub> = 25°C)

Rating	Symbol	Max	Unit
Collector-Emitter Voltage	V <sub>CEO</sub>	45	V
Collector-Base Voltage	V <sub>CBO</sub>	50	V
Emitter-Base Voltage	V <sub>EBO</sub>	6.0	V
Collector Current – Continuous	I <sub>C</sub>	100	mAdc

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) T <sub>A</sub> = 25°C Derated above 25°C	P <sub>D</sub>	200	mW
Thermal Resistance, Junction-to-Ambient (Note 1)	R <sub>θJA</sub>	600	°C/W
Total Device Dissipation, FR-4 Board (Note 2) T <sub>A</sub> = 25°C Derated above 25°C	P <sub>D</sub>	300	mW
Thermal Resistance, Junction-to-Ambient (Note 2)	R <sub>θJA</sub>	400	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

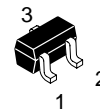
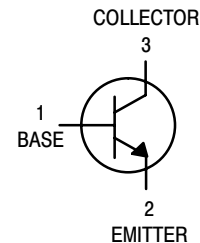
- FR-4 @ min pad.
- FR-4 @ 1.0 × 1.0 in pad.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



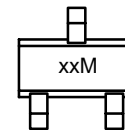
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CASE 463  
SC-75/SOT-41  
6  
STYLE 1

#### MARKING DIAGRAM



xx = Device Code  
M = Date Code

#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

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## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>					
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 10 mA)	BC847 Series V <sub>(BR)CEO</sub>	45	–	–	V
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 10 μA, V <sub>EB</sub> = 0)	BC847 Series V <sub>(BR)CES</sub>	50	–	–	V
Collector–Base Breakdown Voltage (I <sub>C</sub> = 10 μA)	BC847 Series V <sub>(BR)CBO</sub>	50	–	–	V
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 1.0 μA)	BC847 Series V <sub>(BR)EBO</sub>	6.0	–	–	V
Collector Cutoff Current (V <sub>CB</sub> = 30 V) (V <sub>CB</sub> = 30 V, T <sub>A</sub> = 150°C)	I <sub>CBO</sub>	–	–	15 5.0	nA μA
<b>ON CHARACTERISTICS</b>					
DC Current Gain (I <sub>C</sub> = 10 μA, V <sub>CE</sub> = 5.0 V)	BC847A BC847B BC847C	h <sub>FE</sub>	–	90 150 270	–
(I <sub>C</sub> = 2.0 mA, V <sub>CE</sub> = 5.0 V)	BC847A BC847B BC847C		110 200 420	180 290 520	220 450 800
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0.5 mA) (I <sub>C</sub> = 100 mA, I <sub>B</sub> = 5.0 mA)	V <sub>CE(sat)</sub>	–	–	0.25 0.6	V
Base–Emitter Saturation Voltage (I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0.5 mA) (I <sub>C</sub> = 100 mA, I <sub>B</sub> = 5.0 mA)	V <sub>BE(sat)</sub>	–	0.7 0.9	–	V
Base–Emitter Voltage (I <sub>C</sub> = 2.0 mA, V <sub>CE</sub> = 5.0 V) (I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 5.0 V)	V <sub>BE(on)</sub>	580	660	700 770	mV
<b>SMALL–SIGNAL CHARACTERISTICS</b>					
Current–Gain – Bandwidth Product (I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 5.0 Vdc, f = 100 MHz)	f <sub>T</sub>	100	–	–	MHz
Output Capacitance (V <sub>CB</sub> = 10 V, f = 1.0 MHz)	C <sub>obo</sub>	–	–	4.5	pF
Noise Figure (I <sub>C</sub> = 0.2 mA, V <sub>CE</sub> = 5.0 Vdc, R <sub>S</sub> = 2.0 kΩ, f = 1.0 kHz, BW = 200 Hz)	NF	–	–	10	dB

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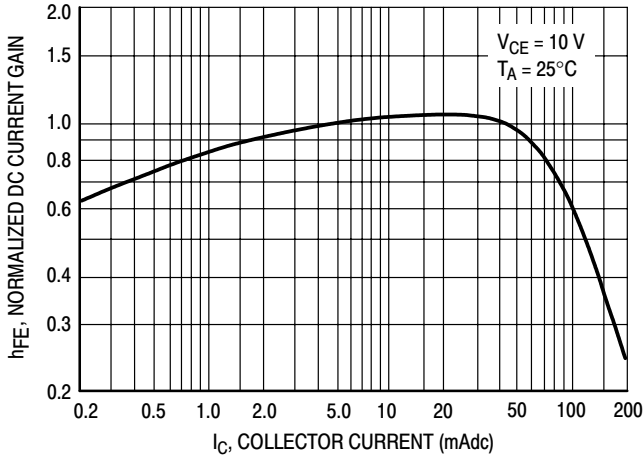


Figure 1. Normalized DC Current Gain

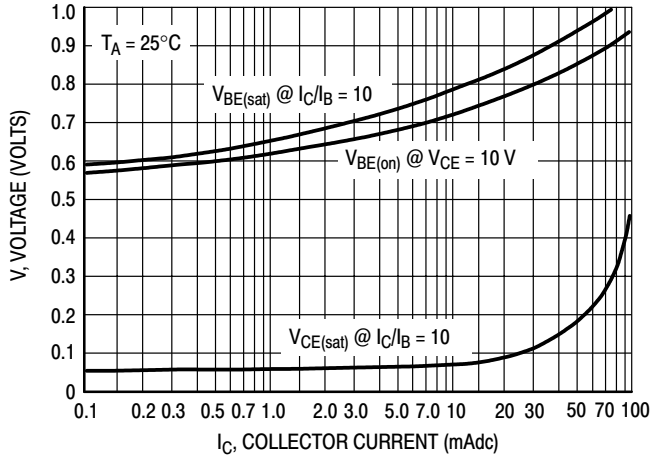


Figure 2. "Saturation" and "On" Voltages

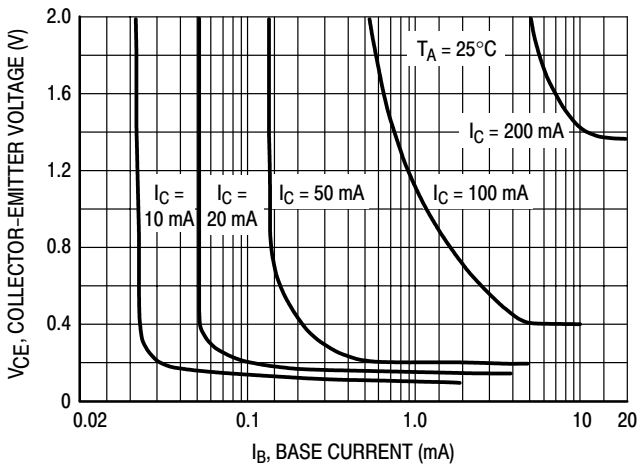


Figure 3. Collector Saturation Region

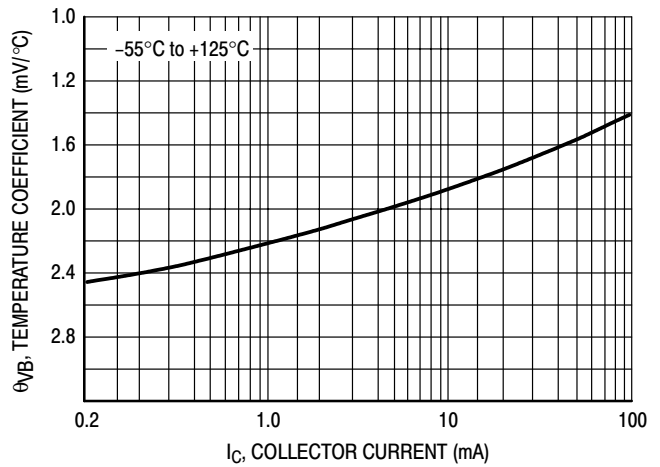


Figure 4. Base-Emitter Temperature Coefficient

# BC847ATT1, BC847BTT1, BC847CTT1

## BC847

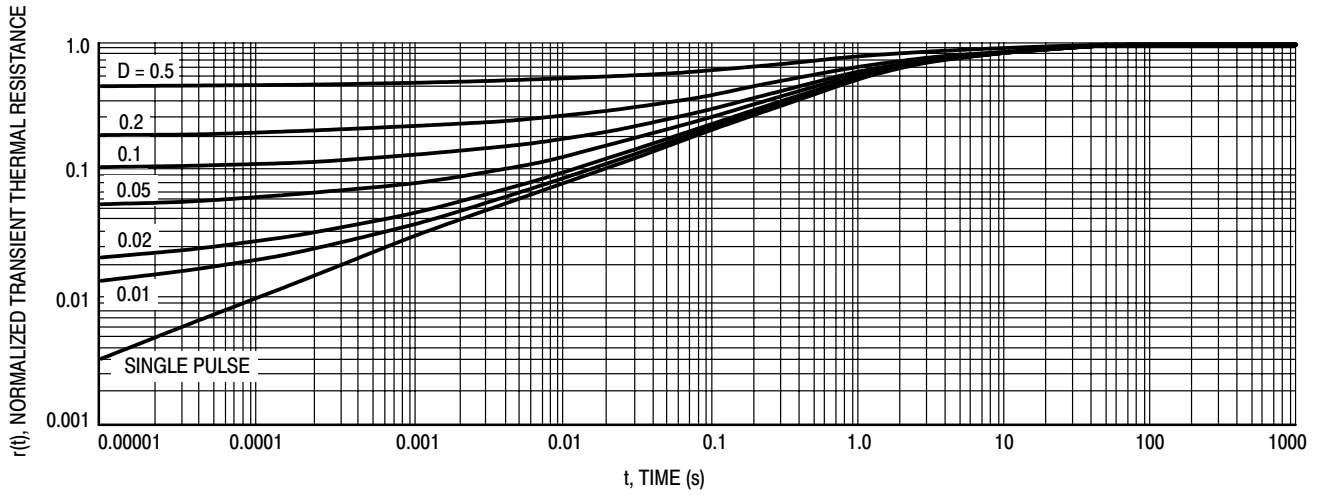


Figure 5. Normalized Thermal Response

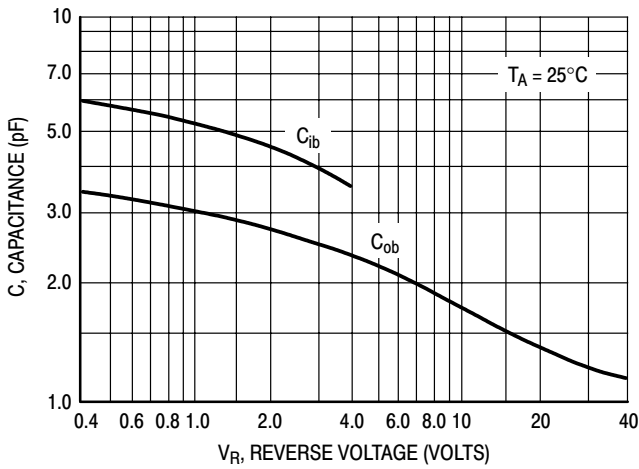


Figure 6. Capacitances

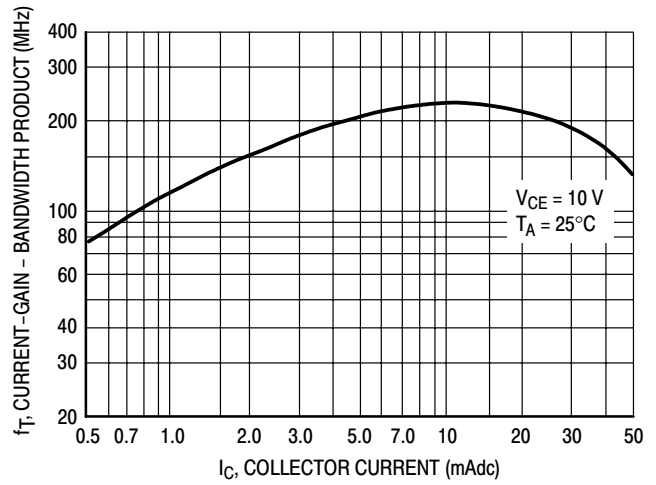


Figure 7. Current-Gain - Bandwidth Product

## BC847ATT1, BC847BTT1, BC847CTT1

### ORDERING INFORMATION

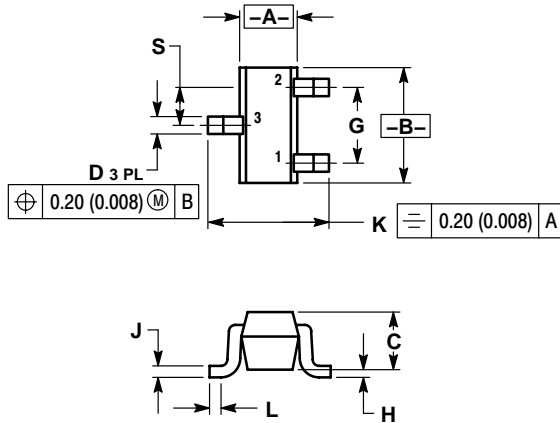
Device	Marking	Package	Shipping†
BC847ATT1	1E	SC-75/SOT-416	3,000 / Tape & Reel
BC847BTT1	1F	SC-75/SOT-416	3,000 / Tape & Reel
BC847BTT1G	1F	SC-75/SOT-416 (Pb-Free)	
BC847CTT1	1G	SC-75/SOT-416	3,000 / Tape & Reel
BC847CTT1G	1G	SC-75/SOT-416 (Pb-Free)	

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# BC847ATT1, BC847BTT1, BC847CTT1

## PACKAGE DIMENSIONS


SC-75/SOT-416  
CASE 463-01  
ISSUE C



### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.70	0.90	0.028	0.035
B	1.40	1.80	0.055	0.071
C	0.60	0.90	0.024	0.035
D	0.15	0.30	0.006	0.012
G	1.00 BSC		0.039 BSC	
H	---	0.10	---	0.004
J	0.10	0.25	0.004	0.010
K	1.45	1.75	0.057	0.069
L	0.10	0.20	0.004	0.008
S	0.50 BSC		0.020 BSC	

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