

# BSS138LT1

Preferred Device

## Power MOSFET 200 mAmps, 50 Volts N-Channel SOT-23

Typical applications are dc-dc converters, power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low Threshold Voltage ( $V_{GS(th)}$ : 0.5V...1.5V) makes it ideal for low voltage applications
- Miniature SOT-23 Surface Mount Package saves board space

### MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

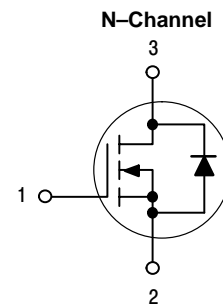
Rating	Symbol	Value	Unit
Drain-to-Source Voltage	$V_{DSS}$	50	Vdc
Gate-to-Source Voltage – Continuous	$V_{GS}$	$\pm 20$	Vdc
Drain Current – Continuous @ $T_A = 25^\circ\text{C}$ – Pulsed Drain Current ( $t_p \leq 10 \mu\text{s}$ )	$I_D$ $I_{DM}$	200 800	mA
Total Power Dissipation @ $T_A = 25^\circ\text{C}$	$P_D$	225	mW
Operating and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 150	$^\circ\text{C}$
Thermal Resistance – Junction-to-Ambient	$R_{\theta JA}$	556	$^\circ\text{C/W}$
Maximum Lead Temperature for Soldering Purposes, for 10 seconds	$T_L$	260	$^\circ\text{C}$



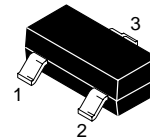
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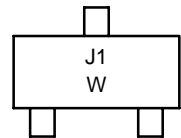
**200 mAmps**  
**50 VOLTS**  
 **$R_{DS(on)} = 3.5 \Omega$**



### MARKING DIAGRAM

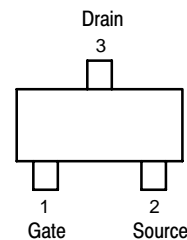


SOT-23  
CASE 318  
STYLE 21



W = Work Week

### PIN ASSIGNMENT



### ORDERING INFORMATION

Device	Package	Shipping
BSS138LT1	SOT-23	3000 Tape & Reel
BSS138LT3	SOT-23	10,000 Tape & Reel

Preferred devices are recommended choices for future use and best overall value.

# BSS138LT1

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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### OFF CHARACTERISTICS

Drain-to-Source Breakdown Voltage ( $V_{GS} = 0\text{ Vdc}$ , $I_D = 250\ \mu\text{Adc}$ )	$V_{(BR)DSS}$	50	–	–	Vdc
Zero Gate Voltage Drain Current ( $V_{DS} = 25\text{ Vdc}$ , $V_{GS} = 0\text{ Vdc}$ ) ( $V_{DS} = 50\text{ Vdc}$ , $V_{GS} = 0\text{ Vdc}$ )	$I_{DSS}$	– –	– –	0.1 0.5	$\mu\text{Adc}$
Gate-Source Leakage Current ( $V_{GS} = \pm 20\text{ Vdc}$ , $V_{DS} = 0\text{ Vdc}$ )	$I_{GSS}$	–	–	$\pm 0.1$	$\mu\text{Adc}$

### ON CHARACTERISTICS (Note 1.)

Gate-Source Threshold Voltage ( $V_{DS} = V_{GS}$ , $I_D = 1.0\text{ mAdc}$ )	$V_{GS(th)}$	0.5	–	1.5	Vdc
Static Drain-to-Source On-Resistance ( $V_{GS} = 2.75\text{ Vdc}$ , $I_D < 200\text{ mAdc}$ , $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$ ) ( $V_{GS} = 5.0\text{ Vdc}$ , $I_D = 200\text{ mAdc}$ )	$r_{DS(on)}$	– –	5.6 –	10 3.5	Ohms
Forward Transconductance ( $V_{DS} = 25\text{ Vdc}$ , $I_D = 200\text{ mAdc}$ , $f = 1.0\text{ kHz}$ )	$g_{fs}$	100	–	–	mmhos

### DYNAMIC CHARACTERISTICS

Input Capacitance	( $V_{DS} = 25\text{ Vdc}$ , $V_{GS} = 0$ , $f = 1\text{ MHz}$ )	$C_{iss}$	–	40	50	$\text{pF}$
Output Capacitance	( $V_{DS} = 25\text{ Vdc}$ , $V_{GS} = 0$ , $f = 1\text{ MHz}$ )	$C_{oss}$	–	12	25	
Transfer Capacitance	( $V_{DG} = 25\text{ Vdc}$ , $V_{GS} = 0$ , $f = 1\text{ MHz}$ )	$C_{rss}$	–	3.5	5.0	

### SWITCHING CHARACTERISTICS (Note 2.)

Turn-On Delay Time	( $V_{DD} = 30\text{ Vdc}$ , $I_D = 0.2\text{ Adc}$ ,)	$t_{d(on)}$	–	–	20	ns
Turn-Off Delay Time		$t_{d(off)}$	–	–	20	

1. Pulse Test: Pulse Width  $\leq 300\ \mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
2. Switching characteristics are independent of operating junction temperature.

# BSS138LT1

## TYPICAL ELECTRICAL CHARACTERISTICS

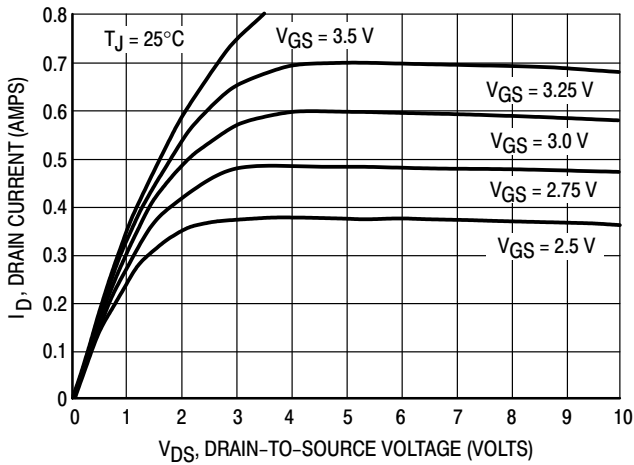


Figure 1. On-Region Characteristics

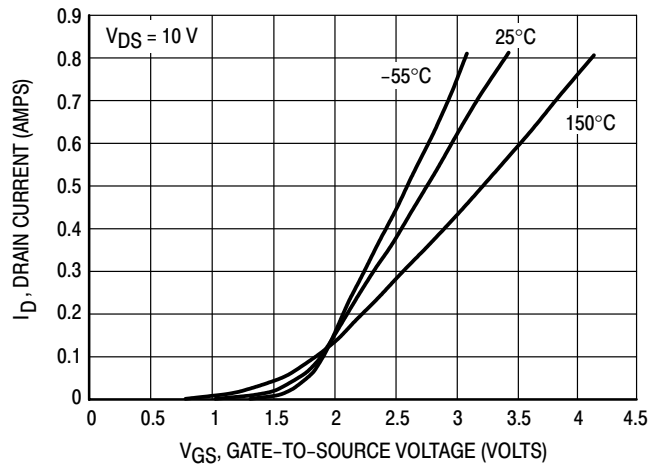


Figure 2. Transfer Characteristics

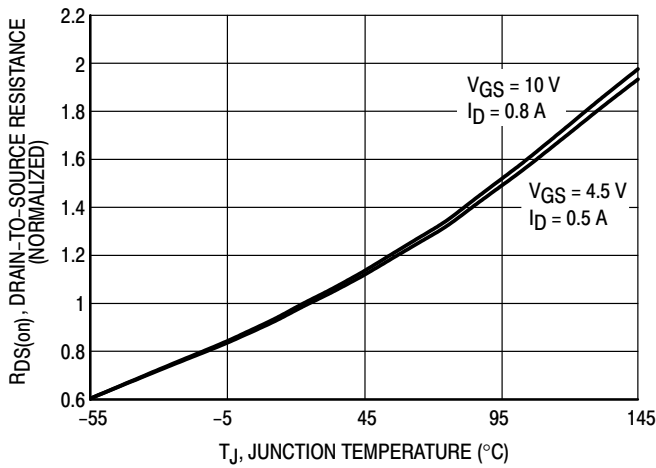


Figure 3. On-Resistance Variation with Temperature

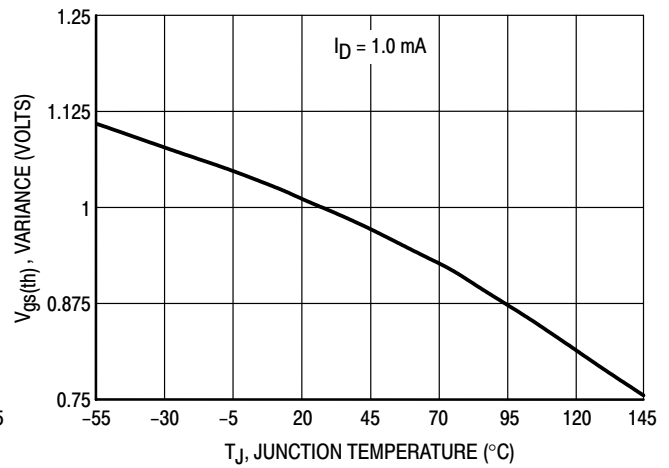


Figure 4. Threshold Voltage Variation with Temperature

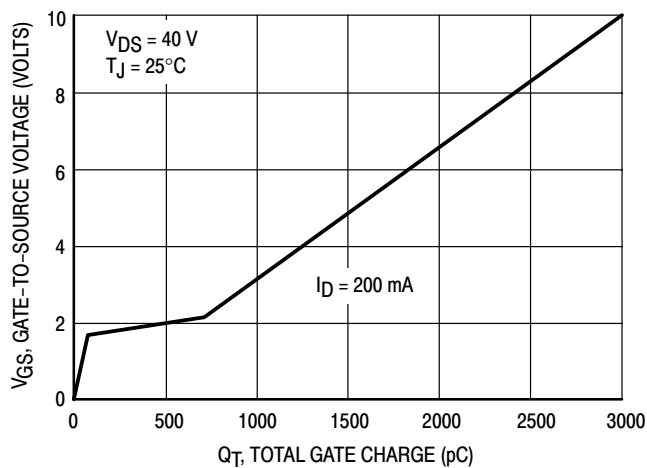


Figure 5. Gate Charge

TYPICAL ELECTRICAL CHARACTERISTICS

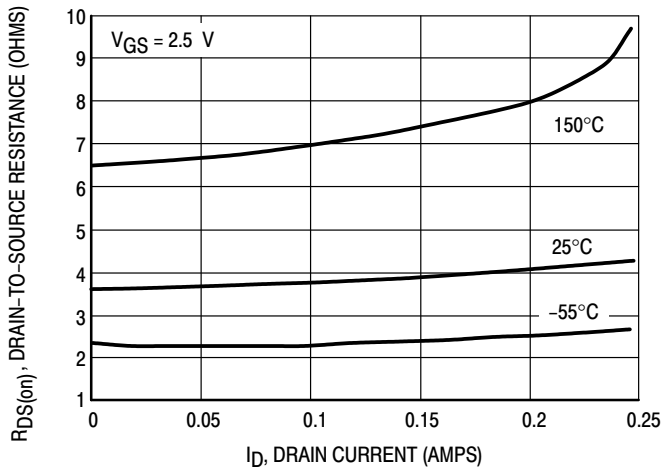


Figure 6. On-Resistance versus Drain Current

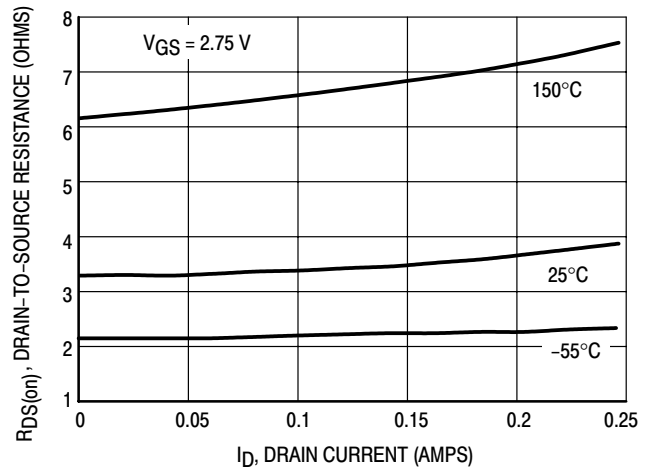


Figure 7. On-Resistance versus Drain Current

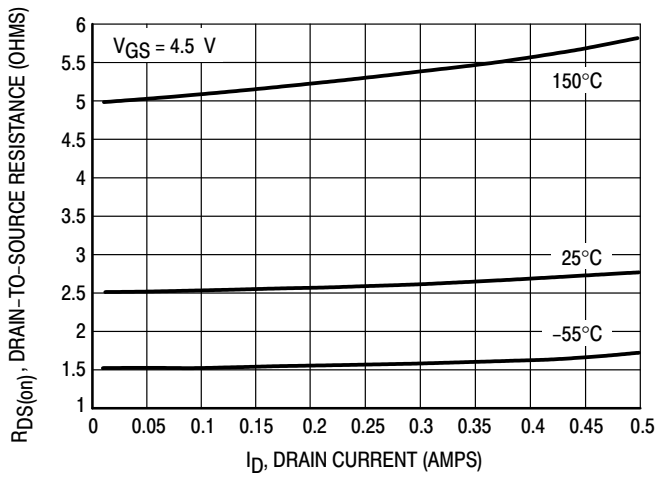


Figure 8. On-Resistance versus Drain Current

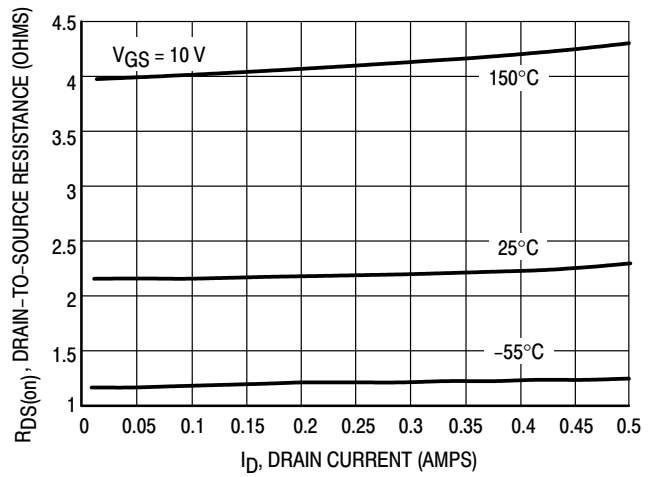


Figure 9. On-Resistance versus Drain Current

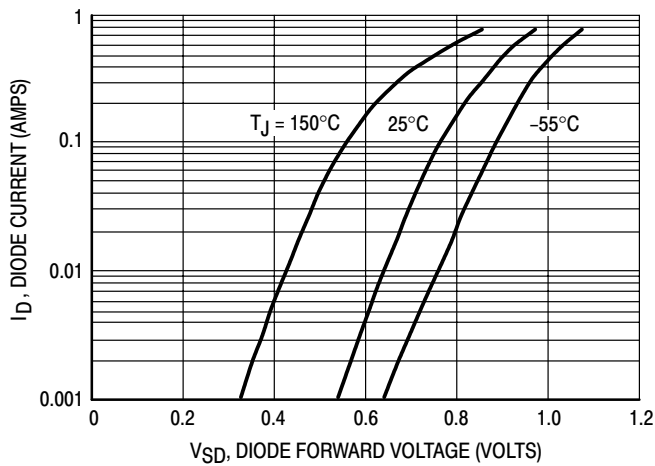


Figure 10. Body Diode Forward Voltage

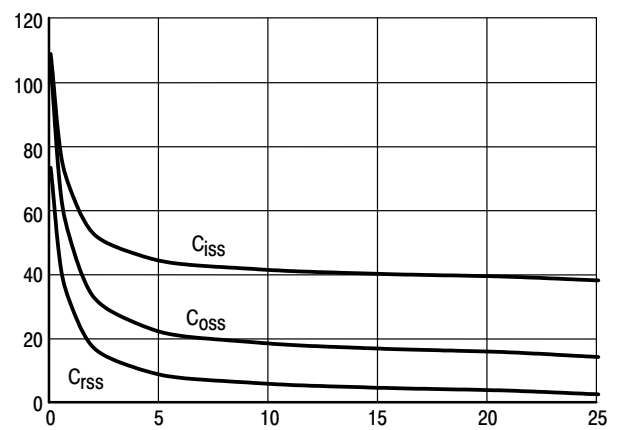


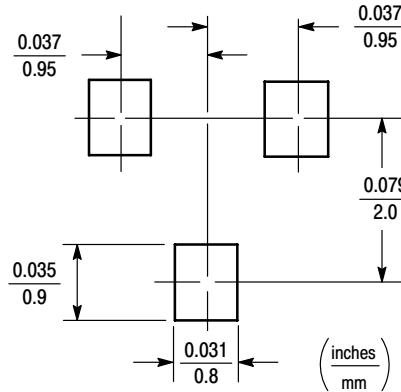
Figure 11. Capacitance

**INFORMATION FOR USING THE SOT-23 SURFACE MOUNT PACKAGE**

**MINIMUM RECOMMENDED FOOTPRINT FOR SURFACE MOUNTED APPLICATIONS**

Surface mount board layout is a critical portion of the total design. The footprint for the semiconductor packages must be the correct size to insure proper solder connection

interface between the board and the package. With the correct pad geometry, the packages will self align when subjected to a solder reflow process.



**SOT-23 POWER DISSIPATION**

The power dissipation of the SOT-23 is a function of the drain pad size. This can vary from the minimum pad size for soldering to a pad size given for maximum power dissipation. Power dissipation for a surface mount device is determined by  $T_{J(max)}$ , the maximum rated junction temperature of the die,  $R_{\theta JA}$ , the thermal resistance from the device junction to ambient, and the operating temperature,  $T_A$ . Using the values provided on the data sheet for the SOT-23 package,  $P_D$  can be calculated as follows:

$$P_D = \frac{T_{J(max)} - T_A}{R_{\theta JA}}$$

The values for the equation are found in the maximum ratings table on the data sheet. Substituting these values into the equation for an ambient temperature  $T_A$  of 25°C,

one can calculate the power dissipation of the device which in this case is 225 milliwatts.

$$P_D = \frac{150^\circ\text{C} - 25^\circ\text{C}}{556^\circ\text{C/W}} = 225 \text{ milliwatts}$$

The 556°C/W for the SOT-23 package assumes the use of the recommended footprint on a glass epoxy printed circuit board to achieve a power dissipation of 225 milliwatts. There are other alternatives to achieving higher power dissipation from the SOT-23 package. Another alternative would be to use a ceramic substrate or an aluminum core board such as Thermal Clad™. Using a board material such as Thermal Clad, an aluminum core board, the power dissipation can be doubled using the same footprint.

**SOLDERING PRECAUTIONS**

The melting temperature of solder is higher than the rated temperature of the device. When the entire device is heated to a high temperature, failure to complete soldering within a short time could result in device failure. Therefore, the following items should always be observed in order to minimize the thermal stress to which the devices are subjected.

- Always preheat the device.
- The delta temperature between the preheat and soldering should be 100°C or less.\*
- When preheating and soldering, the temperature of the leads and the case must not exceed the maximum temperature ratings as shown on the data sheet. When using infrared heating with the reflow soldering method, the difference shall be a maximum of 10°C.

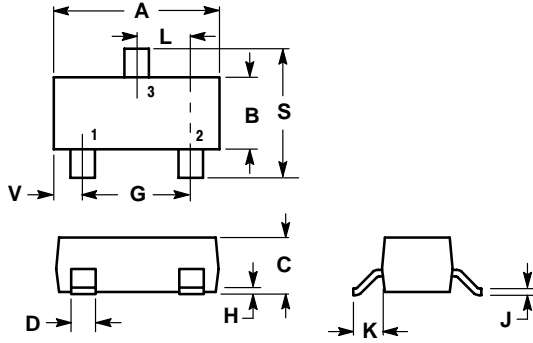
- The soldering temperature and time shall not exceed 260°C for more than 10 seconds.
- When shifting from preheating to soldering, the maximum temperature gradient shall be 5°C or less.
- After soldering has been completed, the device should be allowed to cool naturally for at least three minutes. Gradual cooling should be used as the use of forced cooling will increase the temperature gradient and result in latent failure due to mechanical stress.
- Mechanical stress or shock should not be applied during cooling.

\* Soldering a device without preheating can cause excessive thermal shock and stress which can result in damage to the device.

# BSS138LT1

## PACKAGE DIMENSIONS

SOT-23 (TO-236)  
CASE 318-08  
ISSUE AF



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.


DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.1102	0.1197	2.80	3.04
B	0.0472	0.0551	1.20	1.40
C	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
H	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60

STYLE 21:

- PIN 1. GATE  
2. SOURCE  
3. DRAIN

**Notes**

Thermal Clad is a registered trademark of the Bergquist Company.

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