

MBR120VLSFT1

Surface Mount Schottky Power Rectifier

Plastic SOD-123 Package

... using the Schottky Barrier principle with a large area metal-to-silicon power diode. Ideally suited for low voltage, high frequency rectification or as free wheeling and polarity protection diodes in surface mount applications where compact size and weight are critical to the system. This package also provides an easy to work with alternative to leadless 34 package style. Because of its small size, it is ideal for use in portable and battery powered products such as cellular and cordless phones, chargers, notebook computers, printers, PDAs and PCMCIA cards. Typical applications are ac/dc and dc-dc converters, reverse battery protection, and "Oring" of multiple supply voltages and any other application where performance and size are critical. These state-of-the-art devices have the following features:

- Guardring for Stress Protection
- Optimized for Very Low Forward Voltage
- 125°C Operating Junction Temperature
- Epoxy Meets UL94, VO at 1/8"
- Package Designed for Optimal Automated Board Assembly
- ESD Ratings: Machine Model, C;
Human Body Model, 3B

Mechanical Characteristics

- Reel Options: MBR120VLSFT1 = 3,000 per 7" reel/8 mm tape
MBR120VLSFT3 = 10,000 per 13" reel/8 mm tape
- Device Marking: L2V
- Polarity Designator: Cathode Band
- Weight: 11.7 mg (approximately)
- Case: Epoxy, Molded
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Device Meets MSL 1 Requirements

MAXIMUM RATINGS

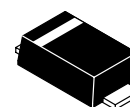
Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	20	V
Average Rectified Forward Current (Rated V_R) $T_L = 119^\circ\text{C}$	$I_{F(AV)}$	1.0	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions, Halfwave, Single Phase, 60 Hz)	I_{FSM}	45	A
Storage Temperature Range	T_{stg}	-65 to +125	°C
Operating Junction Temperature	T_J	-65 to +125	°C
Voltage Rate of Change (Rated V_R)	dv/dt	1000	V/ μs



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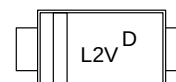
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SCHOTTKY BARRIER RECTIFIER 1.0 AMPERES 20 VOLTS



SOD-123FL
CASE 498
PLASTIC

DEVICE MARKING



L2V = Specific Device Code
D = Date Code

ORDERING INFORMATION

Device	Package	Shipping
MBR120VLSFT1	SOD-123FL	3000/Tape & Reel
MBR120VLSFT3	SOD-123FL	10,000/Tape & Reel

MBR120VLSFT1

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance - Junction-to-Lead (Note 1)	$R_{\theta j l}$	26	$^{\circ}\text{C}/\text{W}$
Thermal Resistance - Junction-to-Lead (Note 2)	$R_{\theta j l}$	21	
Thermal Resistance - Junction-to-Ambient (Note 1)	$R_{\theta j a}$	325	
Thermal Resistance - Junction-to-Ambient (Note 2)	$R_{\theta j a}$	82	

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	$T_J = 25^{\circ}\text{C}$	$T_J = 85^{\circ}\text{C}$	Unit
Maximum Instantaneous Forward Voltage (Note 3) ($I_F = 0.1\text{ A}$) ($I_F = 0.5\text{ A}$) ($I_F = 1.0\text{ A}$)	V_F	0.275 0.315 0.340	0.205 0.270 0.300	V
Maximum Instantaneous Reverse Current (Note 3) (Rated dc Voltage)	I_R	0.60	15	mA

1. Mounted with minimum recommended pad size, PC Board FR4.
2. Mounted with 1 in. copper pad (Cu area 700 mm²).
3. Pulse Test: Pulse Width = 300 μs , Duty Cycle $\leq 2\%$.

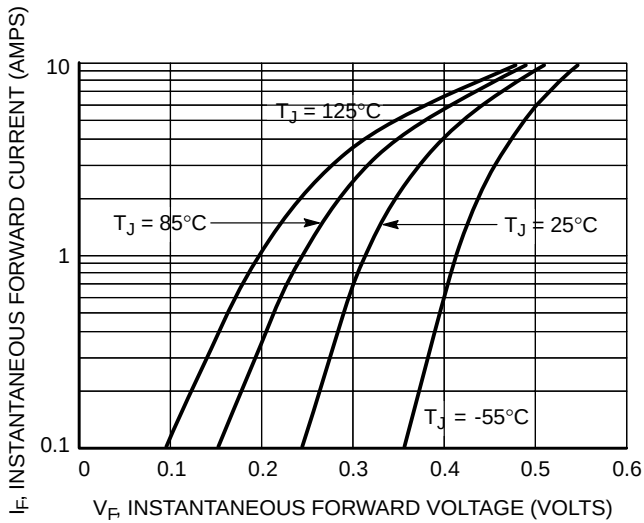


Figure 1. Typical Forward Voltage

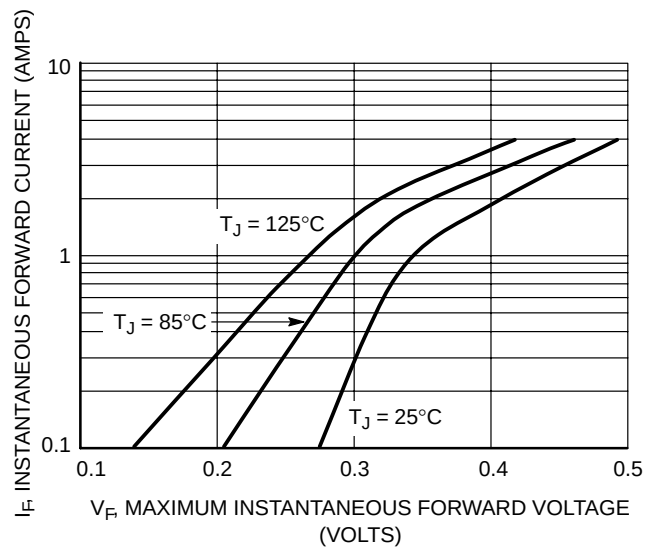


Figure 2. Maximum Forward Voltage

MBR120VLSFT1

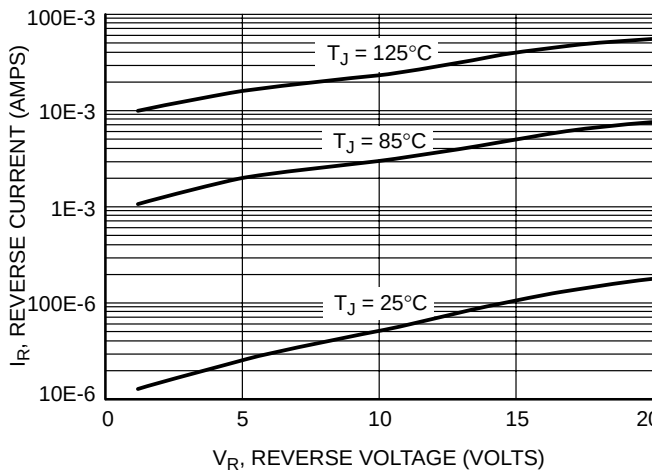


Figure 3. Typical Reverse Current

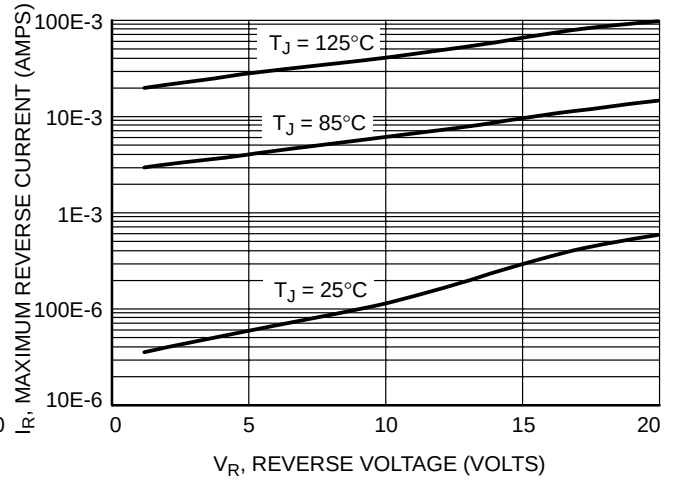


Figure 4. Maximum Reverse Current

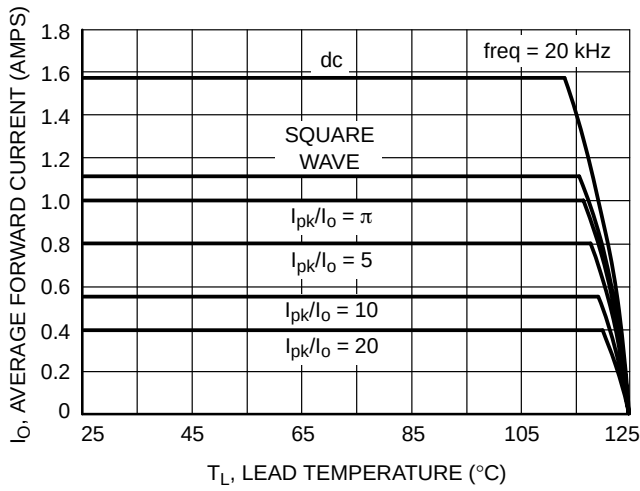


Figure 5. Current Derating

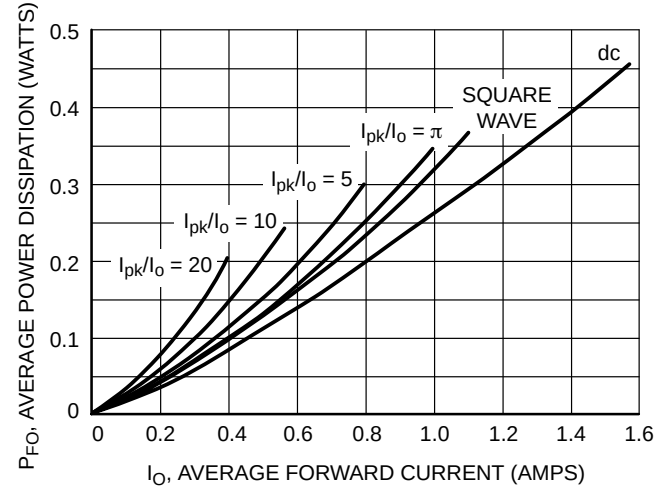


Figure 6. Forward Power Dissipation

MBR120VLSFT1

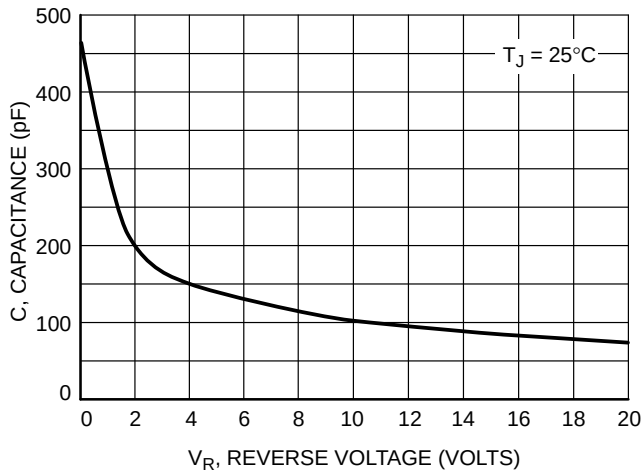


Figure 7. Capacitance

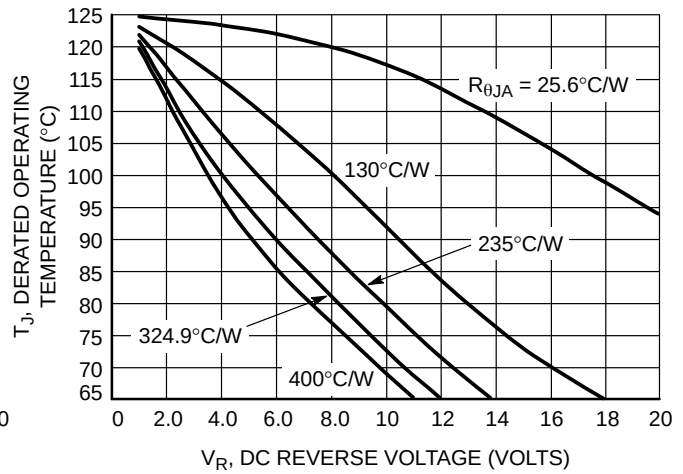


Figure 8. Typical Operating Temperature Derating*

* Reverse power dissipation and the possibility of thermal runaway must be considered when operating this device under any reverse voltage conditions. Calculations of T_J therefore must include forward and reverse power effects. The allowable operating T_J may be calculated from the equation:

$$T_J = T_{Jmax} - r(t)(P_f + P_r) \text{ where}$$

$r(t)$ = thermal impedance under given conditions,
 P_f = forward power dissipation, and
 P_r = reverse power dissipation

This graph displays the derated allowable T_J due to reverse bias under DC conditions only and is calculated as $T_J = T_{Jmax} - r(t)P_r$, where $r(t) = R_{\theta JA}$. For other power applications further calculations must be performed.

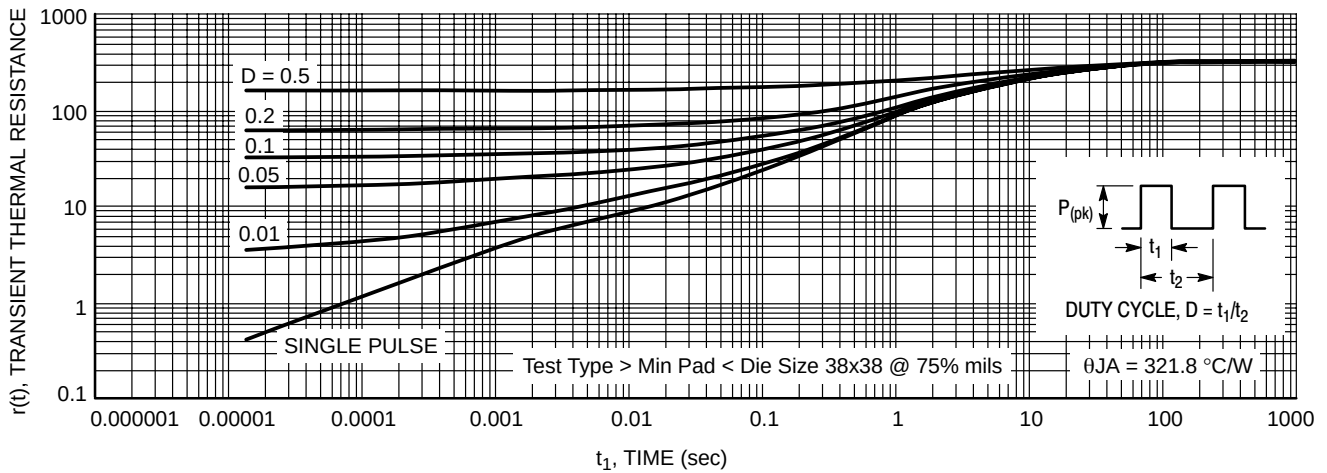
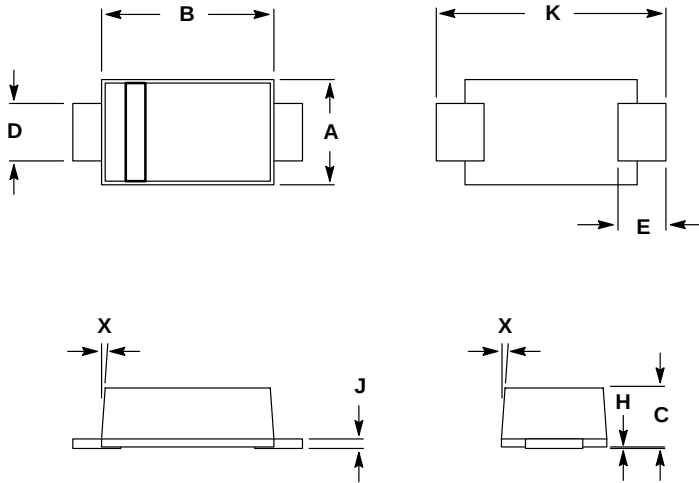


Figure 9. Thermal Response

MBR120VLSFT1

PACKAGE DIMENSIONS

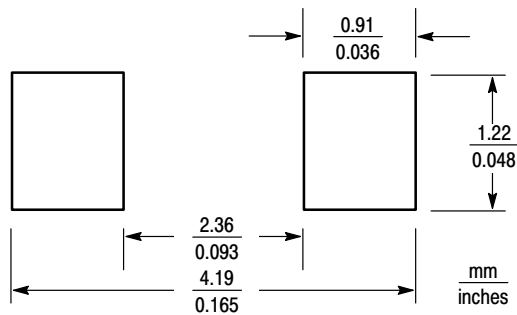
SOD-123LF
CASE 498-01
ISSUE O



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: MILLIMETER.
 3. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
 4. DIMENSIONS D AND J ARE TO BE MEASURED ON FLAT SECTION OF THE LEAD: BETWEEN 0.10 AND 0.25 MM FROM THE LEAD TIP.
 5. DIMENSION X IS A DEGREE ANGLE.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.50	1.80		
B	2.50	2.90		
C	0.90	1.00		
D	0.70	1.10		
H	0.00	0.10		
J	0.10	0.20		
E	0.55	0.95		
K	3.40	3.80		
X	0	8		

RECOMMENDED FOOTPRINT FOR SOD-123FL



SOD-123

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