

# MC10E175, MC100E175

## 5V ECL 9-Bit Latch With Parity

The MC10E/100E175 is a 9-bit latch. It also features a tenth latched output, ODDPAR, which is formed as the odd parity of the nine data inputs (ODDPAR is HIGH if an odd number of the inputs are HIGH).

The E175 can also be used to generate byte parity by using D8 as the parity-type select (L = even parity, H = odd parity), and using ODDPAR as the byte parity output.

The LEN pin latches the data when asserted with a logical high and makes the latch transparent when placed at a logic low level.

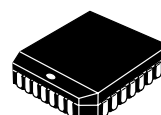
- 9-Bit Latch
- Parity Detection/Generation
- 800 ps Max. D to Output
- Reset
- PECL Mode Operating Range:  $V_{CC} = 4.2\text{ V}$  to  $5.5\text{ V}$  with  $V_{EE} = 0\text{ V}$
- NECL Mode Operating Range:  $V_{CC} = 0\text{ V}$  with  $V_{EE} = -4.2\text{ V}$  to  $-5.5\text{ V}$
- Internal Input 50 K $\Omega$  Pulldown Resistors
- ESD Protection: Human Body Model; > 1 KV, Machine Model; > 75 V
- Meets or Exceeds JEDEC Spec EIA/JESD78 IC Latchup Test
- Moisture Sensitivity Level 1  
For Additional Information, see Application Note AND8003/D
- Flammability Rating: UL 94 V-0 @ 0.125 in, Oxygen Index: 28 to 34
- Transistor Count = 416 devices



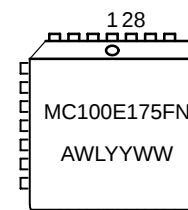
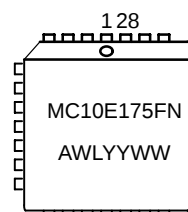
**ON Semiconductor®**

<http://onsemi.com>

### MARKING DIAGRAMS



PLCC-28  
FN SUFFIX  
CASE 776



A = Assembly Location  
WL = Wafer Lot  
YY = Year  
WW = Work Week

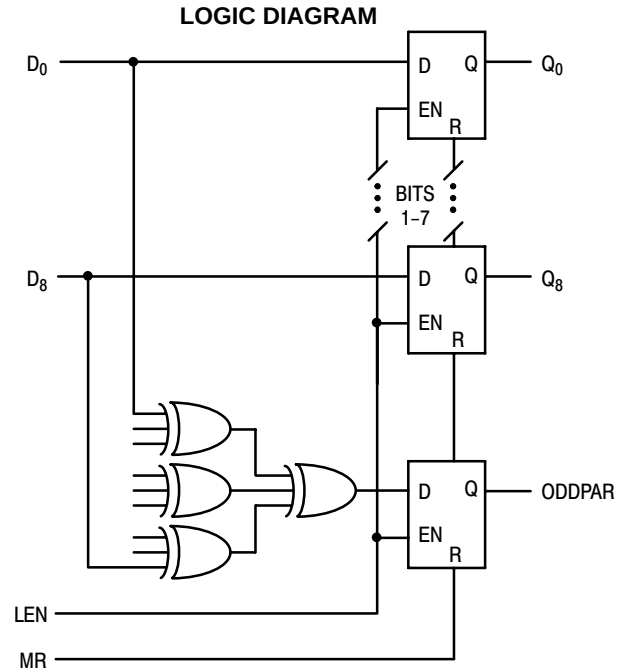
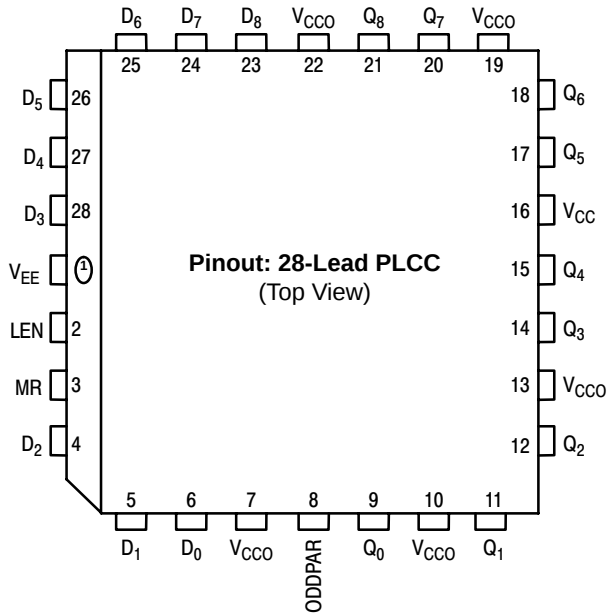
### ORDERING INFORMATION

| Device        | Package | Shipping <sup>†</sup> |
|---------------|---------|-----------------------|
| MC10E175FN    | PLCC-28 | 37 Units/Rail         |
| MC10E175FNR2  | PLCC-28 | 500 Units/Reel        |
| MC100E175FN   | PLCC-28 | 37 Units/Rail         |
| MC100E175FNR2 | PLCC-28 | 500 Units/Reel        |

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MC10E175, MC100E175

## LOGIC DIAGRAM AND PINOUT ASSIGNMENT



\* All V<sub>CC</sub> and V<sub>CCO</sub> pins are tied together on the die.

Warning: All V<sub>CC</sub>, V<sub>CCO</sub>, and V<sub>EE</sub> pins must be externally connected to Power Supply to guarantee proper operation.

## PIN DESCRIPTION

| PIN                                | FUNCTION          |
|------------------------------------|-------------------|
| D <sub>0</sub> – D <sub>8</sub>    | ECL Data Inputs   |
| LEN                                | ECL Latch Enable  |
| MR                                 | ECL Master Reset  |
| Q <sub>0</sub> – Q <sub>8</sub>    | ECL Data Outputs  |
| ODDPAR                             | ECL Parity Output |
| V <sub>CC</sub> , V <sub>CCO</sub> | Positive Supply   |
| V <sub>EE</sub>                    | Negative Supply   |
| NC                                 | No Connect        |

## FUNCTION TABLE

| D | EN | MR | Q              | ODDPAR                  |
|---|----|----|----------------|-------------------------|
| H | L  | L  | H              | H if odd no. of Dn HIGH |
| L | L  | L  | L              | H if odd no. of Dn HIGH |
| X | H  | L  | Q <sub>0</sub> | Q <sub>0</sub>          |
| X | X  | H  | L              | L                       |

# MC10E175, MC100E175

## MAXIMUM RATINGS (Note 1)

| Symbol           | Parameter                                          | Condition 1                                    | Condition 2                                                          | Rating       | Unit         |
|------------------|----------------------------------------------------|------------------------------------------------|----------------------------------------------------------------------|--------------|--------------|
| V <sub>CC</sub>  | PECL Mode Power Supply                             | V <sub>EE</sub> = 0 V                          |                                                                      | 8            | V            |
| V <sub>EE</sub>  | NECL Mode Power Supply                             | V <sub>CC</sub> = 0 V                          |                                                                      | -8           | V            |
| V <sub>I</sub>   | PECL Mode Input Voltage<br>NECL Mode Input Voltage | V <sub>EE</sub> = 0 V<br>V <sub>CC</sub> = 0 V | V <sub>I</sub> ≤ V <sub>CC</sub><br>V <sub>I</sub> ≥ V <sub>EE</sub> | 6<br>-6      | V<br>V       |
| I <sub>out</sub> | Output Current                                     | Continuous<br>Surge                            |                                                                      | 50<br>100    | mA<br>mA     |
| T <sub>A</sub>   | Operating Temperature Range                        |                                                |                                                                      | 0 to +85     | °C           |
| T <sub>stg</sub> | Storage Temperature Range                          |                                                |                                                                      | -65 to +150  | °C           |
| θ <sub>JA</sub>  | Thermal Resistance (Junction-to-Ambient)           | 0 LFPM<br>500 LFPM                             | 28 PLCC<br>28 PLCC                                                   | 63.5<br>43.5 | °C/W<br>°C/W |
| θ <sub>JC</sub>  | Thermal Resistance (Junction-to-Case)              | Standard Board                                 | 28 PLCC                                                              | 22 to 26     | °C/W         |
| T <sub>sol</sub> | Wave Solder                                        | <2 to 3 sec @ 248°C                            |                                                                      | 265          | °C           |

1. Maximum Ratings are those values beyond which device damage may occur.

## 10E SERIES PECL DC CHARACTERISTICS V<sub>CCx</sub> = 5.0 V; V<sub>EE</sub> = 0.0 V (Note 2)

| Symbol          | Characteristic               | 0°C  |      |      | 25°C |      |      | 85°C |      |      | Unit |
|-----------------|------------------------------|------|------|------|------|------|------|------|------|------|------|
|                 |                              | Min  | Typ  | Max  | Min  | Typ  | Max  | Min  | Typ  | Max  |      |
| I <sub>EE</sub> | Power Supply Current         |      | 110  | 132  |      | 110  | 132  |      | 110  | 132  | mA   |
| V <sub>OH</sub> | Output HIGH Voltage (Note 3) | 3980 | 4070 | 4160 | 4020 | 4105 | 4190 | 4090 | 4185 | 4280 | mV   |
| V <sub>OL</sub> | Output LOW Voltage (Note 3)  | 3050 | 3210 | 3370 | 3050 | 3210 | 3370 | 3050 | 3227 | 3405 | mV   |
| V <sub>IH</sub> | Input HIGH Voltage           | 3830 | 3995 | 4160 | 3870 | 4030 | 4190 | 3940 | 4110 | 4280 | mV   |
| V <sub>IL</sub> | Input LOW Voltage            | 3050 | 3285 | 3520 | 3050 | 3285 | 3520 | 3050 | 3302 | 3555 | mV   |
| I <sub>IH</sub> | Input HIGH Current           |      |      | 150  |      |      | 150  |      |      | 150  | μA   |
| I <sub>IL</sub> | Input LOW Current            | 0.5  | 0.3  |      | 0.5  | 0.25 |      | 0.3  | 0.2  |      | μA   |

NOTE: Devices are designed to meet the DC specifications shown in the above table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 lfpm is maintained.

2. Input and output parameters vary 1:1 with V<sub>CC</sub>. V<sub>EE</sub> can vary -0.46 V / +0.06 V.

3. Outputs are terminated through a 50 Ω resistor to V<sub>CC</sub>-2 volts.

## 10E SERIES NECL DC CHARACTERISTICS V<sub>CCx</sub> = 0.0 V; V<sub>EE</sub> = -5.0 V (Note 4)

| Symbol          | Characteristic               | 0°C   |       |       | 25°C  |       |       | 85°C  |       |       | Unit |
|-----------------|------------------------------|-------|-------|-------|-------|-------|-------|-------|-------|-------|------|
|                 |                              | Min   | Typ   | Max   | Min   | Typ   | Max   | Min   | Typ   | Max   |      |
| I <sub>EE</sub> | Power Supply Current         |       | 110   | 132   |       | 110   | 132   |       | 110   | 132   | mA   |
| V <sub>OH</sub> | Output HIGH Voltage (Note 5) | -1020 | -930  | -840  | -980  | -895  | -810  | -910  | -815  | -720  | mV   |
| V <sub>OL</sub> | Output LOW Voltage (Note 5)  | -1950 | -1790 | -1630 | -1950 | -1790 | -1630 | -1950 | -1773 | -1595 | mV   |
| V <sub>IH</sub> | Input HIGH Voltage           | -1170 | -1005 | -840  | -1130 | -970  | -810  | -1060 | -890  | -720  | mV   |
| V <sub>IL</sub> | Input LOW Voltage            | -1950 | -1715 | -1480 | -1950 | -1715 | -1480 | -1950 | -1698 | -1445 | mV   |
| I <sub>IH</sub> | Input HIGH Current           |       |       | 150   |       |       | 150   |       |       | 150   | μA   |
| I <sub>IL</sub> | Input LOW Current            | 0.5   | 0.3   |       | 0.5   | 0.065 |       | 0.3   | 0.2   |       | μA   |

NOTE: Devices are designed to meet the DC specifications shown in the above table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 lfpm is maintained.

4. Input and output parameters vary 1:1 with V<sub>CC</sub>. V<sub>EE</sub> can vary -0.46 V / +0.06 V.

5. Outputs are terminated through a 50 Ω resistor to V<sub>CC</sub>-2 volts.

## MC10E175, MC100E175

### 100E SERIES PECL DC CHARACTERISTICS $V_{CCx} = 5.0\text{ V}$ ; $V_{EE} = 0.0\text{ V}$ (Note 6)

| Symbol   | Characteristic               | 0°C  |      |      | 25°C |      |      | 85°C |      |      | Unit          |
|----------|------------------------------|------|------|------|------|------|------|------|------|------|---------------|
|          |                              | Min  | Typ  | Max  | Min  | Typ  | Max  | Min  | Typ  | Max  |               |
| $I_{EE}$ | Power Supply Current         |      | 110  | 132  |      | 110  | 132  |      | 127  | 152  | mA            |
| $V_{OH}$ | Output HIGH Voltage (Note 7) | 3975 | 4050 | 4120 | 3975 | 4050 | 4120 | 3975 | 4050 | 4120 | mV            |
| $V_{OL}$ | Output LOW Voltage (Note 7)  | 3190 | 3295 | 3380 | 3190 | 3255 | 3380 | 3190 | 3260 | 3380 | mV            |
| $V_{IH}$ | Input HIGH Voltage           | 3835 | 3975 | 4120 | 3835 | 3975 | 4120 | 3835 | 3975 | 4120 | mV            |
| $V_{IL}$ | Input LOW Voltage            | 3190 | 3355 | 3525 | 3190 | 3355 | 3525 | 3190 | 3355 | 3525 | mV            |
| $I_{IH}$ | Input HIGH Current           |      |      | 150  |      |      | 150  |      |      | 150  | $\mu\text{A}$ |
| $I_{IL}$ | Input LOW Current            | 0.5  | 0.3  |      | 0.5  | 0.25 |      | 0.5  | 0.2  |      | $\mu\text{A}$ |

NOTE: Devices are designed to meet the DC specifications shown in the above table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 lfpm is maintained.

6. Input and output parameters vary 1:1 with  $V_{CC}$ .  $V_{EE}$  can vary  $-0.46\text{ V}$  /  $+0.8\text{ V}$ .

7. Outputs are terminated through a  $50\ \Omega$  resistor to  $V_{CC} - 2\text{ volts}$ .

### 100E SERIES NECL DC CHARACTERISTICS $V_{CCx} = 0.0\text{ V}$ ; $V_{EE} = -5.0\text{ V}$ (Note 8)

| Symbol   | Characteristic               | 0°C   |       |       | 25°C  |       |       | 85°C  |       |       | Unit          |
|----------|------------------------------|-------|-------|-------|-------|-------|-------|-------|-------|-------|---------------|
|          |                              | Min   | Typ   | Max   | Min   | Typ   | Max   | Min   | Typ   | Max   |               |
| $I_{EE}$ | Power Supply Current         |       | 110   | 132   |       | 110   | 132   |       | 127   | 152   | mA            |
| $V_{OH}$ | Output HIGH Voltage (Note 9) | -1025 | -950  | -880  | -1025 | -950  | -880  | -1025 | -950  | -880  | mV            |
| $V_{OL}$ | Output LOW Voltage (Note 9)  | -1810 | -1705 | -1620 | -1810 | -1745 | -1620 | -1810 | -1740 | -1620 | mV            |
| $V_{IH}$ | Input HIGH Voltage           | -1165 | -1025 | -880  | -1165 | -1025 | -880  | -1165 | -1025 | -880  | mV            |
| $V_{IL}$ | Input LOW Voltage            | -1810 | -1645 | -1475 | -1810 | -1645 | -1475 | -1810 | -1645 | -1475 | mV            |
| $I_{IH}$ | Input HIGH Current           |       |       | 150   |       |       | 150   |       |       | 150   | $\mu\text{A}$ |
| $I_{IL}$ | Input LOW Current            | 0.5   | 0.3   |       | 0.5   | 0.25  |       | 0.5   | 0.2   |       | $\mu\text{A}$ |

NOTE: Devices are designed to meet the DC specifications shown in the above table, after thermal equilibrium has been established. The circuit is in a test socket or mounted on a printed circuit board and transverse air flow greater than 500 lfpm is maintained.

8. Input and output parameters vary 1:1 with  $V_{CC}$ .  $V_{EE}$  can vary  $-0.46\text{ V}$  /  $+0.8\text{ V}$ .

9. Outputs are terminated through a  $50\ \Omega$  resistor to  $V_{CC} - 2\text{ volts}$ .

# MC10E175, MC100E175

**AC CHARACTERISTICS**  $V_{CCx} = 5.0\text{ V}$ ;  $V_{EE} = 0.0\text{ V}$  or  $V_{CCx} = 0.0\text{ V}$ ;  $V_{EE} = -5.0\text{ V}$  (Note 10)

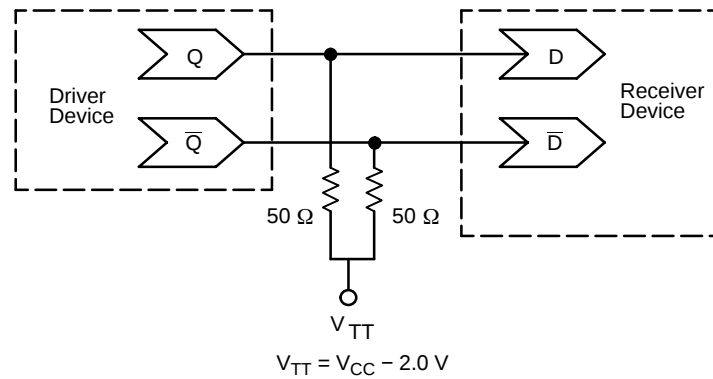
| Symbol                 | Characteristic                | 0°C  |      |      | 25°C |      |      | 85°C |      |      | Unit |
|------------------------|-------------------------------|------|------|------|------|------|------|------|------|------|------|
|                        |                               | Min  | Typ  | Max  | Min  | Typ  | Max  | Min  | Typ  | Max  |      |
| $f_{MAX}$              | Maximum Toggle Frequency      | 700  | 1100 |      | 700  | 1100 |      | 700  | 1100 |      | MHz  |
| $t_{PLH}$<br>$t_{PHL}$ | Propagation Delay to Output   |      |      |      |      |      |      |      |      |      | ps   |
|                        | D to Q                        | 450  | 600  | 800  | 450  | 600  | 800  | 450  | 600  | 800  |      |
|                        | D to ODDPAR                   | 850  | 1150 | 1450 | 850  | 1150 | 1450 | 850  | 1150 | 1450 |      |
|                        | LEN to Q                      | 525  | 700  | 900  | 525  | 700  | 900  | 525  | 700  | 900  |      |
|                        | LEN to ODDPAR                 | 525  | 700  | 900  | 525  | 700  | 900  | 525  | 700  | 900  |      |
|                        | MR to Q( $t_{PHL}$ )          | 525  | 700  | 900  | 525  | 700  | 900  | 525  | 700  | 900  |      |
|                        | MR to ODDPAR( $t_{PHL}$ )     | 525  | 700  | 900  | 525  | 700  | 900  | 525  | 700  | 900  |      |
| $t_s$                  | Setup Time                    |      |      |      |      |      |      |      |      |      | ps   |
|                        | D (Q)                         | 275  | 100  |      | 275  |      |      | 275  |      |      |      |
|                        | D (ODDPAR)                    | 900  | 700  |      | 900  |      |      | 900  |      |      |      |
| $t_h$                  | Hold Time                     |      |      |      |      |      |      |      |      |      | ps   |
|                        | D (Q)                         | 175  | -100 |      | 175  |      |      | 175  |      |      |      |
|                        | D (ODDPAR)                    | -300 | -70  |      | -300 |      |      | -300 |      |      |      |
| $t_{RR}$               | Reset Recovery Time           | 850  | 600  |      | 850  | 600  |      | 850  | 600  |      | ps   |
| $t_{SKEW}$             | Within-Device Skew (Note 11)  |      |      |      |      |      |      |      |      |      | ps   |
|                        | LEN, MR                       |      | 75   |      |      | 75   |      |      | 75   |      |      |
|                        | D to Q                        |      | 75   |      |      | 75   |      |      | 75   |      |      |
|                        | D to ODDPAR                   |      | 200  |      |      | 200  |      |      | 200  |      |      |
| $t_{JITTER}$           | Random Clock Jitter (RMS)     |      | < 1  |      |      | < 1  |      |      | < 1  |      | ps   |
| $t_r$<br>$t_f$         | Rise/Fall Times<br>(20 - 80%) | 300  | 500  | 800  | 300  | 500  | 800  | 300  | 500  | 800  | ps   |

10. 10 Series:  $V_{EE}$  can vary -0.46 V / -0.06 V.

100 Series:  $V_{EE}$  can vary -0.46 V / -0.8 V.

11. Within-device skew is defined as identical transitions on similar paths through a device.

## MC10E175, MC100E175



**Figure 1. Typical Termination for Output Driver and Device Evaluation**  
(See Application Note AND8020 – Termination of ECL Logic Devices.)

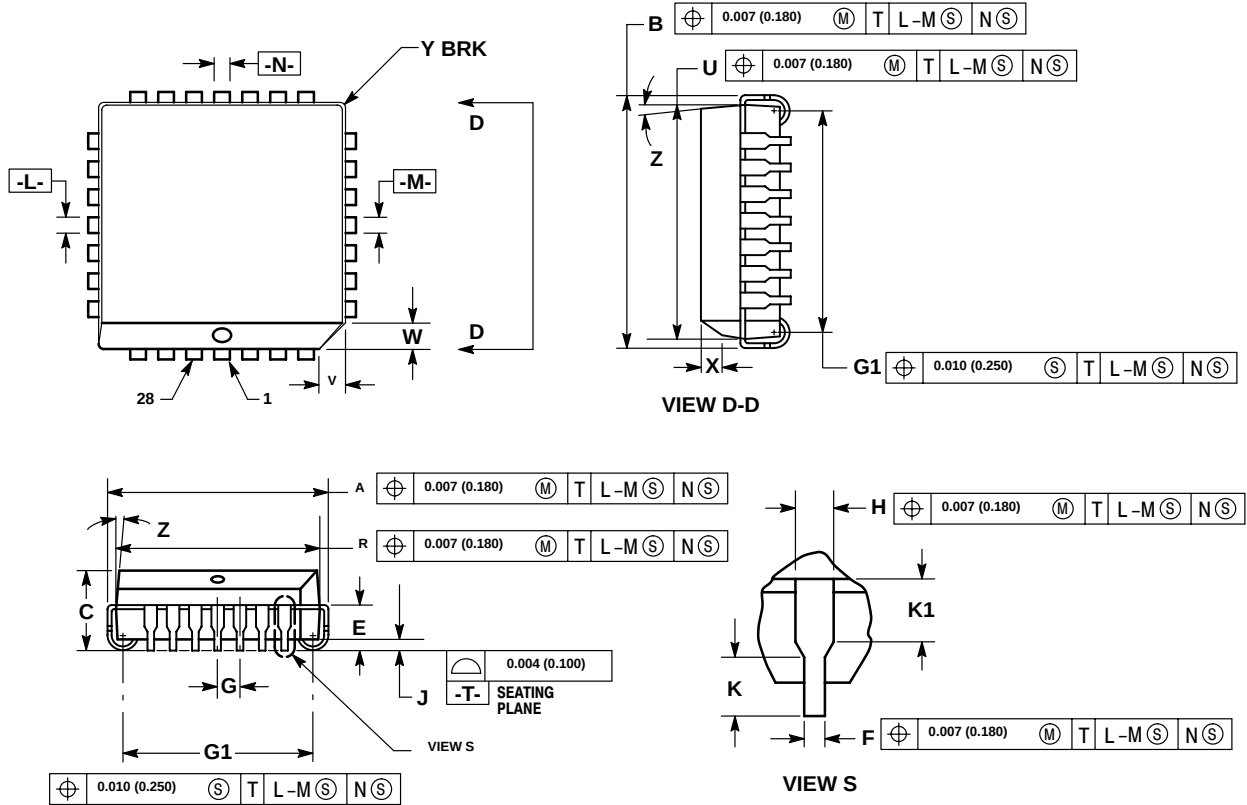
### Resource Reference of Application Notes

- |                |                                                               |
|----------------|---------------------------------------------------------------|
| <b>AN1404</b>  | – ECLinPS Circuit Performance at Non-Standard $V_{IH}$ Levels |
| <b>AN1405</b>  | – ECL Clock Distribution Techniques                           |
| <b>AN1406</b>  | – Designing with PECL (ECL at +5.0 V)                         |
| <b>AN1503</b>  | – ECLinPS I/O SPICE Modeling Kit                              |
| <b>AN1504</b>  | – Metastability and the ECLinPS Family                        |
| <b>AN1568</b>  | – Interfacing Between LVDS and ECL                            |
| <b>AN1596</b>  | – ECLinPS Lite Translator ELT Family SPICE I/O Model Kit      |
| <b>AN1650</b>  | – Using Wire-OR Ties in ECLinPS Designs                       |
| <b>AN1672</b>  | – The ECL Translator Guide                                    |
| <b>AND8001</b> | – Odd Number Counters Design                                  |
| <b>AND8002</b> | – Marking and Date Codes                                      |
| <b>AND8020</b> | – Termination of ECL Logic Devices                            |

# MC10E175, MC100E175

## PACKAGE DIMENSIONS

PLCC-28  
FN SUFFIX  
PLASTIC PLCC PACKAGE  
CASE 776-02  
ISSUE E



### NOTES:

- DATUMS -L-, -M-, AND -N- DETERMINED WHERE TOP OF LEAD SHOULDER EXITS PLASTIC BODY AT MOLD PARTING LINE.
- DIM G1, TRUE POSITION TO BE MEASURED AT DATUM -T-, SEATING PLANE.
- DIM R AND U DO NOT INCLUDE MOLD FLASH. ALLOWABLE MOLD FLASH IS 0.010 (0.250) PER SIDE.
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
- THE PACKAGE TOP MAY BE SMALLER THAN THE PACKAGE BOTTOM BY UP TO 0.012 (0.300). DIMENSIONS R AND U ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY EXCLUSIVE OF MOLD FLASH, TIE BAR BURRS, GATE BURRS AND INTERLEAD FLASH, BUT INCLUDING ANY MISMATCH BETWEEN THE TOP AND BOTTOM OF THE PLASTIC BODY.
- DIMENSION H DOES NOT INCLUDE DAMBAR PROTRUSION OR INTRUSION. THE DAMBAR PROTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO BE GREATER THAN 0.037 (0.940). THE DAMBAR INTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO BE SMALLER THAN 0.025 (0.635).

| DIM | INCHES    |       | MILLIMETERS |       |
|-----|-----------|-------|-------------|-------|
|     | MIN       | MAX   | MIN         | MAX   |
| A   | 0.485     | 0.495 | 12.32       | 12.57 |
| B   | 0.485     | 0.495 | 12.32       | 12.57 |
| C   | 0.165     | 0.180 | 4.20        | 4.57  |
| E   | 0.090     | 0.110 | 2.29        | 2.79  |
| F   | 0.013     | 0.019 | 0.33        | 0.48  |
| G   | 0.050 BSC |       | 1.27 BSC    |       |
| H   | 0.026     | 0.032 | 0.66        | 0.81  |
| J   | 0.020     | —     | 0.51        | —     |
| K   | 0.025     | —     | 0.64        | —     |
| R   | 0.450     | 0.456 | 11.43       | 11.58 |
| U   | 0.450     | 0.456 | 11.43       | 11.58 |
| V   | 0.042     | 0.048 | 1.07        | 1.21  |
| W   | 0.042     | 0.048 | 1.07        | 1.21  |
| X   | 0.042     | 0.056 | 1.07        | 1.42  |
| Y   | —         | 0.020 | —           | 0.50  |
| Z   | 2°        | 10°   | 2°          | 10°   |
| G1  | 0.410     | 0.430 | 10.42       | 10.92 |
| K1  | 0.040     | —     | 1.02        | —     |

# MC10E175, MC100E175

ON Semiconductor and  are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## PUBLICATION ORDERING INFORMATION

### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor  
P.O. Box 5163, Denver, Colorado 80217 USA

**Phone:** 303-675-2175 or 800-344-3860 Toll Free USA/Canada

**Fax:** 303-675-2176 or 800-344-3867 Toll Free USA/Canada

**Email:** [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

**N. American Technical Support:** 800-282-9855 Toll Free  
USA/Canada

**Japan:** ON Semiconductor, Japan Customer Focus Center  
2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051  
**Phone:** 81-3-5773-3850

**ON Semiconductor Website:** <http://onsemi.com>

**Order Literature:** <http://www.onsemi.com/litorder>

For additional information, please contact your  
local Sales Representative.