

MMBTA56WT1

Driver Transistor

PNP Silicon

Moisture Sensitivity Level: 1
ESD Rating: Human Body Model – 4 kV
Machine Model – 400 V

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|--------------------------------|-----------|-------|------|
| Collector–Emitter Voltage | V_{CEO} | –80 | Vdc |
| Collector–Base Voltage | V_{CBO} | –80 | Vdc |
| Emitter–Base Voltage | V_{EBO} | –4.0 | Vdc |
| Collector Current – Continuous | I_C | –500 | mAdc |

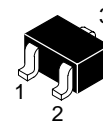
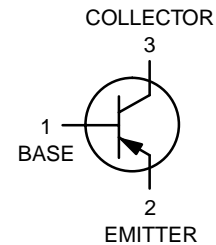
THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
|---|-----------------|-------------|--------------------|
| Total Device Dissipation FR–5 Board $T_A = 25^\circ\text{C}$ | P_D | 150 | mW |
| Thermal Resistance, Junction to Ambient | $R_{\theta JA}$ | 833 | $^\circ\text{C/W}$ |
| Junction and Storage Temperature | T_J, T_{stg} | –55 to +150 | $^\circ\text{C}$ |



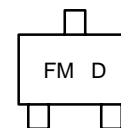
ON Semiconductor®

<http://onsemi.com>



**SC-70
CASE 419
STYLE 3**

MARKING DIAGRAM



FM = Specific Device Code
D = Date Code

ORDERING INFORMATION

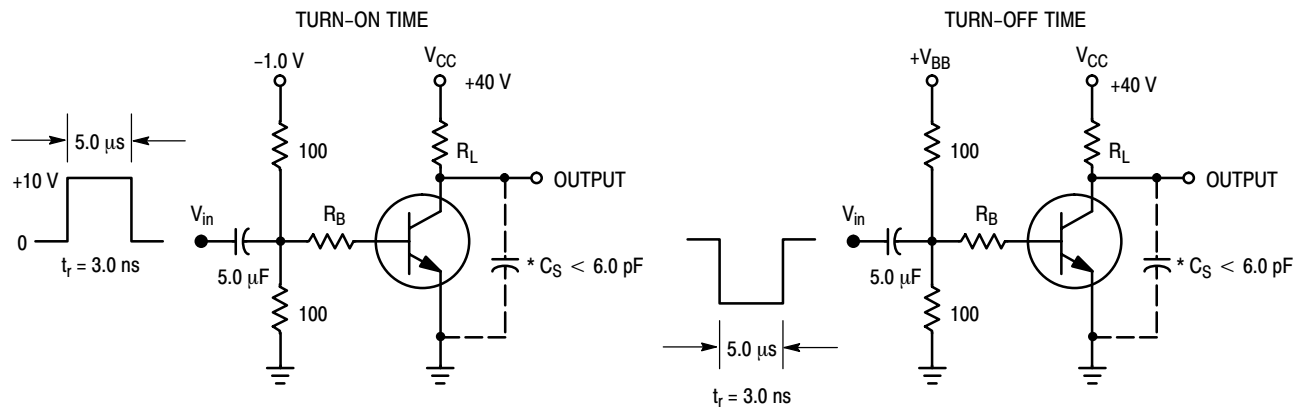
| Device | Package | Shipping |
|------------|---------|------------------|
| MMBTA56WT1 | SC–70 | 3000/Tape & Reel |

MMBTA56WT1

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

| Characteristic | Symbol | Min | Max | Unit |
|--|---------------|------------|--------|---------------|
| OFF CHARACTERISTICS | | | | |
| Collector-Emitter Breakdown Voltage (Note 1) ($I_C = -1.0\text{ mA}$, $I_B = 0$) | $V_{(BR)CEO}$ | -80 | - | Vdc |
| Emitter-Base Breakdown Voltage ($I_E = -100\text{ }\mu\text{A}$, $I_C = 0$) | $V_{(BR)EBO}$ | -4.0 | - | Vdc |
| Collector Cutoff Current ($V_{CE} = -60\text{ Vdc}$, $I_B = 0$) | I_{CES} | - | -0.1 | μA |
| Collector Cutoff Current ($V_{CB} = -60\text{ Vdc}$, $I_E = 0$) ($V_{CB} = -80\text{ Vdc}$, $I_E = 0$) | I_{CBO} | - | -0.1 | μA |
| ON CHARACTERISTICS | | | | |
| DC Current Gain ($I_C = -10\text{ mA}$, $V_{CE} = -1.0\text{ Vdc}$) ($I_C = -100\text{ mA}$, $V_{CE} = -1.0\text{ Vdc}$) | h_{FE} | 100 100 | - - | - |
| Collector-Emitter Saturation Voltage ($I_C = -100\text{ mA}$, $I_B = -10\text{ mA}$) | $V_{CE(sat)}$ | - | -0.25 | Vdc |
| Base-Emitter On Voltage ($I_C = -100\text{ mA}$, $V_{CE} = -1.0\text{ Vdc}$) | $V_{BE(on)}$ | - | -1.2 | Vdc |
| SMALL-SIGNAL CHARACTERISTICS | | | | |
| Current-Gain - Bandwidth Product (Note 2) ($I_C = -100\text{ mA}$, $V_{CE} = -1.0\text{ Vdc}$, $f = 100\text{ MHz}$) | f_T | 50 | - | MHz |

1. Pulse Test: Pulse Width $\leq 300\text{ }\mu\text{s}$, Duty Cycle $\leq 2.0\%$.
2. f_T is defined as the frequency at which $|h_{fe}|$ extrapolates to unity.



*Total Shunt Capacitance of Test Jig and Connectors
For PNP Test Circuits, Reverse All Voltage Polarities

Figure 1. Switching Time Test Circuits

MMBTA56WT1

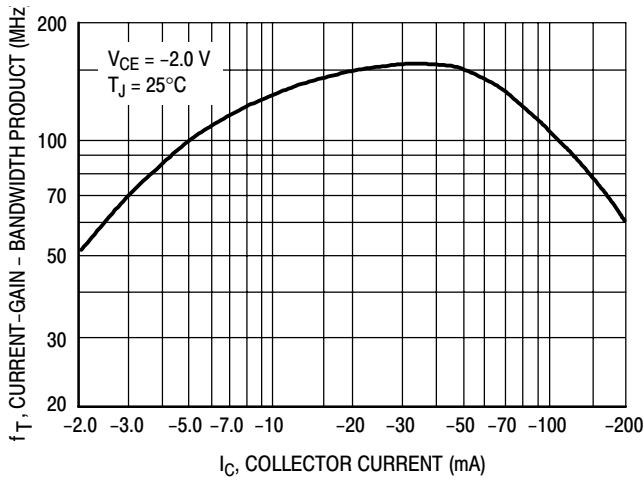


Figure 2. Current-Gain — Bandwidth Product

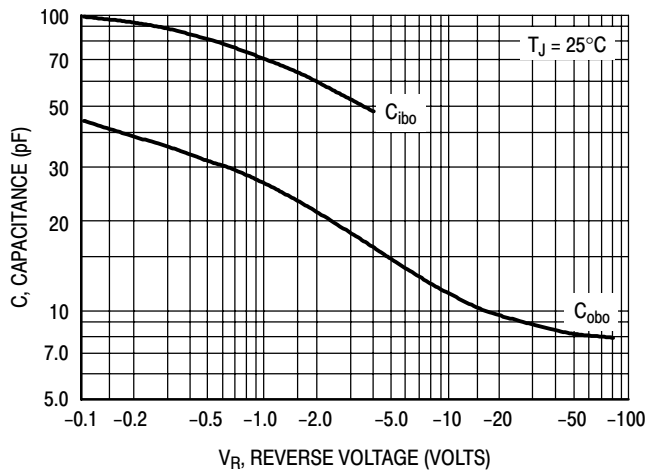


Figure 3. Capacitance

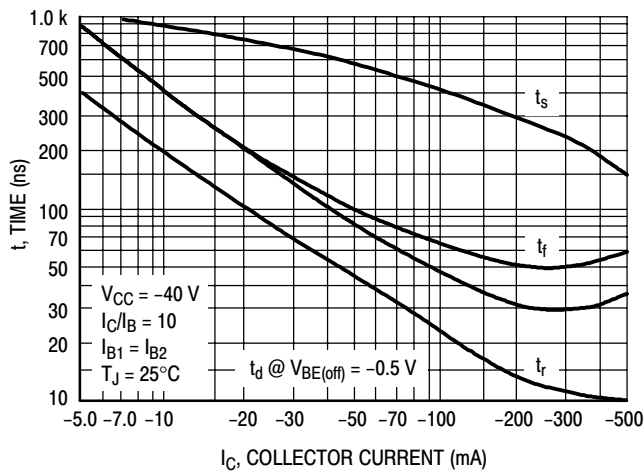


Figure 4. Switching Time

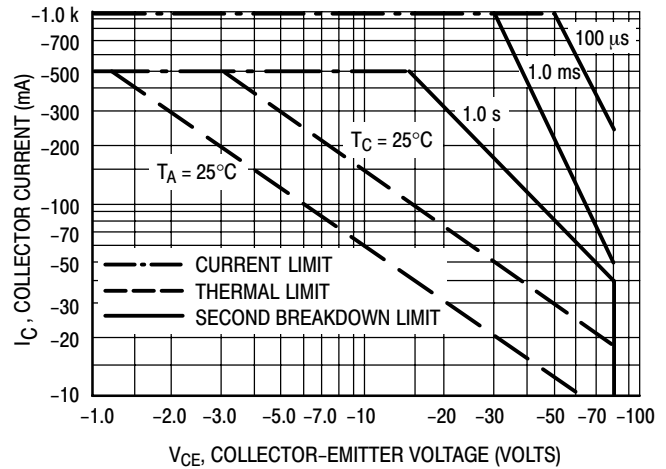


Figure 5. Active-Region Safe Operating Area

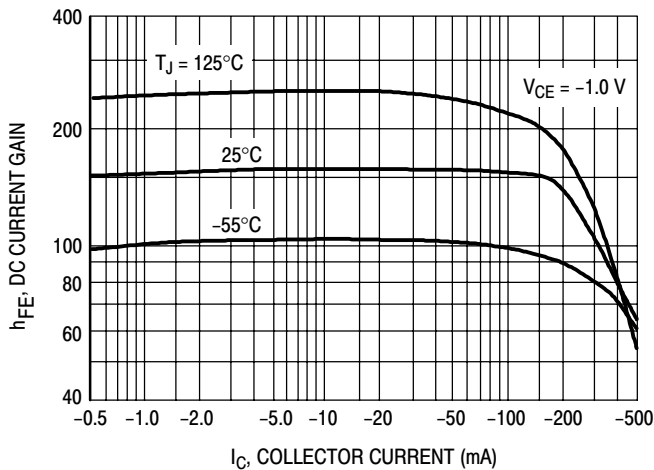


Figure 6. DC Current Gain

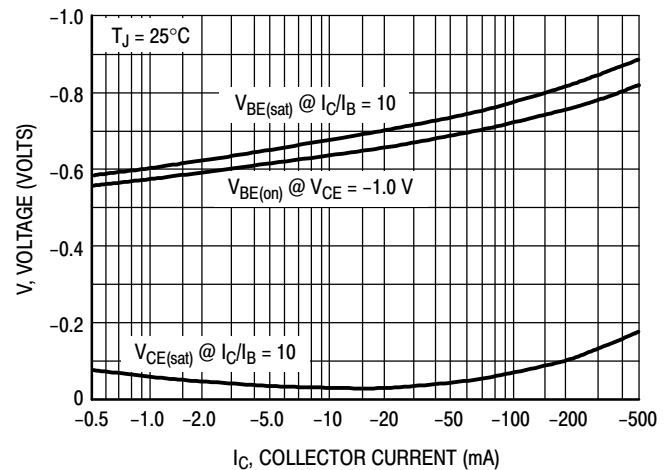


Figure 7. "ON" Voltages

MMBTA56WT1

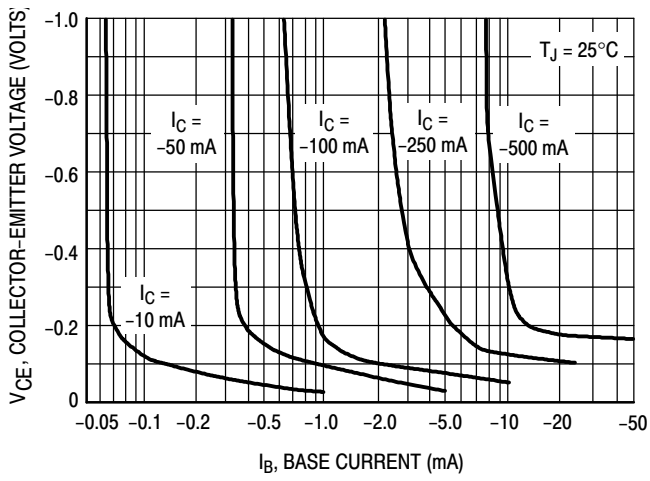


Figure 8. Collector Saturation Region

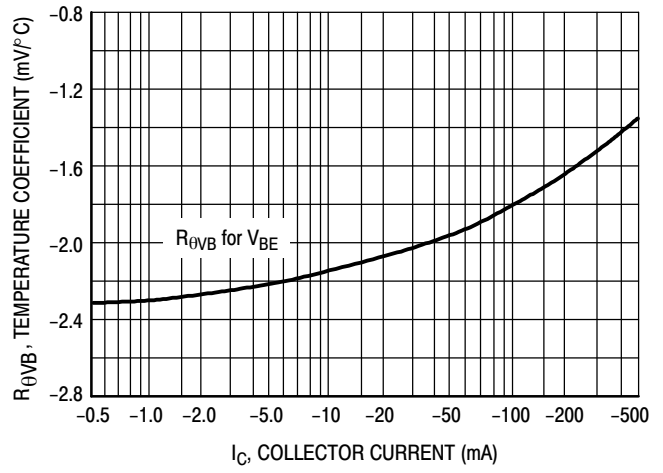


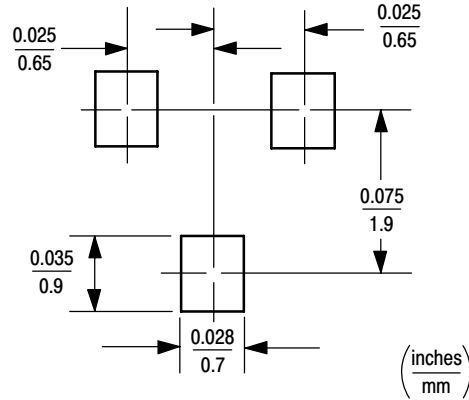
Figure 9. Base-Emitter Temperature Coefficient

INFORMATION FOR USING THE SC-70/SOT-323 SURFACE MOUNT PACKAGE

MINIMUM RECOMMENDED FOOTPRINT FOR SURFACE MOUNTED APPLICATIONS

Surface mount board layout is a critical portion of the total design. The footprint for the semiconductor packages must be the correct size to insure proper solder connection

interface between the board and the package. With the correct pad geometry, the packages will self align when subjected to a solder reflow process.



SC-70/SOT-323 POWER DISSIPATION

The power dissipation of the SC-70/SOT-323 is a function of the pad size. This can vary from the minimum pad size for soldering to the pad size given for maximum power dissipation. Power dissipation for a surface mount device is determined by $T_{J(max)}$, the maximum rated junction temperature of the die, $R_{\theta JA}$, the thermal resistance from the device junction to ambient; and the operating temperature, T_A . Using the values provided on the data sheet, P_D can be calculated as follows.

$$P_D = \frac{T_{J(max)} - T_A}{R_{\theta JA}}$$

The values for the equation are found in the maximum ratings table on the data sheet. Substituting these values

into the equation for an ambient temperature T_A of 25°C, one can calculate the power dissipation of the device which in this case is 150 milliwatts.

$$P_D = \frac{150^\circ\text{C} - 25^\circ\text{C}}{833^\circ\text{C/W}} = 150 \text{ milliwatts}$$

The 833°C/W assumes the use of the recommended footprint on a glass epoxy printed circuit board to achieve a power dissipation of 150 milliwatts. Another alternative would be to use a ceramic substrate or an aluminum core board such as Thermal Clad®. Using a board material such as Thermal Clad, a higher power dissipation can be achieved using the same footprint.

SOLDERING PRECAUTIONS

The melting temperature of solder is higher than the rated temperature of the device. When the entire device is heated to a high temperature, failure to complete soldering within a short time could result in device failure. Therefore, the following items should always be observed in order to minimize the thermal stress to which the devices are subjected.

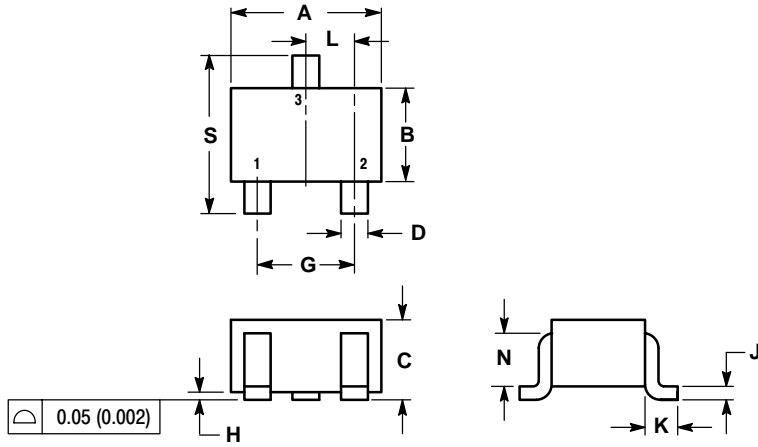
- Always preheat the device.
- The delta temperature between the preheat and soldering should be 100°C or less.*
- When preheating and soldering, the temperature of the leads and the case must not exceed the maximum temperature ratings as shown on the data sheet. When using infrared heating with the reflow soldering method, the difference should be a maximum of 10°C.

- The soldering temperature and time should not exceed 260°C for more than 10 seconds.
 - When shifting from preheating to soldering, the maximum temperature gradient should be 5°C or less.
 - After soldering has been completed, the device should be allowed to cool naturally for at least three minutes. Gradual cooling should be used as the use of forced cooling will increase the temperature gradient and result in latent failure due to mechanical stress.
 - Mechanical stress or shock should not be applied during cooling
- * Soldering a device without preheating can cause excessive thermal shock and stress which can result in damage to the device.

MMBTA56WT1

PACKAGE DIMENSIONS

SC-70/SOT-323
CASE 419-04
ISSUE L



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

| DIM | INCHES | | MILLIMETERS | |
|-----|-----------|-------|-------------|------|
| | MIN | MAX | MIN | MAX |
| A | 0.071 | 0.087 | 1.80 | 2.20 |
| B | 0.045 | 0.053 | 1.15 | 1.35 |
| C | 0.032 | 0.040 | 0.80 | 1.00 |
| D | 0.012 | 0.016 | 0.30 | 0.40 |
| G | 0.047 | 0.055 | 1.20 | 1.40 |
| H | 0.000 | 0.004 | 0.00 | 0.10 |
| J | 0.004 | 0.010 | 0.10 | 0.25 |
| K | 0.017 REF | | 0.425 REF | |
| L | 0.026 BSC | | 0.650 BSC | |
| N | 0.028 REF | | 0.700 REF | |
| S | 0.079 | 0.095 | 2.00 | 2.40 |


STYLE 3:

- PIN 1. BASE
- EMITTER
- COLLECTOR

Notes

MMBTA56WT1

Thermal Clad is a registered trademark of the Bergquist Company.

ON Semiconductor is a trademark and  is a registered trademark of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

PUBLICATION ORDERING INFORMATION

Literature Fulfillment:

Literature Distribution Center for ON Semiconductor
P.O. Box 5163, Denver, Colorado 80217 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: ONlit@hibbertco.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

JAPAN: ON Semiconductor, Japan Customer Focus Center
4-32-1 Nishi-Gotanda, Shinagawa-ku, Tokyo, Japan 141-0031
Phone: 81-3-5740-2700
Email: r14525@onsemi.com

ON Semiconductor Website: <http://onsemi.com>

For additional information, please contact your local Sales Representative.