

MSB1218A-RT1

Preferred Device

PNP Silicon General Purpose Amplifier Transistor

This PNP Silicon Epitaxial Planar Transistor is designed for general purpose amplifier applications. This device is housed in the SC-70/SOT-323 package which is designed for low power surface mount applications.

- High h_{FE} , 210–460
- Low $V_{CE(sat)}$, < 0.5 V
- Available in 8 mm, 7-inch/3000 Unit Tape and Reel

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

Rating	Symbol	Value	Unit
Collector–Base Voltage	$V_{(BR)CBO}$	45	Vdc
Collector–Emitter Voltage	$V_{(BR)CEO}$	45	Vdc
Emitter–Base Voltage	$V_{(BR)EBO}$	7.0	Vdc
Collector Current – Continuous	I_C	100	mAdc
Collector Current – Peak	$I_{C(P)}$	200	mAdc

THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Power Dissipation (Note 1)	P_D	150	mW
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	–55 to +150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Min	Max	Unit
Collector–Emitter Breakdown Voltage ($I_C = 2.0\text{ mAdc}$, $I_B = 0$)	$V_{(BR)CEO}$	45	–	Vdc
Collector–Base Breakdown Voltage ($I_C = 10\text{ }\mu\text{Adc}$, $I_E = 0$)	$V_{(BR)CBO}$	45	–	Vdc
Emitter–Base Breakdown Voltage ($I_E = 10\text{ }\mu\text{Adc}$, $I_C = 0$)	$V_{(BR)EBO}$	7.0	–	Vdc
Collector–Base Cutoff Current ($V_{CB} = 20\text{ Vdc}$, $I_E = 0$)	I_{CBO}	–	0.1	μA
Collector–Emitter Cutoff Current ($V_{CE} = 10\text{ Vdc}$, $I_B = 0$)	I_{CEO}	–	100	μA
DC Current Gain (Note 2) ($V_{CE} = 10\text{ Vdc}$, $I_C = 2.0\text{ mAdc}$)	h_{FE1}	210	340	–
Collector–Emitter Saturation Voltage (Note 2) ($I_C = 100\text{ mAdc}$, $I_B = 10\text{ mAdc}$)	$V_{CE(sat)}$	–	0.5	Vdc

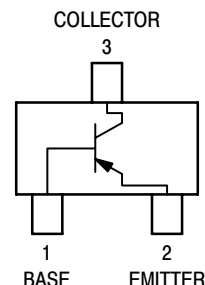
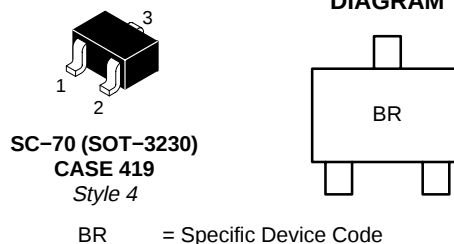
1. Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.
2. Pulse Test: Pulse Width $\leq 300\text{ }\mu\text{s}$, D.C. $\leq 2\%$.



ON Semiconductor®

<http://onsemi.com>

MARKING DIAGRAM



ORDERING INFORMATION

Device	Package	Shipping [†]
MSB1218A-RT1	SC-70	3000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

MSB1218A-RT1

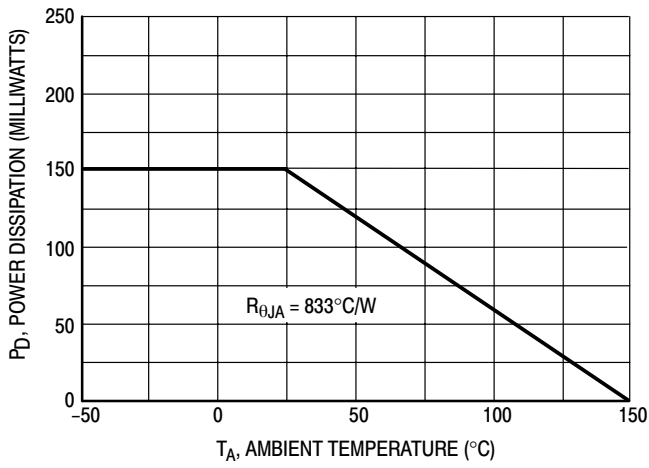


Figure 1. Derating Curve

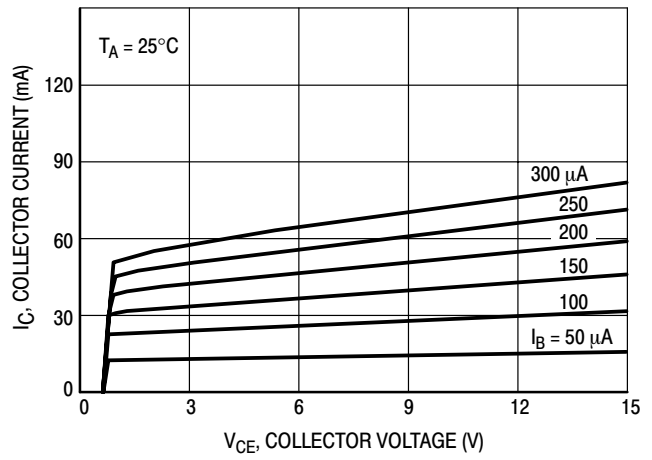


Figure 2. $I_C - V_{CE}$

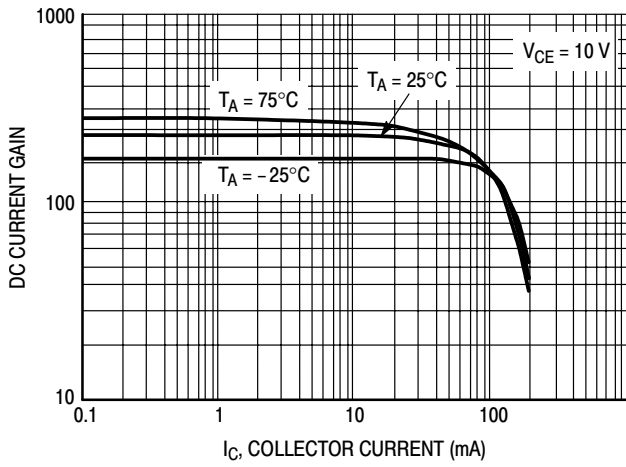


Figure 3. DC Current Gain

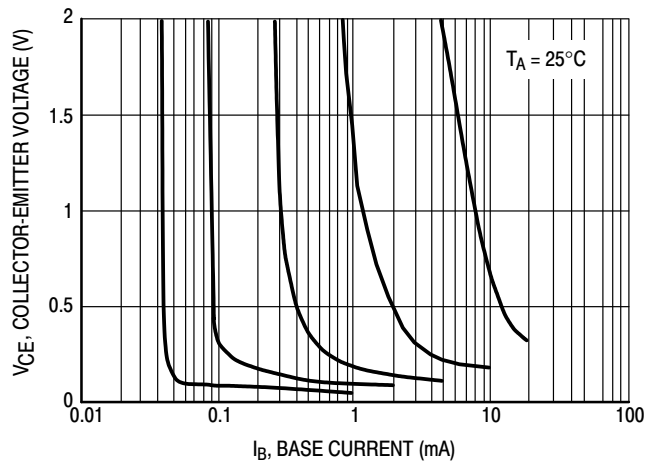


Figure 4. Collector Saturation Region

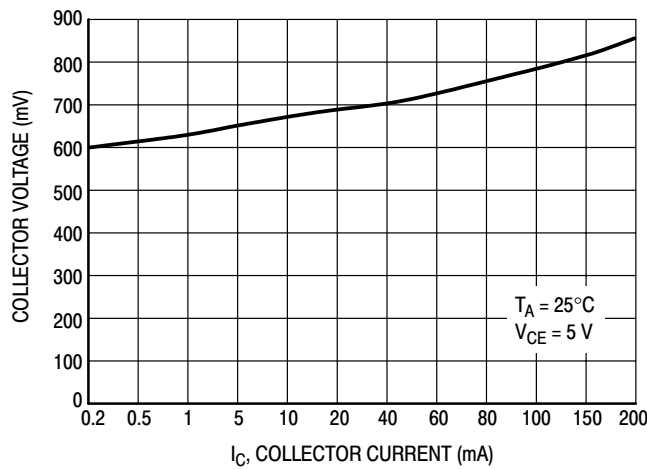


Figure 5. On Voltage

MSB1218A-RT1

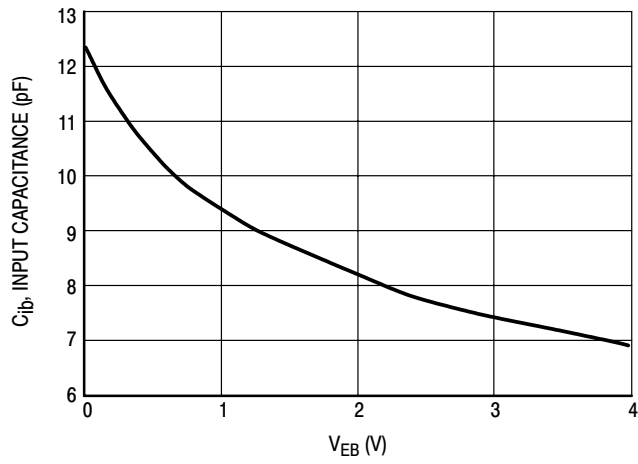


Figure 6. Capacitance

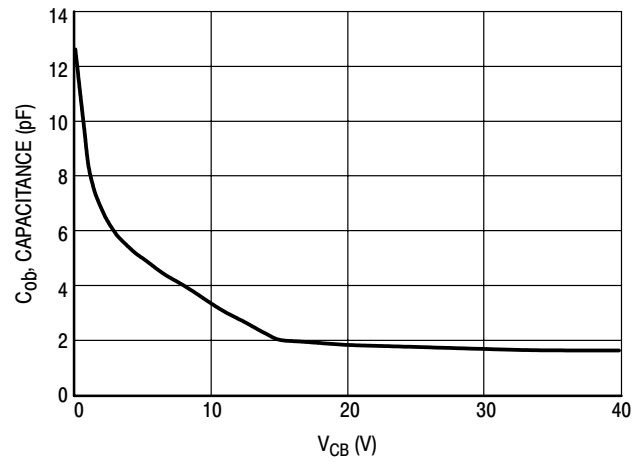
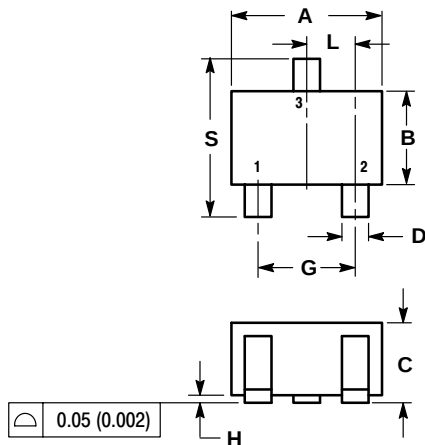


Figure 7. Capacitance

MSB1218A-RT1

PACKAGE DIMENSIONS

SC-70 (SOT-323)
CASE 419-04
ISSUE L



NOTES:

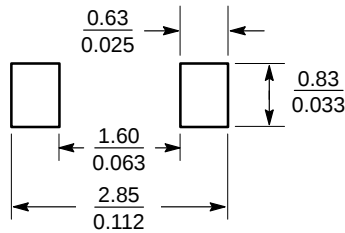
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.071	0.087	1.80	2.20
B	0.045	0.053	1.15	1.35
C	0.032	0.040	0.80	1.00
D	0.012	0.016	0.30	0.40
G	0.047	0.055	1.20	1.40
H	0.000	0.004	0.00	0.10
J	0.004	0.010	0.10	0.25
K	0.017 REF		0.425 REF	
L	0.026 BSC		0.650 BSC	
N	0.028 REF		0.700 REF	
S	0.079	0.095	2.00	2.40

STYLE 4:

- PIN 1: CATHODE
2: CATHODE
3: ANODE

SOLDERING FOOTPRINT



SCALE 10:1 (mm inches)

SOD-323

ON Semiconductor and  are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor
P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center
2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051
Phone: 81-3-5773-3850

ON Semiconductor Website: <http://onsemi.com>

Order Literature: <http://www.onsemi.com/litorder>

For additional information, please contact your
local Sales Representative.