

# MUR1610CT, MUR1615CT, MUR1620CT, MUR1640CT, MUR1660CT

## SWITCHMODE™ Power Rectifiers

This series is designed for use in switching power supplies, inverters and as free wheeling diodes, these state-of-the-art devices have the following features:

### Features

- Ultrafast 35 and 60 Nanosecond Recovery Times
- 175°C Operating Junction Temperature
- Popular TO-220 Package
- Epoxy Meets UL 94 V-0 @ 0.125 in
- High Temperature Glass Passivated Junction
- High Voltage Capability to 600 Volts
- Low Leakage Specified @ 150°C Case Temperature
- Current Derating @ Both Case and Ambient Temperatures
- Pb-Free Packages are Available\*

### Mechanical Characteristics

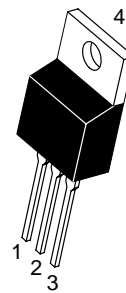
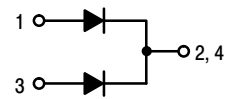
- Case: Epoxy, Molded
- Weight: 1.9 grams (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes:  
260°C Max. for 10 Seconds
- Shipped 50 units per plastic tube



ON Semiconductor®

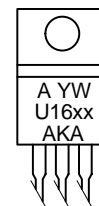
<http://onsemi.com>

## ULTRAFAST RECTIFIERS 8.0 AMPERES 100-600 VOLTS



TO-220AB  
CASE 221A  
PLASTIC

### MARKING DIAGRAM



A = Assembly Location  
Y = Year  
W = Work Week  
U16 = Device Code  
xx = 10, 15, 20, 40, or 60  
AKA = Location Code

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# MUR1610CT, MUR1615CT, MUR1620CT, MUR1640CT, MUR1660CT

## MAXIMUM RATINGS

Rating	Symbol	MUR16					Unit
		10CT	15CT	20CT	40CT	60CT	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	$V_{RRM}$ $V_{RWM}$ $V_R$	100	150	200	400	600	V
Average Rectified Forward Current Total Device, (Rated $V_R$ ), $T_C = 150^\circ\text{C}$	Per Leg Total Device $I_{F(AV)}$	8.0 16					A
Peak Rectified Forward Current (Rated $V_R$ , Square Wave, 20 kHz), $T_C = 150^\circ\text{C}$	Per Diode Leg $I_{FM}$	16					A
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)	$I_{FSM}$	100					A
Operating Junction Temperature and Storage Temperature	$T_J, T_{stg}$	- 65 to +175					$^\circ\text{C}$

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

## THERMAL CHARACTERISTICS (Per Diode Leg)

Maximum Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	3.0	2.0	$^\circ\text{C/W}$
----------------------------------------------	-----------------	-----	-----	--------------------

## ELECTRICAL CHARACTERISTICS (Per Diode Leg)

Maximum Instantaneous Forward Voltage (Note 1) ( $I_F = 8.0$ Amps, $T_C = 150^\circ\text{C}$ ) ( $I_F = 8.0$ Amps, $T_C = 25^\circ\text{C}$ )	$V_F$	0.895 0.975	1.00 1.30	1.20 1.50	V
Maximum Instantaneous Reverse Current (Note 1) (Rated dc Voltage, $T_C = 150^\circ\text{C}$ ) (Rated dc Voltage, $T_C = 25^\circ\text{C}$ )	$i_R$	250 5.0	500 10		$\mu\text{A}$
Maximum Reverse Recovery Time ( $I_F = 1.0$ Amp, $di/dt = 50$ Amps/ $\mu\text{s}$ ) ( $I_F = 0.5$ Amp, $I_R = 1.0$ Amp, $I_{REC} = 0.25$ Amp)	$t_{rr}$	35 25	60 50		ns

1. Pulse Test: Pulse Width = 300  $\mu\text{s}$ , Duty Cycle  $\leq 2.0\%$

## ORDERING INFORMATION

Device	Package	Shipping†
MUR1610CT	TO-220	50 Units / Rail
MUR1615CT	TO-220	50 Units / Rail
MUR1620CT	TO-220	50 Units / Rail
MUR1620CTG	TO-220 (Pb-Free)	50 Units / Rail
MUR1640CT	TO-220	50 Units / Rail
MUR1640CTG	TO-220 (Pb-Free)	50 Units / Rail
MUR1660CT	TO-220	50 Units / Rail
MUR1660CTG	TO-220 (Pb-Free)	50 Units / Rail

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MUR1610CT, MUR1615CT, MUR1620CT, MUR1640CT, MUR1660CT

MUR1610CT, MUR1615CT, MUR1620CT

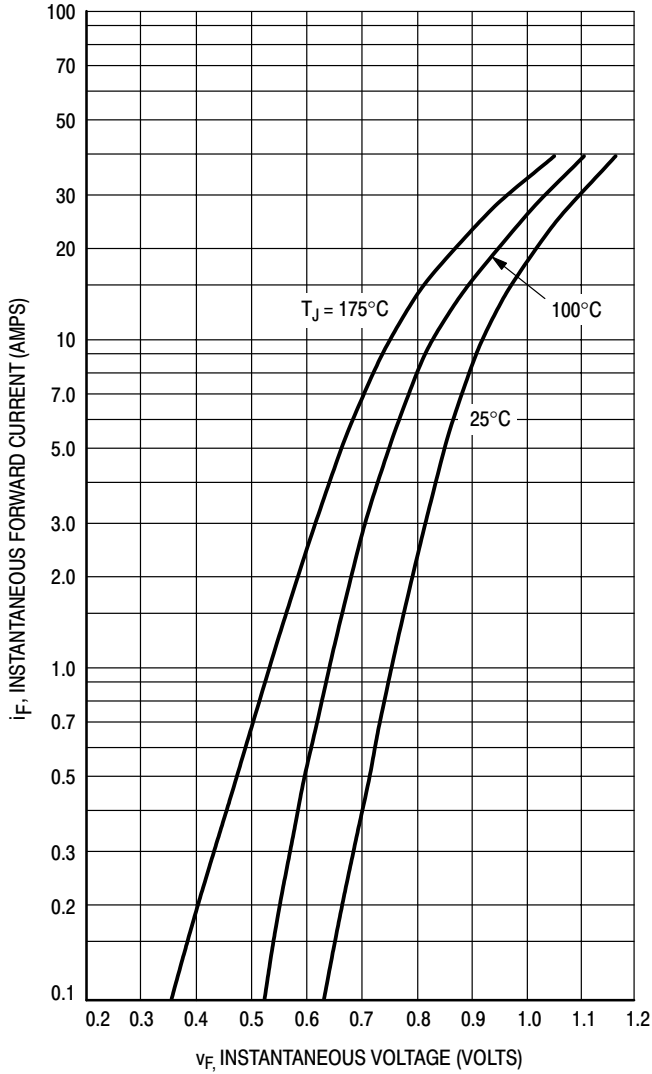


Figure 1. Typical Forward Voltage, Per Leg

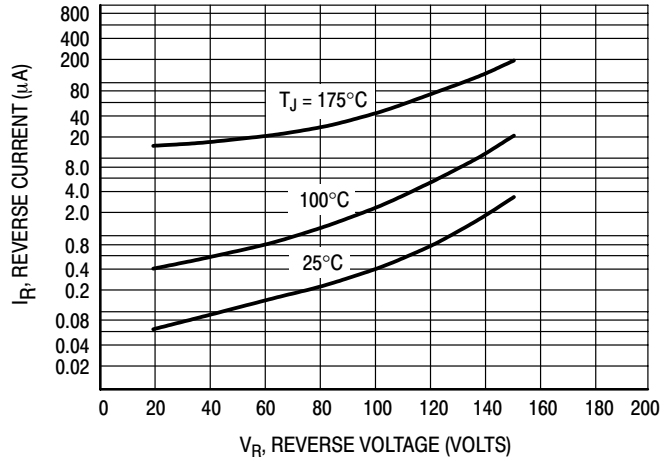


Figure 2. Typical Reverse Current, Per Leg\*

\* The curves shown are typical for the highest voltage device in the voltage grouping. Typical reverse current for lower voltage selections can be estimated from these same curves if  $V_R$  is sufficiently below rated  $V_R$ .

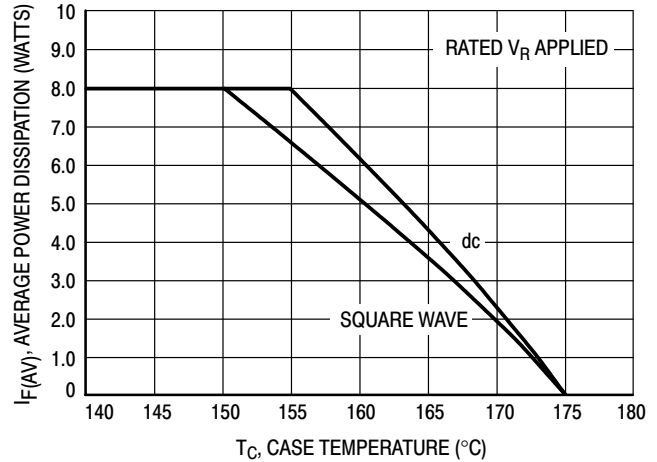


Figure 3. Current Derating, Case, Per Leg

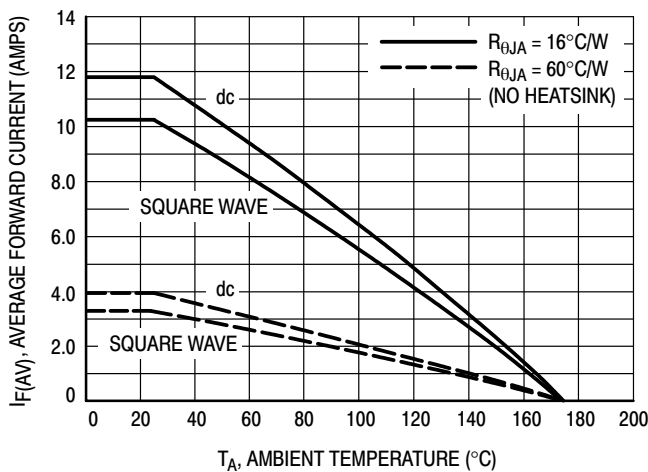


Figure 4. Current Derating, Ambient, Per Leg

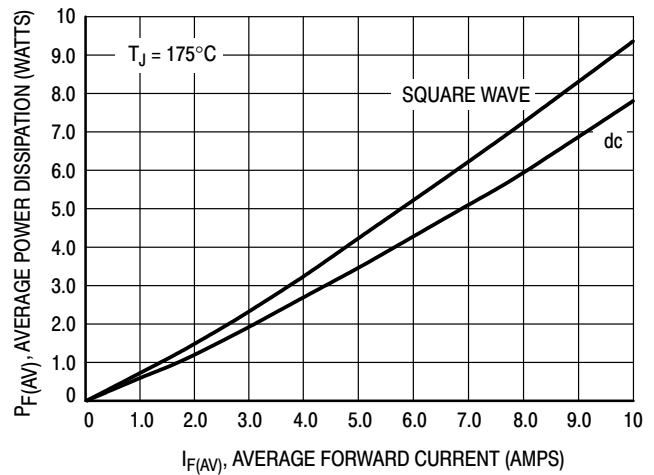


Figure 5. Power Dissipation, Per Leg

MUR1640CT

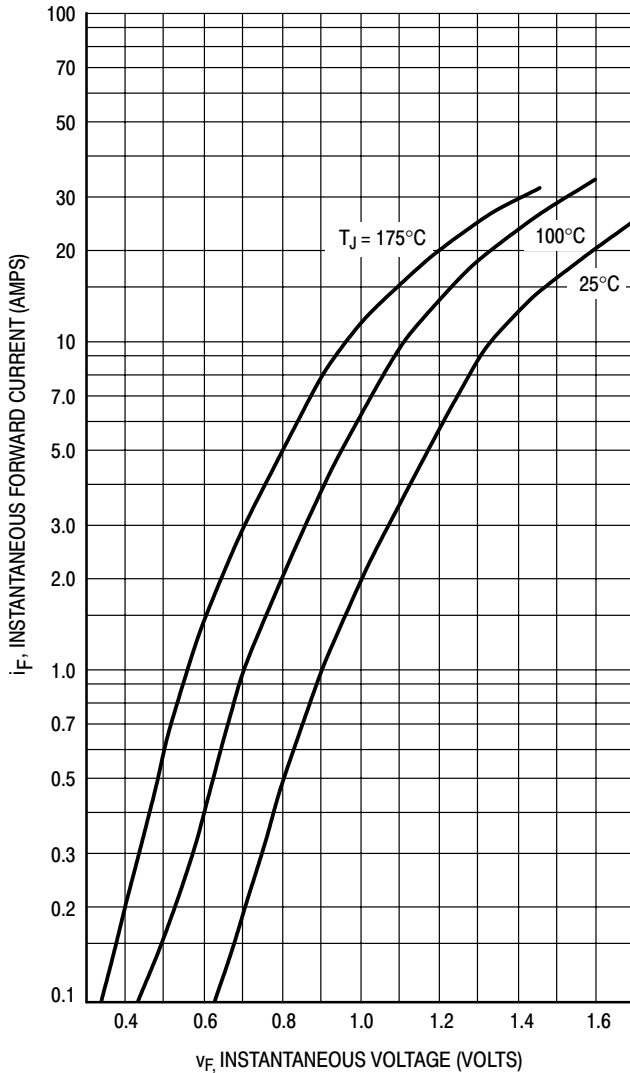


Figure 6. Typical Forward Voltage, Per Leg

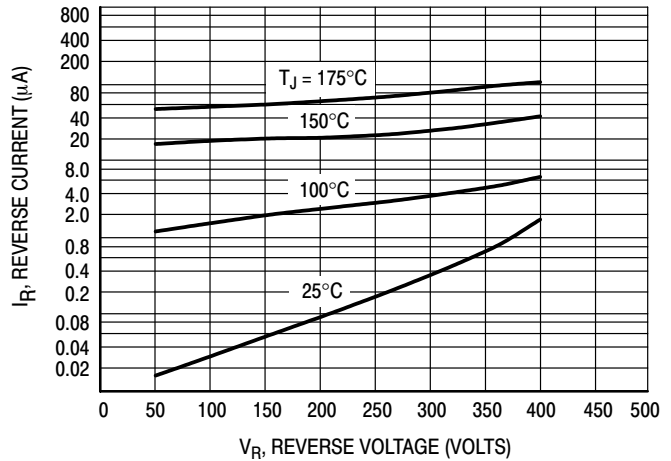


Figure 7. Typical Reverse Current, Per Leg\*

\* The curves shown are typical for the highest voltage device in the voltage grouping. Typical reverse current for lower voltage selections can be estimated from these curves if  $V_R$  is sufficiently below rated  $V_R$ .

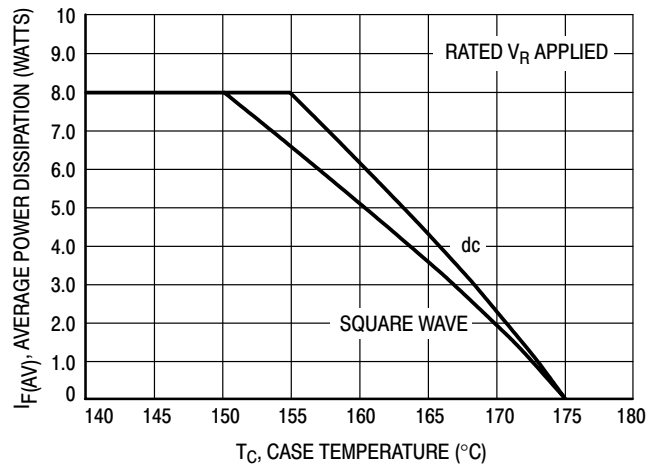


Figure 8. Current Derating, Case, Per Leg

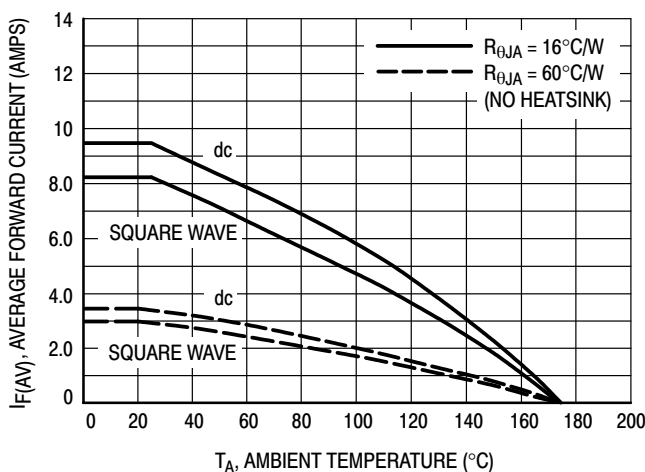


Figure 9. Current Derating, Ambient, Per Leg

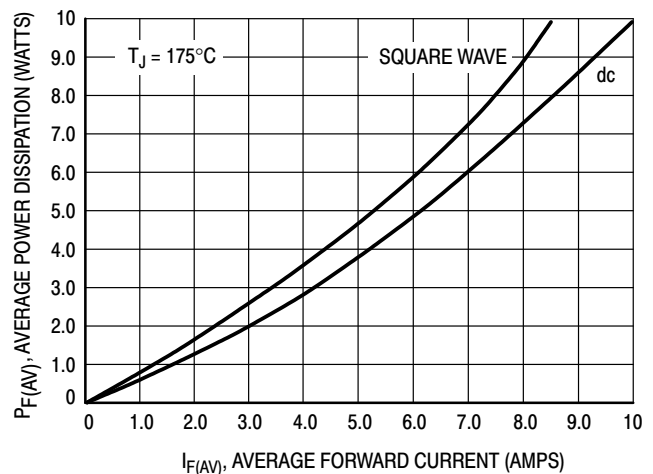


Figure 10. Power Dissipation, Per Leg

MUR1660CT

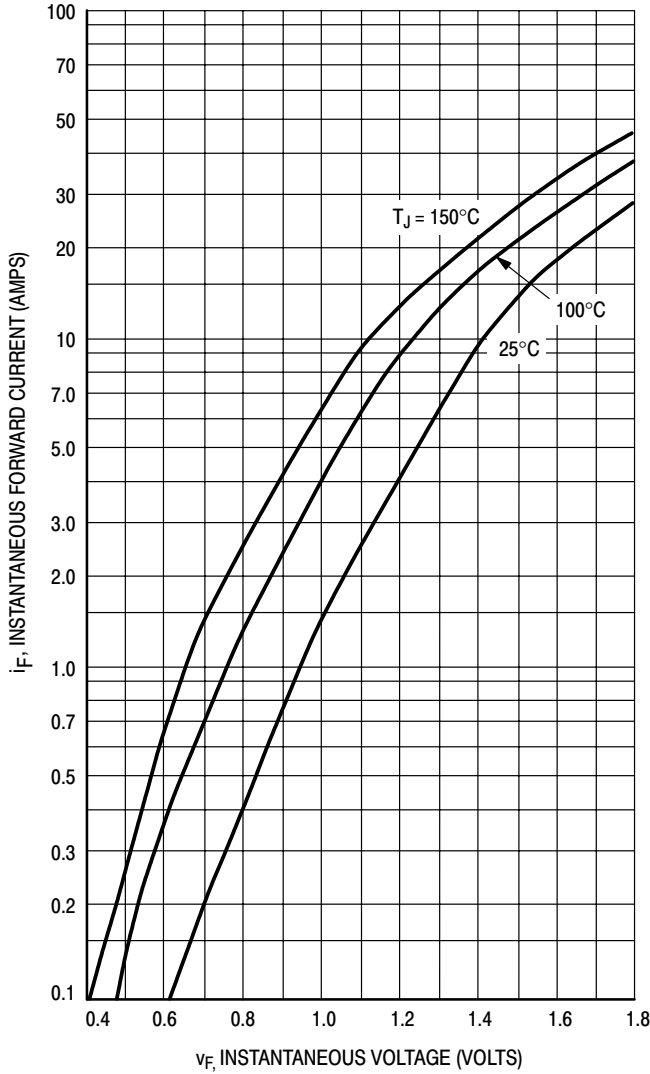


Figure 11. Typical Forward Voltage, Per Leg

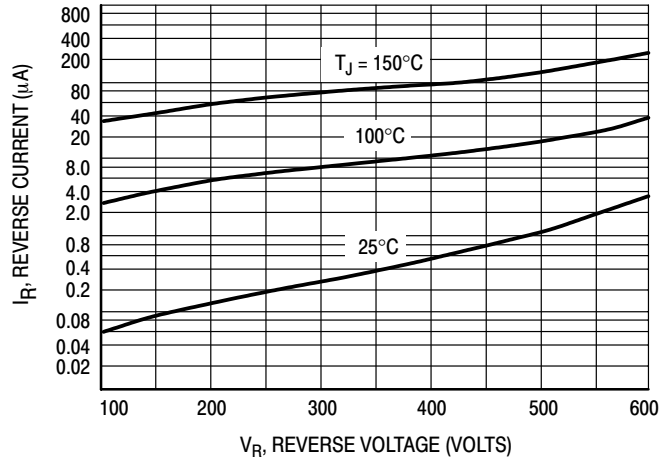


Figure 12. Typical Reverse Current, Per Leg\*

\* The curves shown are typical for the highest voltage device in the voltage grouping. Typical reverse current for lower voltage selections can be estimated from these same curves if  $V_R$  is sufficiently below rated  $V_R$ .

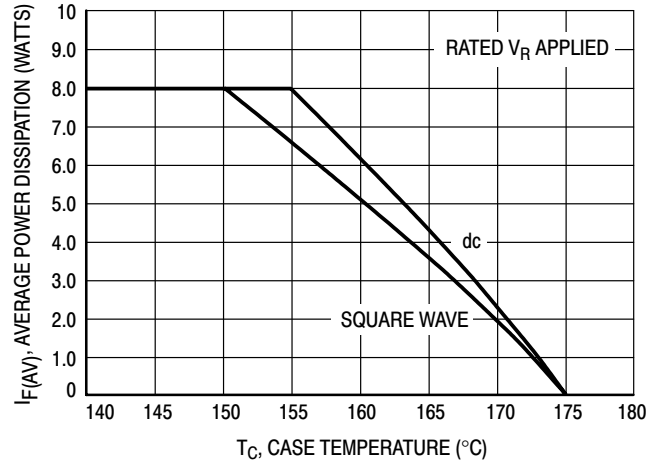


Figure 13. Current Derating, Case, Per Leg

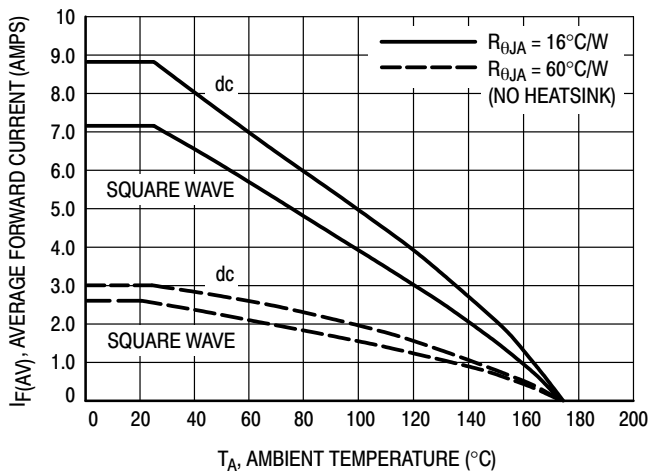


Figure 14. Current Derating, Ambient, Per Leg

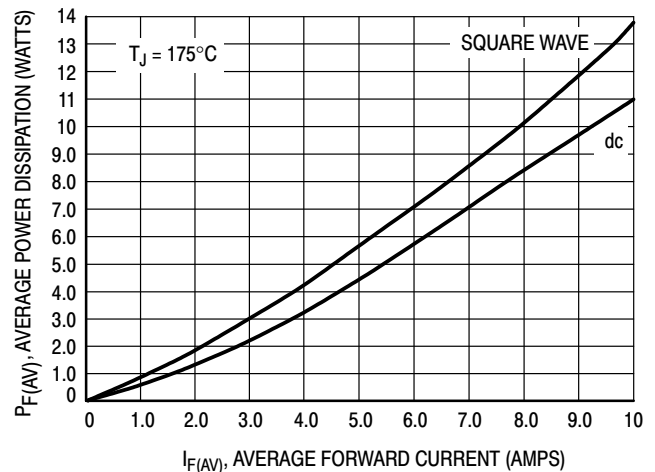


Figure 15. Power Dissipation, Per Leg

MUR1610CT, MUR1615CT, MUR1620CT, MUR1640CT, MUR1660CT

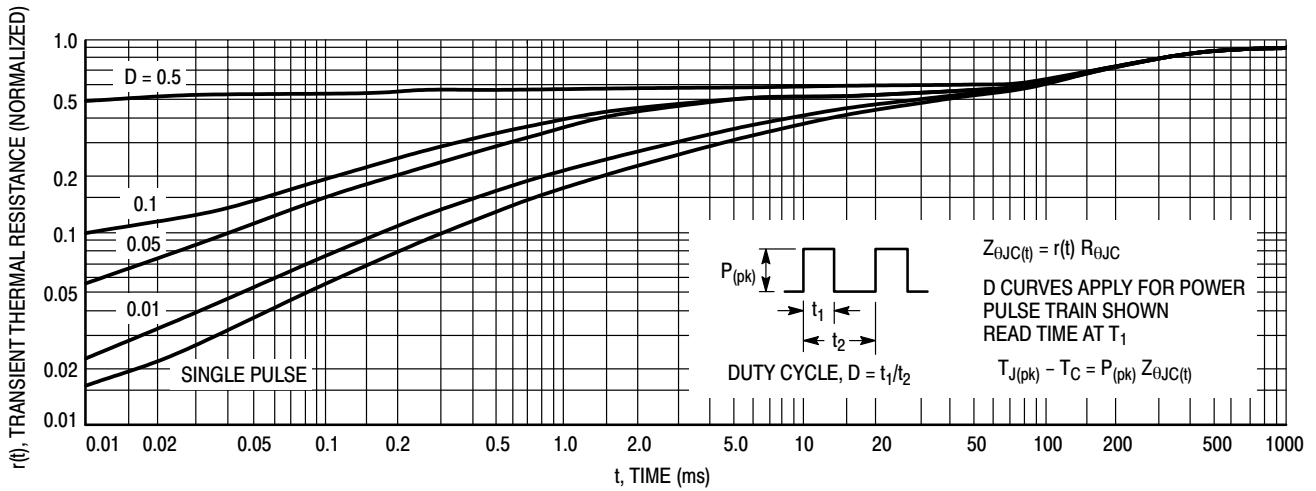


Figure 16. Thermal Response

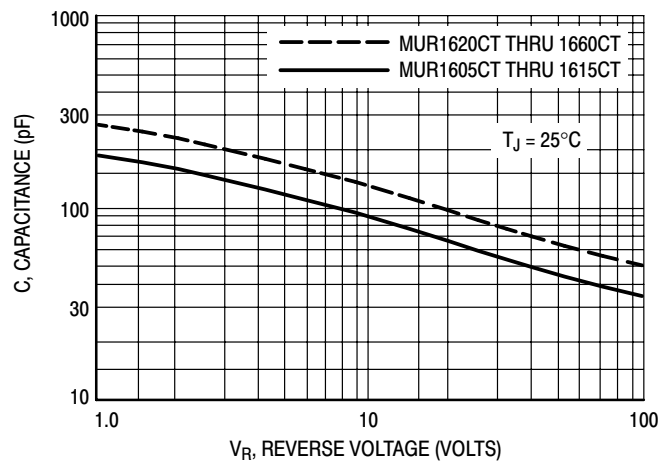
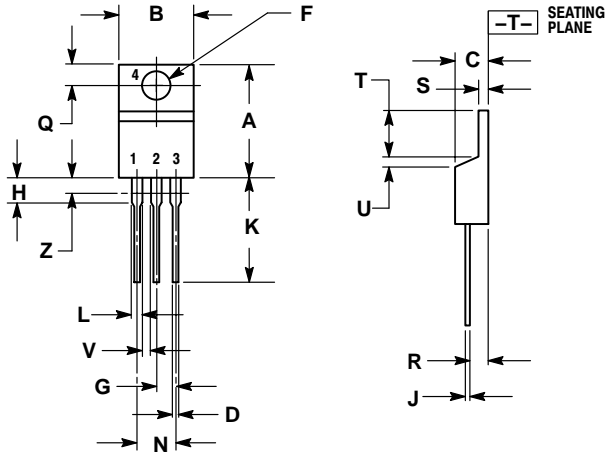


Figure 17. Typical Capacitance, Per Leg

MUR1610CT, MUR1615CT, MUR1620CT, MUR1640CT, MUR1660CT

PACKAGE DIMENSIONS

TO-220 THREE-LEAD  
TO-220AB  
CASE 221A-09  
ISSUE AA




- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.570	0.620	14.48	15.75
B	0.380	0.405	9.66	10.28
C	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
H	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045	---	1.15	---
Z	---	0.080	---	2.04

# MUR1610CT, MUR1615CT, MUR1620CT, MUR1640CT, MUR1660CT

SWITCHMODE is a trademark of Semiconductor Components Industries, LLC (SCILLC).

**ON Semiconductor** and  are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## PUBLICATION ORDERING INFORMATION

### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor  
P.O. Box 61312, Phoenix, Arizona 85082-1312 USA  
**Phone:** 480-829-7710 or 800-344-3860 Toll Free USA/Canada  
**Fax:** 480-829-7709 or 800-344-3867 Toll Free USA/Canada  
**Email:** [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

**N. American Technical Support:** 800-282-9855 Toll Free  
USA/Canada

**Japan:** ON Semiconductor, Japan Customer Focus Center  
2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051  
**Phone:** 81-3-5773-3850

**ON Semiconductor Website:** <http://onsemi.com>

**Order Literature:** <http://www.onsemi.com/litorder>

For additional information, please contact your  
local Sales Representative.

**MUR1620CT/D**